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## DECEMBER'85 tilectronies

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 "plugs into the wall." Find out about software that lets you use your PC to design custom gate arrays. Find out about nonvolatile shadow RAM and bubble memory. Find out about a complete telephone on a chip, and much much more.Our thanks to Motorola for providing the photo of the silicon and end-products that you see on the cover. To find out just what that silicon can do, turn to page 45.

# NExt Month 

## THE JANUARY ISSUE IS ON SALE DECEMBER 3

## BUILD YOUR OWN SATELLITE RECEIVER

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> SEASOX'S GREETINGS
> The editors and staff of Radio-Electronics join in sending Foliday greetings and our best wisfies for a fappy new year

[^0]NEW!
Uniden
Bearcat
Regency

Scanners
Communications Electronics,", the world's largest distributor of radio scanners, introduces new scanners and scanner accessories from J.I.L., Regency and also Uniden/Bearcat. Chances are the police, fire and weather emergencies you'll read about in tomorrow's paper are coming through on a scanner today.

## NEW! Regency? MX7000-K List price S699.95/CE Price S429.00/SPECIAL

 10-Band, 20 Channel © Crystalless • AC/DC Frequency range: $25-550 \mathrm{MHz}$. continuous coverage and 800 MHz . to 1.2 GHz continuous coverage In addition to normal scanner listening, the MX7000 offers CB, VHF, and UHF TV audio, FM Broadcast, all aircraft bands (civil and military), 800 MHz communications, cellular telephone, and when connected to a printer or CRT, satellite weather pictures.
## NEW! Regency ${ }^{\ominus}$ MX5000-K

 List price S599.95/CE price S329.00/SPEECIAL Mult-Band, 20 Channel - No-crystal scanner Search • Lockout • Priority • AC/DC Selectable AM-FM modes - LCD display World's first continuous coverage scanner Frequency range: 25-550 MHz continuous coverage. Never before have so many features come in suich a small package. The Regency MX5000 mobile or home scanner has continuous coverage from 25 to 550 MHz . That means you can hear CB, Television audio, FM broadcast stations, all aircraft bands including military and the normal scanner bands, all on your choice of 20 programmable channels.
## NEW! Regency ${ }^{\text {© }}$ MX4000-K List price $\$ 629.95 /$ CE price $\$ 299.00 /$ SPECIAL

 Multr-Band, 20 Channel © No-crystal scanner Search • Lockout • Priority © AC/DC Selectable AM-FM modes © LCD display Bands: $30-50,118-136,144-174,440-512,800-950 \mathrm{MHz}$ The Regency MX4000 is gives coverage in the standard VHF and UHF ranges with the important addition of the 800 MHz , and aircraft bands It features keyboard entry, multifunction liquid crystal display and variable search increments.
## NEW! Regency ${ }^{\circledR}$ Z60-K <br> List price $\$ 379.95 /$ CE price $\$ 216.00 /$ SPECIAL

 8-Band, 60 Channel - No-crystal scanner Bands: $30-50,88-108,118-136,144-174,440-512 \mathrm{MHz}$ Cover your choicé of over 15,000 frequencies on 60 channels at the touch of your finger.

## NEW! JIL SX-400-K <br> \section*{List price $\$ 799.95 /$ CE price $\$ 469.00 /$ SPECIAL}

 Mult'-Band, 20 Channel © No-crystal Scanner Search e Lockout • Priority © AC/DC Frequency range: $26-520 \mathrm{MHz}$ continuous coverage.With optionally equipped RF converiers $150 \mathrm{KHz}-3.7 \mathrm{GHz}$ With optionally equipped RF converters $150 \mathrm{KHz}-3.7 \mathrm{GHz}$.
The JIL SX-400 synthesized scanner is designed for The JIL SX-400 synthesized scanner is designed for mand features not found in ordinary scanners. The SX 400 will cover from 150 KHz to 3.7 GHz . with RF converters. Order the following RF converters for you SX-400 scanner. RF-1030-K at $\$ 259.00$ each for frequency range $150 \mathrm{KHz} .-30 \mathrm{MHz}$. USB, LSB, CW and AM. (CW filter required for CW signal reception); RF-5080-K at \$199.00 each for $500-800 \mathrm{MHz}$; RF-8014-K at $\$ 199.00$ each for $800 \mathrm{MHz} \cdot-1.4 \mathrm{GHz}$. Be sure to also order ACB-300-K at $\$ 99.00$ each which is an antenna control box for connection of the RF converters. The RC-4000-K data interface at $\$ 259.00$ each gives you control of the SX-400 scanner and RF converters through a computer. Add $\$ 3.00$ shipping for each RF converter, data interface or antenna control box. If you need further information on the JIL scanners, contact JIL directly at 213-926-6727 or write JIL at 17120 Edwards Road, Cerritos, California 90701 U.S.A

## SPECIAL! JIL SX-200-K

## List price \$499.95/CE special price \$159.00

 Frequency range $26-88,108-180,380-514 \mathrm{MHz}$ The JIL SX-200 scanner tunes military, F.B.I., Space Satellites, Police and Fire, Drug Enforcement Agencies, Defense Department, Aeronautical AM band, Aero Navigation Band, Fish \& Game, Immigration, Paramedics, Amateur Radio, Justice Department, State Department, Amateur Radio, Justice Department, State Department,plus other thousands of radio frequencies most other scanners can't pick up. The SX-200 has selectable AM/FM receiver circuits, tri-switch squelch settings AM/FM receiver circuits, tri-switch squelch settings signal, audio and signal \& audio, outboard $A C$ powe
supply - DC at 12 volts built-in, quartz clock - bright supply - DC at 12 volts built-in, quartz clock - bright
vacuum fluorescent blue readouts and dimmer, dual vacuum fluorescent blue readouts and dimmer, dual
level search speeds, tri-level scan delay switches, 16 level search speeds, tri-level scan delay switches,
memory channels in two channels banks, receive fine memory channels in two channels banks, receive
tune (RIT) $\pm 2 \mathrm{KHz}$., dual level RF gain settings -20 db pad, AGC test points for optional signal strength meters

## Regency ${ }^{\text {® }}$ HX1000-K

## 6-Band, 30 Channel - No Crystal scanner

 Search • Lockout • Priority - Scan delay Sidelit liquid crystal display • Digital Clock Sidelit liquid crystal display © Digital Clock Frequency range: $30-50,144-174,440-512 \mathrm{MHz}$.The new handheld Regency HX 1000 scanner is fully keyboard programmable for the ultimate in versatility. You can scan up to 30 channels at the same time. When you activate the priority control, you automat ically override all other calls to listen to your favorite frequency. The LCD display is even sidelit for night use. Order MA-256-K rapid charge drop-in battery charger for $\$ 79.00$ plus $\$ 3.00$ shipping/handling. Includes wall charger, carrying case, belt clip.

## flexible antenna and nicad battery. Order now

## NEW! Bearcat ${ }^{8}$ 100XL-K <br> List price $\$ 349.95 /$ CE price $\$ 229.00$

9-Band, 16 Channel e Priority o Scan Delay Search - Limit • Hold - Lockout • AC/DC Frequency range: $30-50,118-174,406-512 \mathrm{MHz}$. The world's first no-crystal handheld scanner now has a LCD channel display with backlight for low light use and aircraft band coverage at the same low price. Size is $1.3 \%^{\prime \prime} \times 71^{\prime \prime} \times 27 s^{\prime \prime}$. The Bearcat 100 XL has wide frequency coverage that includes all public service bands (Low, High, UHF and "T" bands), the AM aircraft band, the 2meter and 70 cm . amateur bands, plus military and meter and 70 cm . amateur bands, plus military and
federal government frequencies. Wow... what a scanner! federal government frequencies. Wow... what a scanner!
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earphone, battery charger/AC adapter, six AA ni-cad earphone, battery charger/AC adapter, six AA ni-cad
batteries and flexible antenna. Orderyour scannernow.

## NEW! Regency ${ }^{8}$ HX2000-K <br> The World's First 800 MHz. Handheld Scanner

 List price $\$ 569.95 /$ CE price $\$ 359.00$7-Band, 20 Channel - No-crystal scanner Priority control © Search/Scan © AC/DC Sidelit liquid crystal display e Memory backup Sidelit liquid crystal display e Memory backup
Bands: $118-136,144-174,440-512,800-950 \mathrm{MHz}$. Bands: 118-136, 144-174, 440-512, $800-950 \mathrm{MHz}$.
The HX2000 scanner operates on 120 V AC or 6 VDC . The HX2000 scanner operates on 120 V AC or 6 VDC.
Scans 15 channels per second. Size $3^{\prime \prime} \times 7^{\prime \prime} \times 11 / 2$." Includes wall charger, carrying case, belt clip, flexible antenna and nicad batteries. Selectable AM/FM modes.

MX4000

NEW! Bearcat ${ }^{\circ}$ 800XLT-K List price $\$ 499.95 /$ CE price $\$ 329.00$
12-Band, 40 Channel - No-crystal scanner Priority control © Search/Scan © AC/DC Bands: 29-54, 118-174, 406-512, 806-912 MHz The Uniden 800 XLT receives 40 channels in two banks.

OTHER RADIOS AND ACCESSORIES
Panasonic RF-2600-K Shortwave receiver.
Panasonic RF-B300-K Shortwave receiver
Panasonic
RD55-K Uniden Visor mount Radar Detector BC 20/20-K Bearcat 40 channel scanner BC $210 \times W-K$ Bearcat 20 channel scanner BC-WA-K Bearcat Weather Alert ${ }^{\text { }}$
DX1000-K Bearcat shortwave receive
PC22-K Uniden remote mount CB transceive PC55-K Uniden mobile mount CB transceiver Z10-K Regency 10 channel scanner Z30-K Regency 30 channel scanner Z45-K Regency 45 channel scanner R1060-K Regency 10 channel scanner MX3000-K Regency 30 channel scanne C403-K Regency 4 channel scanner R106-K Regency 10 channel scanne RH250B-KRegency 10 channelVHF transceiver RU150B-KRegency 10 channel UHF transceiver RPH410-K 10 ch . handheld no-crystal transciever BC10-K Battery charger for Regency RPH410.
MA256-K Drop-in charger for HX1000 scanner MA256-K Drop-in charger for HX1000 scanne MA257-K Cigarette lighter cord for HX1000 EC10-K Programming toolfor Regency RPH4 10 EC10-K Programming toolfor Regency RPR410 SMRU150-K Service man, for Regency RU150 SMRPH410-K Service man, for Regency RPH410 SMMX7000-K Svc. man. for MX7000 \& M 55000 SMMX3000-K Service man for Regency MX3000 B-4-K 1.2 V AAA Ni-Cad batteries (set of four) A-135C-K Crystal certificate
FB-E-K Frequency Directory for Eastern U.S.A FB-W-K Frequency Directory for Western U.S.A TSG-K "Top Secret" Registry of U.S. Govt. Freq. TIC-K Techniques for Intercepting Comm. RRF-K Railroad frequency directory
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USAMM-K Mag mount VHF/UHF ant. w/ 12 ' cable USAK-K ${ }^{2 / 4}$ "hole mount VHF/UHF ant. w/ 17 ' cable USATLM-K Trunk lip mount VHF/UHF antenna Add $\$ 3.00$ shipping for all accessories ordered at the same time. Add $\$ 12.00$ shipping per shortwave receiver

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# What's News 

## "Expert" computer program teaches how to sail a boat

A prototype computer program called Skipper, that will help teach the basics of sailboat handling, was described by RCA researchers at the past International Conference on Consumer Electronics in Chicago.
The learner interacts with the system through the graphic display and game paddles of his personal computer. In the experimental system demonstrated, a larger computer connected to the user's personal computer contains the tutoring program.


PROTOTYPE COMPUTER SYSTEM teaches the beginner the language of boating. Engineer R.M. Peterson of RCA's Princeton Laborators is at the helm of Skipper.

The user selects from "menus" that cover the various lessons. For example, one menu contains: Parts of a Boat, Steering Practice, The Sail in Detail, and Practice with the Sheet (the line that controls the sail position).

Other menu choices allow control of the boat by the game pad-
dles. A graphic display shows a chart of the course and a representation of the boat, including orientation, rudder angle, sail angle, and wind direction. With the program, the beginner can try to sail the boat by trial and error, secure in the knowledge that a mistake could not be fatal. Or he can give control to the expert, Skipper, and see how the "pro" solves the different problems.

That type of computer program is known as an "expert system." It employs reasoning like that of a human expert to solve problems in a narrow area of expertise. Skipper is intended as a demonstration of possible future "how-to" intelligence systems, which could be made available to students accessing remote computers through telecommunications, or on their own computers as they become more powerful.

## Electronic translator is boon to tourists

A pocket electronic translator that makes 4,000 words of one of three foreign languages available to the tourist or student, or supplies English equivalents to 4,000 foreign words, has been made available by Langenscheidt, the world's largest publisher of bilingual dictionaries. At present three editions are available: French, German and Spanish.

Translator 8000, as it is called, features a 48 K memory and can also double as a pocket calculator. It is made for Langenscheidt by Sharp.

With just a few keystrokes, the translator can locate an English word and then provide the user with its equivalent in either German, French, or Spanish, depending on the model. The unit also
will inform the user of any variant translations or alternate meanings. In addition, the unit can translate from the selected foreign language to English.

To test someone's command of a language, the Translator 8000 can be set up to call up words at random. The user can then check his understanding of the word with a touch of a button.

For further information, contact Langenscheidt Publishers, Inc., 46-35 54th Road, Maspeth, NY 11378.

## Cordless speaker system can be put anywhere

A completely portable cordless high-fidelity speaker system that includes a built-in two-channel FM receiver, an amplifier, a microphone, and a miniature stereo transmitter, has been introduced by Nady Systems of Oakland, CA. The system uses either batteries or an AC adapter for power.

The transmitter is the size of a pack of cigarettes. The stereo receiver and amplifier are housed within the portable speaker enclosure.

Applications for the system are numerous. Used as a public address system or for sound reinforcement, speakers can be spotted around a large hall, or outdoor area, within 100-200 feet from the transmitter. As an adjunct to a home stereo system, a pair of speakers can be placed on the patio or at the poolside. Each speaker has its own volume and tone control.

Suggested retail price for the complete system is $\$ 199.95$. Additional receiver-amplifier-speaker units are $\$ 99.99$. An additional handheld microphone with builtin transmitter is also available. R-E


FEATURES

- Character display of set-up information on CRT(VOLTS/DIV, TIME/DIV, CAL or UNCAL, Delay Time, Trigger source, etc.)
- Cursor read-out function eliminates conventional calculation proceedures ( $\mathrm{V}, \triangle \mathrm{V}, \Delta \mathrm{V} \%, \Delta \mathrm{~T}, 1 / \Delta \mathrm{T}, \Delta \mathrm{T} \%$, phase).
- Digital measuring function displays, DC voltage, AC voltage, and Frequency.
- Comment display function allows user's comments to be displayed on the CRT such as date and measurement data. Excellent for photo documentation.
- 4 channel, 8 trace, DC to 100 MHz , High sensitivity $1 \mathrm{mV} /$ Div, Delayed sweep, Full TV triggering, Alternate triggering.
List Price $\$ 2,490.00 \quad$ Sale Price $\$ 2,250.00$
HITACHI V-209 DC to 20 MHz Mini-Portable Dual-Trace Oscilloscope

- A three-way power supply takes the V-209 anywhere, anytime for speedy field measurements.
- High sensitivity ( $1 \mathrm{mV} /$ div at 10 MHz ) and fast sweep times ( $50 \mathrm{~ns} /-$ div) make the V-209 a valuable tool in R\&D as well as servicing in a wide variety of applications including computers and video equipment.
- A Built-in TV sync separation circuit with easily selectable TV-V and TV-H modes ensures stable observation of both vertical and horizontal TV signal waveforms.
- An auto focus feature maintains constant trace without the necessity of troublesome manual focus adjustments.
- The V-209 is just what it claims to be-a powerful field service tool in a compact, lightweight package.

List Price $\$ 945.00$
Sale Price \$708.00

MODEL V-212
$\$ 461.00$


Model V-422 shown
CIRCLE 126 ON FREE INFORMATION CARD

DC to $20 \mathrm{MHz}, 1 \mathrm{mV} /$ div, Dual Trace. Features 6" Rectangular CRT (w/two X10 probes).

MODEL V-422
$\$ 694.00$
DC to $40 \mathrm{MHz}, 1 \mathrm{mV} /$ div, Dual Trace, D.C. offset for DMM Output, Verticle Mode Trigger $6^{\prime \prime}$ CRT (w/two X10 probes).

MODEL V-1050F
\$1,276.00
DC to $100 \mathrm{MHz}, .5 \mathrm{mV} / \mathrm{div}$, Quad Trace, Delayed Sweep. Full TV Triggering, Alternate time base (w/two X10 probes).

Full 2 year parts and labor warranties on the above scopes. HITACHI GSA CONTRACT NUMBER GSOOS57322

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# Video News 

## DAVID L.ACHENBRUCH

CONTRIBUTING EDITOR


- VHS Progress. The long-term future of the VHS recording format is a major preoccupation among Japanese manufacturers. For the immediate future, they have decided to avoid any improvements that will sacrifice compatibility. But as for the more distant future, there is a sharp dispute as to whether to forsake compatibility, at least partly, in favor of a broadcast-quality picture. One proposal under consideration is the use of metal tape and a highband video signal. That would be completely incompatible with present VHS recorders and tapes, but proponents of that approach also advocate a "compatibility switch," or automatic switching, so that conventional VHS tapes would still be playable on the super VHS machines. According to one VHS recorder manufacturer, that new "super" format could be introduced in about two years.
- 8 mm Gaining. The only threat to VHS on the horizon comes from the new 8 mm video format. Sony, which insists it is not abandoning the Beta format, continues to push 8 mm in a way that indicates that it is being positioned as the next generation of VCR's. Sony's first 8 mm video products were portable camcorders, taking advantage of the 8 mm cassette's small size. Those, however, were followed by a home deck with digital audio and a switch to double the cassette's playing time to four hours. In its latest move, Sony is priming the pump with prerecorded 8 mm cassettes. Sony made its first software move in England, where it sponsored the duplication of some 50 cassettes-divided about half-and-half between major movies and music videos, many of them with digital stereo soundtracks. Eastman Kodak has now joined the movement to 8 mm home decks by preparing to
companies-Matsushita, Philips, and Sony in the hardware field, and Warner Records and Polygram in software. The new system is tentatively called CV (for Compact Video), and it could be available to the public in 1987. The dises, as currently envisioned, would have a combination of digital still and motion pictures and could be used for music videos, games, data, entertainment, education, and instruction, with emphasis on interaction.

CV players are expected to be microprocessorbased, with audio and video outputs and interface devices such as keypad, joystick, or mouse. For audio purposes, CV and CD dises will be compatible. That is, a CD will be playable on a CV turntable, while the audio portion of a CV will play back on a CD player.

- More digital TV's. The expected avalanche of digital television sets hasn't yet materialized because of a continued shortage of IC's; but at press time, three sets using digital signal processing had been introduced for the 1986 model year. Toshiba's 20-inch set with the picture-in-picture feature was the first and has been on the market for several months at a suggested list price of $\$ 1,300$. Panasonic's first digital set, at $\$ 1,200$, also is a 20 -inch unit and has the picture-in-picture feature. (An image from any video source can be superimposed in any corner of the screen.) Sony's digital TV, using its black-faced XBR Trinitron, is a 25 -inch model with an alphanumeric wireless remote-control keyboard and an 80-page memory. Called the "Home Management Helper," it can be programmed to give reminders of birthdays, anniversaries, bills due, and so forth. The suggestd list price is $\$ 1,350.00$.

Zenith, which had originally scheduled a digital set for early 1985, has now postponed the introduction until an unstated future date. "They look great in the lab," said Chairman Jerry Pearlman, "and we are continuing a major development effort. We have been working on it for six years, and when the product is ready we will introduce it. We will introduce digital only when it works to our satisfaction."

R-I

Take home


OFF
$\mathrm{V} \sim$


# Satellite TV 

## Et tu Ku?

VIRTUALLY EVERYTHING YOU HAVE READ (or heard) about home TVRO's involves the 3.7 - to $4.2-\mathrm{GHz}$ satellite band, also known as the C band. That band was established in the early 1960's, by international agreement, for the purpose of relaying communications between two points via satellite. Unfortunately, that same band was also allocated for terrestrial use as a terrestrial trunk system for telephone services including AT\&T, MCI, and others.
As TVRO systems have spread from rural to metropolitan locations, a conflict between TVRO reception and terrestrial microwave users has developed. The FCC, revising its 1979 rules governing licensing of TVRO terminals, foresaw that conflict and went on record as being unwilling to arbitrate any disputes between TVRO and terrestrial microwave users unless the TVRO system had filed for an otherwise optional license. The FCC still routinely grants such licenses, and, although few homeTVRO owners bother with it, cable and SMATV system operators still file for the license.

After the FCC grants such a license, a cable (or other) TVRO system can protest telephone-company applications to add new terrestrial microwave systems when any such system might cause interference to the licensed TVRO. Of the more than 1.2 million home TVRO's now in operation, few have filed for such a license, and the TVRO industry has found counter-measures for dealing with TI (Terrestrial Interference). Those measures include special anten*publisher of CSD magazine


FIG. 1
nas, special signal traps, and special filters that may be added on to an otherwise conventional homeTVRO system.

However, the FCC has always maintained that, although C-band TVRO's are legal, TVRO should really be relegated to the Ku band. Ku band is the next-higher interna-tionally-allocated satellite-to-earth frequency, and in various parts of the world it includes frequencies from 11.7 to 12.2 GHz , from 12.2 to 12.7 GHz , and, in some European countries, from 11.0 to 11.7 GHz . Ku band's chief advantage is that Ku-band assignments are not shared with terrestrial users (such as telephone companies), so the number of earth locations where terrestrial interference is likely to occur is far smaller than with C band.

## Television and Ku band

Some years ago, when the four major U.S. television networks were studying their own use of sat-
ellites, PBS was the first to choose satellite transmission, and PBS programming is still found on Westar 4. That system now also includes the National Public Radio (NPR) service (in a slightly different format). NBC then chose to use Ku band for several reasons:

- From the beginning NBC saw satellite delivery as a two-way system. They believed (correctly, as it turned out) that, while getting programs to their 200 or so network affiliates was important, getting news and sports feeds from those affiliates was equally, if not more, important.
- It was also easier to send a mobile news van equipped with a Ku-band dish than a ten-or twelvefoot C-band dish to a news scene. Early this year NBC completed equipping its affiliates with Kuband receivers, and 50 or so affiliates are now receiving Ku-band transmitters as well.
- NBC also foresaw the great number of problems with terrestrial interference in the C band, and elected to bypass those problems by going to what was, at the time, the untested Ku band.
$A B C$ and $C B S$ subsequently chose to build their networks around existing C-band satellites, and both networks now routinely connect with affiliates using AT\&T's Telestar 301 and 302 satellites. That gives home TVRO viewers a multitude of unscrambled network feeds to select from.

From the home-TVRO owner's point of view, even with NBC now using Ku band to reach its affiliates, there are three network Cband services still available, plus PBS. That is because NBC operates

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a C-band backup feed for its East Coast network feed, which covers the eastern and central time zones (on F1R). Although the bulk of NBC programming is carried on Ku band, for the time being the full eastern network schedule continues on F1R. However, this fall will be the first time in years that much of NBC's professional football coverage will not be found on C band. And that has some homeTVRO users and sellers concerned.

On a typical Sunday, a high percentage of all professional football games played are broadcast via satellite. That has been one of the fringe benefits of owning a TVRO since its inception. The solution, as you may have guessed, is to equip yourself with a Ku-band terminal.

Until very recently you could not receive both C - and Ku-band signals with the same antenna. Kuband antennas and electronics present no real technical prob-

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## TVRO dealer "Starter Kit" available

Bob Cooper's CSD Magazine has arranged with a number of TVRO equipment suppliers to provide a singlepackage of material that will heip introduce you to the world of TVRO dealership. A short booklet written by Bob Cooper describes the start-up pitfalls to be avoided by any would-be TVRO dealer, in addition, product data and pricing sheets from prominent suppliers in the field are included. That package of material is free of charge and is supplied to firms or individuals in the electronics service business as an introduction to the 1984/85 world of selling TVRO systems retail.

You may obtain your TVRO Dealer Starter Kit free of charge by writing on company letterhead, or by enclosing a business card with your request. Address your inquiries to: TVRO STARTER KIT, P.O. Box 100858, Fort Lauderdale, FL 33310. That kit not available to individuals not involved in some form of electronics sales and service
lems in and of themselves; finding an economical way to receive both bands with the same equipment is the real problem. However, at the recent SPACE/STTI trade show in Nashville, several suppliers displayed combination systems that could receive both C- and Ku-band signals using one dish and (more or less) a single electronics package. Here is how the designers have approached the "dual band" problem:

1. The first problem is the dish. Many of the more popular meshantenna systems have mesh openings significantly larger than the minimum required for suitable Kuband reception. The solution was to close up the mesh by making the holes smaller.
2. The second problem is the feed. Both C- and Ku-band feeds must be at the focal point of the dish, and there is an old law of physics that states that two objects cannot occupy the same space at the same time. Several solutions have been proposed by the equipment manufacturers.

Chaparral and others have combined feeds for both bands into a single feed so that the Ku-band feed sits slightly offset from the dish's focal point. That reduces Kuband gain, but since a Ku-band dish only needs to be one-third the size of a C-band dish to provide the same gain, that
shouldn't be much of a disadvantage.

Southern Star Satellites took a different approach; their C-band antenna is Cassegrain-fed, but they cut a tiny opening in the Cassegrain sub-reflector where they mount a Ku-band feed. Thus, on C band, the dish is a Cassegrain type, and on Ku it is a prime-focus type.
3. The third problem is the downconverter. One early approach was to design primarily for C-band reception, and include a subconverter for Ku band that translated the $11.7-$ to $12.2-\mathrm{GHz}$ signals down to 3.7 to 4.2 MHz for final conversion. That proved to be unworkable, so modern receivers use dual "heads" (the "head" consists of the feed, antenna, LNA, and downconverter); each one downconverts signals from its band to a common IF between 950 and 1450 MHz . The user has a $\mathrm{c} / \mathrm{KU}$ switch at his receiver that he may use to select the desired band; the receiver automatically selects the proper conversion head at the antenna. The receiver deals with only one frequency: the downconverted 950 - to $1450-\mathrm{MHz}$ IF signal.
4. The fourth problem is bandwidth. Most C-band signals operate in a $36-\mathrm{MHz}$ transponder channel, but few actually use more than 27 or 28 MHz for video information. Ku-band channels are typically $54-\mathrm{MHz}$ wide, and the Ku people have adopted a technique whereby they split a $54-\mathrm{MHz}$ channel into a pair of $27-\mathrm{MHz}$ wide channels. They can then transmit two completely separate TV programs through one transponder. Thus, by pure luck, 27 MHz bandwidths are appropriate for both C and Ku bands.

## The future

Ku band is to present-day TVRO what UHF was to terrestrial TV reception in the 1950's. It is the "new kid on the block," and, for now, equipment available for Ku is expensive and hard to find. A handful of C-band OEM's, looking for a marketing nitch they can call their own, have moved into Ku with dual-band systems. Others are certain to follow soon as the Kuband satellite services continue their rapid expansion.


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## Bоокs

SILICONNECTIONS, by Forrest M. Mims III; McGraw-Hill Book Company, 1221 Avenue of the Americas, New York, NY 10020; 240 pages, including 8 -page photo section; $\mathbf{6} \times 9$ inches; \$16.95
This book, subtitled Coming of Age in the Electronic Era, is an autobiography that captures some of the history of electronics from before 1960 to 1985. It covers some of Mims' experiences with MITS (the company that developed the Altair computer); the now-defunct Popular Electronics magazine and other electronics-related press; his fight with Bell Labs over patent infringement; his ideas on and experiences with electronics entrenpeneurships, and much more.
Mims' excellent writing style makes reading Siliconnections a pleasure. And readers of RadioElectronics will find it fun to find out how we "scooped [our] uptown rival with the first digital computer-construction article, Jonathan Titus's article on the Mark-8." That article appeared back in our July 1974 issue. And it's only part of the facinating history of the electronics era.

HOW TO DESIGN CIRCUITS USING SEMICONDUCTORS, by Mannie Horowitz; TAB Books, Inc., Blue Ridge Summit, PA 17214; 341 pages, including index; $51 / 8 \times 81 /{ }^{\prime \prime}$; softcover; \$11.50.

Here is a sourcebook giving practical details of today's semiconductor technology without overwhelming you with theory and superfluous information. It's a handbook designed for realistic bench use by both experimenters and professionals who need to design semiconductor circuits for specific applications.
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## LETTERS

Radio-Electronics 200 Park Ave South New York, NY 10003

DOS version 2.0 or higher. It won't boot with Compaq DOS. However, you will need the Compaq version for its BASIC programming language. (IBM's PC, of course, has parts of its BASIC in ROM.)

The " $Y$ " adapter mentioned in the article is no longer necessary. New units are being shipped with 130-watt supplies that already have 3 drive connectors.

Any readers with comments or questions can write directly to the author, but enclose a self-ad-

## BUILD YOUR OWN PC/XT

Inadvertently, we gave the impression that you should only use

## VERSATILE POWER SUPPLY

The printed-circuit board for the versatile bench power supply that was featured on our October cover is available from Specialty Electronic Services, Inc., PO Box 3320, San Antonio, TX 78211 for $\$ 8.50$ postpaid. (The price was incorrectly posted in the Parts List.)
the Compaq version of MS-DOS in the HiTech PC/XT. That is not cor-



## Jerrold Cordless Cable T.V. Converter (sschannels)




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## PLEASE PUBLISH THOSE LISTS

I am planning on building the printer buffer described in the August and September issues of Ra-dio-Electronics. I am disappointed that the micro code for the ROM was not published.

It seems to go against the do-ityouself attitude of your magazine. I do not have the facilities to burn a ROM, but I know there are many companies that will burn ROM's for a modest fee. I think that paying $\$ 26$ for one ROM is a bit too expensive. I would like to suggest the following policy to you.

I suggest that you either publish the micro code, or keep some type of price limit on parts available only through the author. Perhaps a percentage of the part's raw price could be added on.

I think you publish a fine magazine overall, and if you develop
some policy regarding programmed parts, future micro-processor-based projects may be even more popular with your readers.
ALAN ARKEVEIN
Binghamton, N.Y.

## MAKE YOUR PROJECTS BUILDABLE

I am consistently impressed with the quality and variety of projects included in your "Build This" articles; the "Printer Buffer/Converter" in the Aug. \& Sept. 1985 issues seemed an especially challenging and interesting project to tackle. However, I was disappointed when the article concluded without listing the software necessary to make the system work.

Although the machine code has been made available through a pre-programmed EPROM, you do your build-it-from-scratch readers a disservice by providing such thorough guidelines to build a project, but then withhold the key information necessary for a reader
to complete the project on his own.
You have done a terrific job making your projects "buildable"; detailed assembly instructions, parts sources, and the new PC Service are outstanding features that I hope you will not change. But I urge you to please reconsider the space limitation mentioned in the article and print for us "build-itall" readers the operating code listing in future issues.
WADE UMPHRIES
Cape Girardeau, MO

## CAN YOU HELP?

Reference Build This: August and September issues of Radio Electronics,"Buffer/Converter For Your Printer."
I contacted the address listed in the article for the purchase of a board-set for the printer BufferConverter and was informed that the items were not for sale as listed in your magazine; they were only available as a set (boards and EPROM). I can appreciate the company's position; however, the


## PICK YOUR WICK.

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CIRCLE 54 ON FREE INFORMATION CARD

# "One sportscaster was on the left, the other on the right, and the crowd was all around me as I watched my first stereo baseball game on TV." 

## I COULDN'T BELIEVE IT

My living room seemed as though it was 300 yards wide. I could hear action far to the left,
and far to the right. I could hardly believe I was watching television in my living room on a 15 -inch black and white TV.
It was Saturday afternoon and the Braves' game was being broadcast in stereo, as it usually is. However, I would never have heard it on my TV and stereo without the TE-800 Stereo Decoder.

## HOOKUP

The TE-800 was plugged into the earphone jack on the TV, and then the left and right outputs of the TE-800 were plugged into the auxiliary inputs on my stereo. The way the TE-800 is designed, it could have just as easily been connected to the mono audio output on my VCR, or alligator-clipped to the speaker terminals of the TV if my TV had no earphone jack. The unit is even designed with an MPX input to be used with the newer stereo-ready TVs.

## FEATURES

Only our $\mathrm{SPC}^{\text {nu }}$ (patent pending) circuitry of the TE-800 allows it to


TE-800
be this versatile, and to decode stereo from so many sources. The autostereo circuitry automatically senses when a stereo signal is tuned by the TV or VCR, and triggers the decoder and the front auto pilot light.
The Stereo-Plex ${ }^{\text {nu }}$ circuitry of the TE-800 is even special. When you are tuned to a TV station or program that is not stereo, the Stereo-Plex ${ }^{\text {Th }}$ circuitry may be cactivated. It makes the program sound just like stereo, spreading it out to fill the room.

## ^ MADE IN AMERICA *

I personally think it proper that the company that started Good TV Listening twelve years ago with the world's first Teledapter in 1972...the first TV Audio Tuner in 1974 ...and the first TV Power Amp in 1983, is first again with the universal stereo TV decoder.
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# EOUIPMENT 

 REPORTS
## JNEL Cablemaster Cable/VCR programmer

Cable users can now use all of the features of their VCR's.

CIRCLE 5 ON FREE INFORMATION CARD


MENTION THE WORD "PROGRAMmability" to a cable subscriber/ VCR owner and the response is likely to be a scowl. The reason is quite simple: VCR's, even the socalled cable-ready models, lose much of their programmability when used with cable TV.

The reason for that is the cable converter. That unit is only capable of outputing a single channel at a time. That may not seem like such a big deal, but consider what happens when you want to do some unattended taping. Let's say you are going away for the weekend and you want to record two movies that are scheduled to be shown during that time. One is on HBO on Saturday night; the other on Showtime on Sunday morning. Setting up the VCR to record the first movie is no problem: Set the cable converter to the channel for HBO, and set up the VCR to record the converter's output channel (usually either channel 3 or 4) at the appropriate time.

But what about the second movie? There's no way to change the channel output by the converter, and thus no easy way to record both channels. Of course, if you are only interested in the basic,
non-scrambled services, you can get around that problem on some cable systems by using a cableready VCR. But to view the premium, scrambled services such as HBO, Showtime, etc., you must still use the cable-converter, limiting you to but a single channelunless you own a Cablemaster from JNEL (792 S. Main Street, Mansfield, MA 02084).

## The Cablemaster

The Cablemaster is designed to be used with an infrared remotecontrolled cable converter (it cannot be used with any other type of cable converter). Perhaps the simplest and most direct way to describe the unit is to call it a timercontrolled infrared remote.

Measuring roughly half the size of a typical cable converter, it is programmed in much the same way as a VCR is. Here, however, what you are programming is the cable channel you wish to record, the time and day you wish recording to start, and the length of the recording time. In response to those instructions, the Cablemaster turns on the converter, selects the appropriate channel, and turns the converter off

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when recording is finished.
The unit features an 8-event, 14 day timer. It is set in much the same way a VCR timer is set, using just five pushbutton controls. Time, channel selected, mode of operation, etc. are displayed on an LED readout. Both the readout and controls are hidden under a flipup panel when not in use.

Obviously, since all communications between the unit and the converter are via infrared signals, there are no connections between
the two units. That does not mean, however, that there are no installation considerations. First of all, ca-ble-TV systems do not all use the same infrared units. In fact, there are many different infrared re-mote-controlled converters used by cable-TV companies. And, of course, each uses its own particular infrared "code" for communications. Thus, each Cablemaster must be "customized" for use with your particular converter. That is done via a plug-in module that


JNEL calls a "personality module." By tackling that problem in that way, the manufacturer has made sure that the unit will not become obsolete if your cable company changes equipment. If that should happen, merely remove the old module and replace it with the appropriate new one. One personality module is included with the unit; additional ones can be ordered from the manufacturer. Currently, personality modules are available for almost all converters in common use.

Another installation consideration is the placement of the unit. Since communication is via an infrared link, that obviously means that the Cablemaster must be placed so that the cable converter can "see" the infrared signals. For situations where that is difficult or undesirable, a "remote sender" is included. Simply an infrared transmitter that is connected to the main unit via a 5 -foot cable, the remote sender allows the Cablemaster to be placed on top of, next to, or even behind the cable converter.

The manual that accompanies the unit is pretty much standard fare for that type of equipment. It tells you how to set up and use the unit, and about the manufacturer's 90-day warranty, but nothing else.

Admittedly, this may not appear to be the most "elegant" way to solve the VCR programmability problem. After all, it requires that two units-the Cablemaster and the VCR-be programmed for coordinated operation. However, the approach is simple, and it works.

The Cablemaster, with remote sender, wall-plug power supply, and one personality module, sells for $\$ 99.95$.

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## Triplett Model 4750 Digital Multimeter

True RMS capability and a multitude of features make this a highly regarded member of the Triplett family of DMM's.

CIRCLE 6 ON FREE INFORMATION CARD

## SIMPLIFY VIDEO.



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A NEW BREED OF HIGH-PRECISION, high-performance, yet moder-ately-priced multimeters has slowly been infiltrating the electronics scene. These meters combine $41 / 2$ digit precision, true RMS measurement capability and microprocessor control. Not too many years ago, a $4 \frac{1}{2}$-digit voltmeter going for a price like that of the 4750 would have been perceived as an uncommon value. But, as in so many other areas of electronics, the microprocessor lowers costs and at the same time provides previously undreamed-of utility.

Triplett's Model 4750 is one such DMM. It measures AC and DC voltage in five ranges (to 750 and 1000 volts, respectively), AC and DC current in two ranges ( 250 mA and 10 A for both AC and DC ), and resistance in six ranges (to 25 megohms). Voltage and resistance ranges may be selected manually, or automatically by the 4750 . The 250 mV range allows a resolution of $10 \mu \mathrm{~V}$ for both AC and DC measurements; 100 mv resolution is obtainable on the highest ranges (750 and 1000 volts).

## Accuracy of the Model 4750

DC voltage accuracy is between $0.04 \%$ and $0.05 \%$ for all ranges. AC voltage accuracy is $\pm 1 \%$ over the audio frequency range $(10 \mathrm{~Hz}$ to 20 KHz ). It is important to understand that the 4750 measures and displays AC voltages as true RMS values. Most DMM's measure the average value and multiply it by 1.11 to obtain an RMS value for display. That leads to severe errors when measuring anything but low-distortion sinewaves. However, the 4750 measures and displays the true RMS values of triangular waves, squarewaves, halfand full-wave rectified sinewaves, and pulses of variable width.

So far so good, but many DMM's offer comparable measurement accuracy. What really distinguishes the 4750 DMM is its ability to measure frequency, temperature and dBm , and its datahold, peak-hold and relative display modes.

## Special features

The 4750 measures frequency in one of four automatically selected ranges from 99.999 Hz to 99.999

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KHz at an accuracy of $\pm 0.05 \%$. In the $10-\mathrm{Hz}$ range, $41 / 2$ digits of precision gives a resolution of 0.001 Hz ; and in the highest range you get a resolution of 1 Hz . In the frequency mode, the range keys select input attenuation, and allow you to measure up to $\pm 1000$ volts peak. Input impedance in the low range exceeds 1000 Megohms, and in the other ranges is about 10 Megohms.

Temperature can be measured using a standard K-type thermocouple probe; the 4750 can display temperature in either Fahrenheit $\left(-58^{\circ}-+2192^{\circ}\right)$ or Celsius $\left(-50^{\circ}-+1200^{\circ}\right)$, both at a resolution of $1^{\circ}$, and both at an accuracy of $0.5 \%$ of the current reading.

Relative voltage levels may be read using the dBm function; 0 dBm corresponds to a level of 0.7746 volts RMS at 600 ohms and 1 mW . As with frequency measurement, the 4750 automatically selects the proper range for you.

The DATA-HOLD and PEAK-HOLD functions really increase the versatility of this DMM. Pressing the DATA-H switch freezes the value currently displayed by the LCD (except when measuring frequency). In the PEAK-HOLD mode, the display is frozen at its current value, and is only updated if that value is exceeded. PEAK-HOLD works in the manual mode only; you may use it to measure AC or DC voltage or current, and temperature.

Similarly, you may measure AC or DC voltage or current, and resistance in the relative mode. When you press the rel switch, the value currently displayed by the LCD will be taken as a reference, and subsequent values will be displayed relative to that value. For example, if you press the REL switch when the display reads 10.000 volts, a subsequent input of 15.000 volts will display as 5.000 volts, and a 5.000 volt input will display as -5.000 volts. You may use the rel switch when making resistance measurements to cancel out the effects of lead resistance.

## Convenience features

The 4750 includes a number of features that make it a pleasure to use. One such feature is the continuity checker that beeps if the

circuit under test has a resistance of less than about 20 ohms. The 4750 also has a diode-check function that feeds a $1-\mathrm{mA}$ current through the device under test; the meter then displays the forward voltage drop of that device. Also included are an auto-polarity indicator and a $31 / 2$ digit display mode.

## The mechanics

At power-up the 4750 is set to measure DC voltage in the autoranging mode. Other measurements are selected by means of a 20 -key membrane keypad. The $1 / 2$ inch high digits make the LCD display quite readable; the display also has several small annunciators that indicate the quantity currently being measured, special modes etc.
The 4750 weighs about $3 / 4$ pound (with standard nine-volt battery in place), and measures about $7 \times 31 / 2 \times 1 \frac{1}{2}$ (all dimensions in inches.) It includes a convenient tilt stand and recessed safety probe-input jacks. The 4750 also includes an input jack for an AC adapter (power drain is about 50 mW ) and another for a special "touch-hold" probe that allows the data-hold function to be activated without having to move your hands from the circuit to the 4750's keypad. A 40-page instruction manual is included; its organization leaves something to be desired, and there is no index. But the necessary material is covered more or less adequately. No schematic is included.

So if you're looking for a highperformance DMM with many convenience features, consider the Triplett 4750. We recommend it. The 4750 lists for $\$ 340$. For more information, contact Triplett Corporation, One Triplett Drive, Bluffton, Ohio 45817.

R-E

Your guide to everything that's new in electronics, computers and technical education. Over 400 items. Discover fascinating kits to build, enjoy and learn with, as well as assembled high tech products for home, business and hobby.
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## New Ideas

## This electronic metronome emphasizes the downbeat!



FIG. 1

ELECTRONIC METRONOMES HAVE LONG been popular with both electronics experimenters and musicians with a practical bent. All metronomes provide a steady stream of pulses, but few accent the first beat of the measure-the downbeat. The metronome presented here does, and it allows about 1 to 200 beats per minute. A rotary switch allows you to select an emphasized beat every other beat, every third beat, etc., all the way to once every nine beats.

As shown in Fig. 1-a, IC1-a and IC1-b form an astable multivibrator
whose period of oscillation is approximately equal to $1 /(2.2 \times$ C1 (R1+R2)). The astable's signal is fed to IC1-c, which buffers the signal for further amplification. The astable's output is also fed to the Clock input of IC2, a 4017B (a 4017A is also suitable) decade counter. That IC's Q0 through Q9 outputs go high one at a time for each successive clock pulse received at pin 14. Switch S1 feeds one of those outputs to the 4017B's RESET input; whenever the selected output goes high, the 4017B restarts its counting cycle. That is what detercontinued on page 111

## NEW IDEAS

This column is devoted to new ideas, circuits, device applications, construction techniques, helpful hints, etc.

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## A NEW COMPANY WAS NEEDED AND STARTED

Fortunately，other people in the diskette industry recognized that making ultra－high quality diskettes required the best and newest manufacturing equipment as well as the best people to operate this equipment．Since most manufacturers seemed satisfied to give you only the everyday quality now available， an assemblage of quality conscious individuals decided to start a new company to give you a new and better diskette． They called this product the Super Disk diskette，and you＇re going to love them．Now you have a product you can swear by， not swear at．
HOW THEY MADE THE BEST DISKETTES EVEN BETTER The management of Super Disk diskettes then hired all the top brains in the diskette industry to make the Super Disk product．Then these top bananas（sometimes called floppy freaks）created a new standard of diskette quality and reliability．To learn the＂manufacturing secrets＂of the top diskette makers，they＇ve also hired the remaining＂magnetic media moguls＂from competitors around the world．Then all these world class，top－dollar engineers，physicists，research scientists and production experts（if they＇ve missed you，send in your resume to Super Disk）were given one directive．．．to pool all their manufacturing know－how and create a new， better diskette．
HOW SUPER DISK DISKETTES ARE MANUFACTURED The Super Disk crew then assembled the newest，totally quality monitored，automated production line in the industry． Since the manufacturing equipment at Super Disk is new，it＇s easy for Super Disk to consistently make better diskettes． You can always be assured of ultra－tight tolerances and superb dependability when you use Super Disk diskettes．If all this manufacturing mumbo－jumbo doesn＇t impress you， we＇re sure that at least one of these other benefits from using Super Disk diskettes will：
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2．COMPLETE LINE OF PRODUCTS－For a diskette to be useful to you and your computer，it must be compatable physically．Super Disk has an entire line of $51 / 4$－inch diskettes for your computer
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resistance of inductive components; the switched DC mode removes the effect of thermal voltages, the largest source of error in low-resistance measurements; and a pulsed mode is provided for thermally sensitive devices like fuses.

The model 510, with standard probes, is priced at $\$ 895.00$. It is also available with model 506 micro-probes, at \$945.00.Cambridge Technology, Inc., 2464 Massachusetts Avenue, Cambridge, MA 02140.

THERMAL WRITING OS-

Three types of input amplifiers are available: high gain, low gain, and direct fixed range with sensitivities from $0.5 \mathrm{mV} / \mathrm{cm}$ to 200 volts/cm. Paper speeds are selectable from $500 \mathrm{~mm} /$ second to $1 \mathrm{~mm} /$ minute., and timing intervals available are 0.1 second, 1 second, 0.1 minute, or 1 minute. Several of
these recorders may be slaved to a master $8 K 20$ recorder using the timing-signal output to increase the number of channels recorded synchronously.


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Also standard on this recorder are: a character printer that can print the date and data record number, elapsed time, and will also accept and print an ASCII type input; automatic electronic overrange protection; event marker; simultaneous pen lift, and provision for roll or Z-fold chart paper. Remote control of chart drive, pen heat on/off, paper-drive synchronizer, timing marker, and event marker is also standard. Additionally, this recorder provides timing, record on/off, and recordstatus outputs. Optional are: a paper take up, a standard 19 -inch rackmount adaptor, and a paper rewinder.

The $8 K 20$ Series is priced from \$3475.00.- Soltec Corporation, 11684 Pendelton Street, Sun Valley, CA 91352.

## PC-BOARD LAYOUT KIT, model

 G5024PC, is based on a $18^{\prime \prime} \times 24^{\prime \prime}$, blond-hardwood light box with a fitted royal blue carrying case. The case features dual zipper closure, and a full-size pocket with zipper continued on page 112feature large LED elapsed-time readout, frequency response to 125 Hz , long-life thermal pens, and 2 to 16 channels of recording. The recorder is available with either 40mm or $80-\mathrm{mm}$ galvanometers in seven different mainframes.

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Want to learn about 1985＇s most interesting developments in digital IC＇s？Read on！ ROBERT GROSSBLATT，CIRCUITS EDITOR

IF WE HAD TO SUM UP 1985＇s OVERALL trend in digital IC development，we would have to say＂faster，less power consump－ tion，and smaller．＂ 1985 also saw a con－ tinuation of the trend to push CMOS to its limits－and beyond．It＇s safe to say that， although we haven＇t really approached those limits yet，the semiconductor indus－ try is working hard to get there as quickly as possible．

The majority of new digital IC＇s are CMOS，and the majority of those that aren＇t CMOS are faster versions of IC＇s already in production．Most of 1985 ＇s new entries were memory IC＇s and program－ mable gate arrays．The most exciting event in 1985，however，has to be the pro－ gress attained by the government－spon－ sored VHSIC（Very High Speed Inte－ grated Circuits）program．

## VHSIC and the feds

The Pentagon began its VHSIC pro－ gram in 1980；it is scheduled to continue until 1990．Eight companies are involved in the program：Honeywell，TRW， Hughes，IBM，Texas Instruments， Motorola，National Semiconductor，and

Westinghouse．From the beginning， VHSIC＇s main goal has been to increase the maximum operating speeds of inte－ grated circuits．That goal was to be ap－ proached in two stages：operating speeds of 25 MHz ，then speeds of 100 MHz ．

The first stage was attained in 1985； VHSIC contractors are now producing and delivering $25-\mathrm{MHz}$ IC‘s．Those high speeds have been attained by reducing IC trace width．The industry standard is about three microns，although some man－ ufacturers have selected parts available with two－micron spacing．TI，AMI，and National are among the companies pro－ ducing custom gate arrays with two－mi－ cron spacing．

By contrast，phase one of the VHSIC program was achieved by developing IC＇s with a spacing of 1.25 microns．The goal of phase two is to attain widths of 0.5 micron．Shrinking the trace width does more than just allow an increase in the clock rate：more components can also be squeezed onto the substrate of the IC． Hence，reducing trace widths gives us IC＇s that can both operate faster and han－ dle more complex tasks．

VHSIC IC＇s presently being produced for the Pentagon are designed for specific applications．The day you＇ll be able to call your local supplier and order standard gates with $25-\mathrm{MHz}$ operating speeds is still some time in the future．But in the past，restricted government technology has tended to migrate－eventually－to the civilian market，so it＇s only a matter of time until VHSIC parts become com－ monly available．

However，that＇s not to say that the VHSIC contractors have ignored the com－ mercial market altogether．Honeywell，for example，has signed a licensing agree－ ment with ETA Systems to market the HC20．000，shown in Fig．I．It is a CMOS array containing 20,000 gates．The HC20，000 uses Honeywell＇s CMOS III technology to attain 1.25 －micron trace spacing．

Of the 20,000 gates on the chip， 18,000 are available for user programming；the remaining 2,000 gates are used by ETA＇s proprietary BEST（Built－in Evaluation and Self Test）system，a diagnostic and test－pattern generator that will run user simulations of the IC at full operating


FIG. 1-HONEYWELL'S HC20,000 GATE ARRAY is a CMOS part that has 20,000 gates, $1.25-\mathrm{mi}$ cron spacing, and built-in self-test capability.
speed- 25 MHz . The BEST system can feed the IC with four pseudo-random test patterns, one per clock cycle, and store the resulting checksum in onboard registers.
The CMOS III technology allows internal gate delays of a staggeringly low 400 picoseconds. As Honeywell points out, that makes the $\mathrm{HC} 20,000$ the fastest highdensity gate array currently on the market. Such high speed doesn't come cheaply, of course; when the first production parts become available early in 1986, the price is expected to be at least $\$ 300$.
The manufacture of VHSIC components involves unique fabrication, production, and environmental problems that are just now beginning to be solved. Once the contractors work those problems out, VHSIC parts will become generally available, and they will undoubtedly have a major impact on the OEM market.

## CMOS memory

The introduction of new IC's is always done with the market place in mind, and the major market for the semiconductor industry has always been, and continues to be, OEM's. New, high-density IC's have always been difficult to sell when they were available only from a single source, even though everyone marvels when a new IC replaces an entire circuit board. However, the decision to redesign for such a new IC is a major one for OEM's. They worry about being able to obtain proper quantities of such new parts when they need them. Hence it's interesting to note that several companies introducing new IC's this year are announcing second sourcing at the same time

National Semiconductor, for example, introduced the NMC27C256 this year; it is a 256 K byte-wide ( 32 K by 8 ) CMOS EPROM with access speeds as low as 200 nanoseconds. They also announced that the part would be available from VLSI Technology as the VT27C256. National's second-sourcing arrangements are not the whole story, however. Development of the 27C256 was a joint effort: National de-
signed the IC, and VLSI Technologies developed the manufacturing process. That significantly reduced the time needed to get the IC into production, reduced the cost of development, and, as a result, lowered the IC's price.

## One-megabit ROM

Mostek introduced the MK3901M, a one-megabit ROM with standard addressand data-bus multiplexing. Internal storage arrangements are unique in that a control pin allows you to organize the IC's memory matrices as either $128 \mathrm{~K} \times 8$ or $64 \mathrm{~K} \times 16$. Obviously, Mostek has both the 8 - and the 16 -bit computer markets in mind.

The MK 3901 M comes in a standard 28 pin package, and has an access time of 120 nanoseconds. Mostek achieved those feats by using $1.5-$ micron spacing and double-level metal fabrication techniques. Typically, initial pricing is high: about $\$ 55.00$ in 1000 -piece lots. But as the MK3901M gains in popularity, that price is certain to fall considerably.

## High-speed RAM

Intel uses a similar fabrication technology in their double-metal CHMOS III process to produce the 51C66 and the $51 \mathrm{C} 67,16 \mathrm{~K} \times 1$ static RAM's with access times of 35 nanoseconds. Both parts are housed in the industry-standard 20-pin package, so those IC's are pin-compatible replacements for standard $16 \mathrm{~K} \times 1$ memories such as the 2167

The two parts differ in that the 51C67 has the automatic power-down feature introduced by Intel with their 2147 H some years ago. That feature is completely transparent to the user of the IC; it can result in a $90 \%$ savings over the already low power requirements of the CMOS family.

Automatic power-down works as follows. One clock cycle after the 51C67 is three-stated by putting a high on its SELECT pin, the IC automatically goes into its power-down, data-retention mode, where it stays until the IC is again enabled. The difference in current drain is impressive. The IC uses 60 mA when active, but only 3 mA during power down.

## CMOS DRAM and controller

Although many CMOS DRAM's were introduced in 1984, 1985 saw a dramatic increase in CMOS storage capacity. The 51C64 and 51C256 from Intel (and similar parts available from other manufacturers) are CMOS IC's that are pin compatible with the familiar NMOS 4116's and 4164's.

With CMOS DRAM becoming available, CMOS memory controllers couldn't be far behind. Intel's 82 C 08 is a pin-compatible CMOS version of their 8208. It can directly control dynamic RAM parts as large as 256 K , and, like many CMOS
parts that appeared this year, it has a power-down mode in which only the refresh circuitry operates. Energy savings are substantial; 60 mA is used in the active mode, but only 1 mA in the power-down mode.

Undoubtedly, the 82 C 08 will most commonly be used with microprocessors. Although it works well with eight-bit CPU's, the fact that it can directly control as much as 1 megabyte of memory shows that it was designed with sixteen-bit microprocessors in mind. And that's not surprising considering that Intel is the prime source for the 8088 microprocessor used in the IBM PC.

## Non-volatile RAM

Over the years manufacturers have tried to design memory components that have the ease-of-use characteristics of static RAM, and that also retain data when power is removed. As CMOS technology has improved, the amount of power needed for data retention has been reduced drastically. Today, a few microamps are often enough to ensure complete data retention. Mostek has introduced what must be the ultimate memory of that type: the MK48Z02. As shown in Fig. 2, it is a $2 \mathrm{~K} \times 8$ CMOS static RAM similar to the familiar 6116.
The IC differs from standard CMOS parts in that it is what Mostek calls a "Zeropower" RAM. That means the IC has a built-in power source that provides all the power needed for data retention. The MK 48 Z 02 constantly monitors $\mathrm{V}_{\mathrm{CC}}$, and if it falls within the range of 4.5 to 4.75 volts, the IC automatically writeprotects itself. If the supply falls below 4.5 volts, the IC completely disables itself: inputs are disabled and outputs are three-stated. If the supply falls below about three volts, one of two lithium cells built in the IC will be connected to the internal storage cells, hence guaranteeing data retention. Special comparator and reference circuitry selects which cell to use, and sets an error flag if either cell falls below two volts.

## Shadow RAM

Another non-volatile RAM design is the shadow RAM, which provides invisible, automatic back-up of memory by an EEPROM. Xicor has been manufacturing shadow RAM's for several years now, and only recently have they been appearing in consumer-oriented products. Both Hayes Microcomputer Products and Videx, Inc. have used these "Novrams" to store configuration data in their computer products. This year Mostek introduced the MK4701, shown in Fig. 3, a $128 \times 8$ shadow RAM in a standard 28 pin package.

Mostek's part differs from the competition in that the user has much more flexibility in specifying how the "store"


FIG. 2-MOSTEK'S MK48Z02 IS A $2 \mathrm{~K} \times 8$ CMOS "ZERO-POWER" RAM featuring two built-in lithium cells that automatically supply power to the RAM array whenever power falls below about three volts.


FIG. 3-MOSTEK'S MK4701 IS A $128 \times 8$ SHADOW RAM with ten-year data-retention capability. Data may be written to the IC more than 10,000 times.
function works. In addition to the soft-ware-controlled store command, the MK4701 can be set up to store data automatically whenever it detects a power failure. The trip point for the Auto Store function can be adjusted to prevent false triggering.

Mostek added those features to the IC, and still made it pin compatible with the 6116. The MK4701 acts as a normal RAM until the user puts it in a special configuration mode. In that mode, the user writes directly to EEPROM cells outside the normal memory address space, and those cells contain data on the IC's trip point, how the chip-select pins $\left(\mathrm{CS}_{1}-\mathrm{CS}_{4}\right)$ function, etc. The access time of that IC (150 nanoseconds) makes it the fastest shadow RAM on the market.

## Bubble memory

If you thought that bubble memory was dead and gone, Intel has a surprise for you. They have introduced the BPK70AZ, a complete $128 \mathrm{~K} \times 8$ bubblememory subsystem. The complete system is composed of seven IC's:

- 7110AZ, One-megabit bubble memory
- 7230, Current pulse generator
- 7242, Dual formatter/sense generator
- 7250, Coil predriver
- 7254, Quad VMOS coil-drive transistors
- 7220, Bubble-memory controller

The maximum data transfer rate is 100 kilobaud; random access time is 48 ms . If you compare bubble memory speeds with some of the new memory devices discussed above (or older devices, for that matter), bubble memory may not appear to be on the forefront of technology. But if you compare bubble access times to those of floppy disks, bubble looks much better.

## Programmable Gate Arrays

1985 will also be remembered as the year that Programmable Gate Arrays (and other customizable IC's) came of age. For some time now, development efforts have concentrated on increasing speed while simultaneously reducing power requirements. Memory devices such as NOVRAM's, ROM's, and PROM's may be used to achieve solutions with microprocessors and software. But other solutions are still possible with old-fashioned logic-gates, flip-flops, and the like.

The two-micron CMOS processes used by many IC manufacturers have enabled them to jam an astounding number of
gates onto a single substrate. By fabricating a custom mask for the array, individual gates can be connected together to solve specialized logic problems. The OEM defines the layout of the mask, and the collection of standard logic elements becomes a "custom" part. The advantage for the OEM is that he gets a customized, economical part that can operate at the high speeds associated with simple logic. Of course the entire process is rather complicated as it involves software emulators to design and test the planned array. But the hardware portion is similar to maskprogrammable ROM.
AMI has been in the custom-gate-array business since 1966, and their current two-micron double-metal CMOS technology allows single packages to contain as many as 10,000 gates. If that doesn't impress you, you're probably thinking about transistors, not gates. Remember that each gate can contain four or more transistors.

## Sceptre II

As you no doubt imagine, designing a custom array can be a lengthy process even with the proper tools. To aid that process, AMI has designed a unique piece of software called Sceptre II, a CAD (Computer Aided Design) program that translates user-entered schematic drawings into files that can be processed at AMI to generate array masks.

Sceptre II has several features worthy of mention. First of all it contains an emulation package that allows the user's design to be tested for problems such as bus contention, electrical integrity, and so on. Moreover, unlike similar software from other manufacturers, Sceptre II runs on an IBM PC! The photograph shown in Fig. 4 shows a sample Sceptre screen. After the OEM finalizes the design, a floppy disk is sent to AMI for processing that ultimately results in the array mask.

## Other custom arrays

Both National and Mostek (among others) have realized the advantages of custom gate arrays, and both began pushing the technology heavily in 1985. Mostek announced their GB series of gate arrays fabricated with two-micron technology. Available parts range from the MKGB1000D, with the equivalent of 1120 NAND gates, to the MKGB10000D, with 9776 gates. Those IC's have gate delays of only 1.5 nanoseconds! In Fig. $5-a$, we show a schematic equivalent of one cell, and in Fig. 5-b, a representation of the actual layout of one such cell on the IC's substrate. The Mostek LDD/TT fabrication process uses two-micron spacing, but actually produces gate lengths of 1.8 microns.

National's gate-array series, the SCX6200, is built with their two-micron technology called "MicroCMOS." Like


FIG. 4-AMI'S SCEPTRE II PROGRAMMABLE gate-array design program runs on an IBM PC.

Mostek, National has a software design package (with a library of standard gates and macro definitions) that greatly simplifies designing a custom gate array.

## Sprague's BiMOS II IC's

Anyone who has ever designed a circuit with CMOS IC's is aware of the outputpower limitations imposed by that logic family. Those limitations are a consequence of CMOS's internal construction, and that is one area in which reduced trace widths have no advantage


FIG. 5-AN INDIVIDUAL CELL IN MOSTEK'S MKGB1000D series is composed of four individual MOS transistors, as shown at $a$. The actual layout of those transistors is shown at $b$.
whatsoever. Sprague has solved the problem by distinguishing between pureCMOS components and CMOS-compatible components. After all, power-handling capability is a problem for an IC's output, not its input.
Sprague's BiMOS II family of highspeed power drivers is fabricated with seven-micron spacing. (Effort is underway to upgrade to 5 -micron BiMOS III). What makes BiMOS IC's unique is that they have CMOS inputs for the data and control lines, as shown in Fig. 6-a, and bipolar outputs, as shown in Fig. 6-b, for power handling. Sprague thereby maintains CMOS compatibility, and provides IC's like the UDN2545B, a universal quad driver that can deliver 80 volts at 2 amps! That is possible for two reasons: the hybrid CMOS/Bipolar IC configuration and Sprague's special packaging.

Sprague's batwing package is actually an old technique, but it's well suited for increasing thermal transfer from the IC's substrate to the outside world. The internal wafer is mounted on a copper frame; that frame is brought outside the IC as two connected pins on both sides of the package. That allows internally generated heat to exit the IC via a path that is highly thermally conductive.

Other features of the BiMOS family include the kind of chip-enable logic you find in standard CMOS parts; BiMOS components also feature thermal-shutdown and over-voltage protection.

One of Sprague's more interesting 1985 introductions is the UDN2541/42 quad NAND gate. Each gate appears to the
circuit driving it as a logic-level input. But each output is a transistor stage composed of non-Darlington saturated outputs controlled by an on-chip variable current source. Each output is protected from inductor transients by surge-surpressing diodes. That allows the IC to drive loads of 1.5 amps at 80 volts.

Sprague's BiMOS II UCN-58XX family contains a variety of latching logic elements with word widths ranging from four to thirty-two bits, in both serial and parallel configurations. All members of the family have high drive capability, and although they're not strictly CMOS parts, the internal configuration is transparent to the user. For example, the UCN-5821A is an 8-bit serial-to-parallel converter with CMOS inputs; the IC's outputs can sink 350 mA at 50 volts.


FIG. 6-SPRAGUE'S BIMOS II SERIES of digital and analog IC's makes use of a MOS input stage, like that shown in a, and a bipolar output stage, like that shown in $b$.

## Conclusions

Tremendous increases in speed have been brought about in 1985, along with complementary decreases in power consumption. As advances continue to be made in digital technology, the result will be an ever-narrowing gap between "digital" and "analog." For example, sampling rates for DAC's are increasing rapidly, so it's only a matter of time before digital-to-analog conversion becomes the first processing step in what used to be exclusively analog applications.

When phase two of the government's VHSIC program really takes off, operating speeds of 100 MHz will become commonplace. And when that happens, the difference between digital and analog may disappear altogether. But for now, analog is still very much with us, so, to find out what's new in that field, turn to the next article and keep on reading. R-E


> 1985 was not a banner year for analog－IC development；but things haven＇t exactly ground to a halt，either．Read about some of the year＇s most exciting innovations here．

> ROBERT GROSSBLATT，CIRCUITS EDITOR

THE EXPLOSIVE GROWTH OF THE PERSONAL－COMPUTER market the past few years has caused semiconductor man－ ufacturers to emphasize development of digital integrated circuits to such an extent that one might think development of analog IC＇s had been discontinued altogether．Well， that＇s not really the case．There is still quite a bit of analog development going on；in this article we＇ll discuss some of 1985＇s more interesting innovations．

## Low－power voltage regulators

National Semiconductor＇s LP2950 is the latest member of their low－power，low－dropout regulator family．It has specifications that are real eye－openers for anyone used to the old LM78XX series．For example，standby current （the amount of current necessary to keep the device func－ tioning in standby mode）is only $75 \mu \mathrm{~A}$ ，and dropout voltage（the potential difference between the input and output terminals）is a mere 40 millivolts！Equivalent fig－ ures for the LM78XX series are 8 mA and 2 volts！Even National＇s earlier LM2931 series can＇t match the specifi－ cations of the new LP series．The LM2931＇s quiescent draw is 0.4 mA ，and dropout voltage is 60 mv ．

There are two different versions of the LP2950．Either can supply as much as 100 mA of current，but one has an output that is fixed at five volts，and the other has an output that can be varied from 1.25 to 29 volts．
The fixed regulator comes in a three－pin TO－220 pack－ age similar to the LM2931．The variable version，however，
comes in an eight－pin，modified TO－220 package．The extra pins provide two new functions：power strobing and error flagging．Internal reference circuitry is used to monitor the output voltage．If it drops，（due to low input voltage，excessive load，etc．），an output goes high，and that signal can be used trigger an alarm．The regulator also has a TTL－compatible input that can be used to turn the regulator off（and on）with logic－level signals．If you have ever designed power－strobing circuitry to work with three－ terminal regulators，you＇ll welcome that function with open arms．

As you know，new IC＇s often have high prices until development costs have been amortized，but the LP2950 is currently selling for about a dollar in quantities of 100 ．

## Switching regulator

National has introduced the LM1578，a switching reg－ ulator on an eight－pin mini－DIP that can provide a positive or negative output voltage from a positive input voltage． The LM1578 operates over a range of two to forty volts， can supply as much as 750 mA ，and sells for $\$ 1.35$ in quantities of 100 or more．

## Power converter

National has added an interesting member to their fam－ ily of hybrid products．The HS9151 power converter， shown in Fig．1，is a single－package five－volt，three－amp power supply．The HS9151 comes in a metal package with


FIG. 1-NATIONAL'S HS9151 hybrid power converter supplies five volts at three amps; it measures just $3.5 \times 1.5 \times 0.4$ inches.
dimensions of $3.5 \times 1.5 \times 0.4$ inches. It contains a complete switching power supply with built-in current-limiting and ther-mal-shutdown circuitry. Using the IC is simple: regulated DC comes out of one end, and-get this-the other end plugs directly into a 117 -volt AC socket!

The $1-\mathrm{MHz}$ system clock is brought out of the HS9151 via two pins so that several HS9151's may be cascaded. One HS9151 is designated the master unit, and its clock is used to drive that of the others. Connections to the internal filter capacitor are also brought out on two pins, since that capacitor has a value of only $100 \mu \mathrm{~F}$. By connecting an external capacitor in parallel with the HS9151's internal capacitor, ripple may be greatly reduced.

Pricing of the HS9151 is a bit steep compared with standard voltage reg-ulators-about $\$ 80.00$ in quantities of 100. What it will eventually cost in low quantities is anyone's guess, but we hope that price will come down soon!

## Pressure transducers

Motorola has introduced the MPX2010 series of pressure transducers. As shown in Fig. 2, they are small, silicon piezoresistive sensors with unusually linear response curves. Preliminary specifications reveal that output voltage increases linearly from zero to 25 millivolts, corresponding to a $0-$ to 1.5 -PSI increase in pressure. The IC uses a small silicon diaphragm as a strain gauge, and a thin-film resistor network provides the varying output voltage. After the wafers are fabricated, a laser is used to trim them into specification for linearity, temperature compensation, and offset calibration.


FIG. 2-MOTOROLA'S MPX2010 pressure transducers feature output voltage that increases linearly from zero to 25 millivolts, corresponding to a zero to 1.5 PSI increase in pressure.

The MPX2010 is available in several different packages, so that either pressure or vacuum measurements may be made. Moreover, a double-ported package is available if a single application requires both types of measurements to be made.

## Telephone on a chip

Most of Mostek's innovations in 1985 were in digital circuits, but they have announced one new communications product. The MK5375 is a CMOS IC that contains, in one package, almost all the electronics needed to build a working telephone. A block diagram of that IC is shown in Fig. 3. The MK5375 can pulse or tone dial, and it can store ten sixteendigit numbers. It also has last-number redial and the ability to store both the "*" and "\#" keys. Data retention at powerdown is guaranteed with a backup supply of 1.3 volts at 200 nanoamps. That gives the backup battery an expected life-span approximately equal to its shelf life.

## Video multiplexer

Although not strictly an analog part, Harris' HI524 makes a big step forward in high-speed CMOS technology. The HI524 is a single-ended four-channel multiplexer that switches reliably at rates exceeding 1 MHz and handles analog inputs exceeding 10 MHz . Harris calls that IC a "video multiplexer," but its 700 -ohm "on" resistance will cause problems for video switching because of the resulting signal loss. A further complication is the fact that 700 ohms is only a nominal value. As with all analog switches, the "on" resistance may vary from batch to batch by as much as twenty percent. That means signal balancing will have to be done whenever parts or boards are switched.

Harris recommends that the HI524 be used with their HA2541 buffer amp so that the output level can be trimmed into specification for unity gain or the required input/output differential. Harris is presently considering design and production of a HI524-type part with an on-board buffer amp, so that the signal level through the IC can easily be trimmed, regardless of a particular IC's "on" resistance. That would make the HI524 much more useful as a video switch.

In spite of its limitations, the HI524 is still an exciting part. Two switches are used in each channel of the IC; that cuts crosstalk down to an impressive -60 dB at 10 MHz . It's that switch arrangement; by the way, that causes the IC's unusually high "on" resistance. The 10 MHz bandwidth is, at the moment, the highest in the industry, and if you're willing to live with an additional 3-dB drop, you can pash the bandwidth to 16 MHz .

## PCM transcoder

Harris has also introduced the world's first PCM (Pulse Code Modulator) transcoder, the HC5560. What distinguishes the HC5560 from other CODEC (CO-der-DECoder) and PCM devices currently available is that this IC can match any of the four common line encoding/decoding schemes currently used in Europe and North America. Two mode-select pins on the IC allow the user to choose among the following encoding schemes: HDB3 (High Density Bipolar Three), AMI (Alternate Mark Inversion), B6ZS (Bipolar Six Zero Substitution), or B8ZS (Bipolar Eight Zero Substitution).

The HC5560 is a CMOS part. It has TTL-compatible inputs and outputs, and it operates from a 5 -volt, $100-\mathrm{mA}$ supply. The HC5560 includes additional features such as simultaneous encoding and decoding, and asynchronous operation with loopback control and transmission-error detection.

## High-speed DAC's

1985 marked Honeywell's first foray into the commercial market. Signal Pro-


FIG. 3-THE HEART OF MOTOROLA'S MPX2010 pressure transducer is a moloithic pressure-seiser chip that can be seen in the package on the left.
cessing Technologies, Honeywell's commercial division, brought out two new high speed DAC's (Digital to Analog Converters) aimed particularly at CAD (Computer Aided Design) applications. The DAC3401 and DAC3402, shown in Fig. 5, are ECL IC's with TTL compatible inputs and outputs; the two IC's can clock data at 200 and 100 megawords per second, respectively. Either IC can accommodate a CRT display with a resolution of 2000 by 2000 pixels, and either can directly drive a standard RS-343A 75-ohm video load. Honeywell has included a variety of onboard video adjustments including sync, blank, reference white, as well as $+10 \%$ luminance.

## High-power components

Sprague, famous for high-voltage and high-current components, has come out with a new series of power op-amps, the ULN37XX series. They are available in single and dual combinations, and they come in two different package styles, depending on the amount of power they can handle. The ULN3751, for example, is a single op-amp in an eight-pin mini-DIP.

High-power semiconductors are traditionally packaged in metal cans, as metal is a good thermal-transfer medium. However, metal packages are expensive. Ideally, we'd like to get high-power capability in a low-cost plastic package. The people at Sprague have moved a step


FIG. 4-MOSTEK'S MK5375 TELEPHONE-ON-A-CHIP requires only a few additional components to make a complete, working telephone.

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FIG. 5-HONEYWELL'S DAC3401 AND DAC3402 are ECL digital-to-analog converters that can clock data at 200 and 100 megawords per second, respectively.
closer to that ideal by mounting the IC wafer on a copper frame and bringing the frame outside the package as "batwings" on both sides of the IC. Pins 2 and 3, and pins 6 and 7, are joined together to form the batwings. That allows a standard mini-DIP to handle output currents of up to 2.5 amps ! Since the frame is electrically connected to the substrate of the IC, maximum thermal transfer can occur. Sprague has, in effect, built the heatsink into the IC's package.
Like National, Sprague is also using a multi-lead, modified TO-220 package. Devices like the ULN3751Z and ULN3755W (single and dual op-amps, respectively) are rated for output currents of 3.5 amps !

Members of the Sprague power op-amp series can operate with supply voltages ranging from $\pm 3$ to $\pm 15$ volts, and they typically have output-voltage swings of 9 volts peak-to-peak. A motor-drive circuit, for example, usually has several components between the motor and the control circuitry that take care of voltage translation, current boosting, etc. The new Sprague parts allow you to drive a 6 -volt DC motor directly.

All members of the ULN37XX family feature an open-loop gain of 90 dB , unity gain stability, high supply and commonmode rejection, thermal shutdown, and so on. The dual versions have some extra features. Each op-amp in the package has what Sprague calls a "boost pin." Ordinarily, the peak-to-peak voltage swing of an op-amp will be several volts less than the supply voltage. However, by applying a voltage at least three volts higher than the supply voltage to the boost pins, the output-voltage swing will increase by about $20 \%$. And though you can still draw as much as 3.5 amps through the main output circuitry, the boost voltage only has to supply about 50 ma . Hence you can generate that higher voltage from the main power supply with a standard diode-capacitor voltage multiplier.

## Low-power audio amplifier

Sprague's ULN2283B low-power amplifier is packaged in an eight-pin miniDIP, and it uses the batwing packaging configuration discussed above. With a nine-volt supply, the IC can provide a voltage gain of about 40 dB and one watt of audio power into an eight-ohm load. As shown in Fig. 6, necessary support cir-


FIG. 6-SPRAGUE'S ULN2283B LOW-POWER audio amplifier requires only a few capacitors to make it a fully functional amplifier.
handles as much as 60 volts and 4 amps per channel. That means you can control a load of 240 watts with a single section of an IC! It's amazing to consider that a single IC can therefore control as much as 960 watts of power!

## AM stereo

Motorola and Sprague (under license from Motorola) are producing AM-stereo decoders for Motorola's CQUAM stereo system. That is the system used in the "AM Stereo Decoder" project that appeared in the January 1984 issue of RadioElectronics. If any significant market ever develops for AM stereo, both companies are sure to introduce a wide range of support chips specifically for CQUAM.

## Conclusions

As we already mentioned, this hasn't been the most exciting year for analog technology. The semiconductor industry


FIG. 7-SPRAGUE'S ULN2944W IS A QUAD power-driver that can supply as much as 60 volts and 4 amps per channel.
cuitry is minimal: two capacitors. If you need more than one watt of output power, Sprague has introduced a whole family of single-IC audio power amps ranging from the ULN2283B to the ULN3793W. The latter IC is packaged in a TO-220-style case; it can supply twenty watts of power to a four-ohm load!

## Motor drivers

Sprague has also been adding to their line of motor drivers. The UDN2944W, shown in Fig. 7, is a quad driver that
has been riding the crest of the computer boom for the last couple of years, so the big push for new technology has been in the digital market.

But the thrust of competition periodically swings back and forth, and we've already seen a resurgence of interest in analog components. As the computer market continues thinning out, the semiconductor manufacturers' interests will return to analog devices. That means next year we should start seeing all kinds of new analog goodies announced. R-E

## 



## THE MOST

ONE OF THE FIRST PLACES WE HEAD FOR AT the Summer Consumer Electronics Show， held annually in Chicago，is the exhibit honoring the recipients of the special De－ sign and Engineering Awards．Those awards，presented by the EIA（Electronics Industries Association），honor products that represent the vanguard in consumer electronics．All the products feature either the use of an innovative technology or an innovative design．While much of what is shown in the exhibit is high－end equip－ ment，most of the designs and innovations honored will eventually appear in more modestly priced versions．

1985 was a banner year for innovation， and the EIA chose to honor more than 150 products．We＇ve had a chance to examine a number of those，and what follows are some of the highlights．

## Audio

There was scarcely a visitor to CES who didn＇t have the Compact Disc（CD） in mind while walking through the aisles． Sony（Sony Drive，Park Ridge，NJ 07656），which had been the first company to produce a completely portable CD play－ er，built the diminutive disc player into the CFD－5＂boom box＂portable stereo．The


THE SONY CFD－5 portable stereo features a built－in compact disc player

CFD－5 houses not only the disc player（a non－programmable one that is remarka－
bly immune to casual bumps), but also an AM/FM stereo receiver, stereo cassette deck, five band equalizer, and two detachable speakers. At 17.3 pounds, that unit is not something you'd want to lug everywhere, but it is sure to be the hit of many an outdoor party.

In home CD players, dbx , Inc., (P.O. Box 100C. Newton, MA 02195), well known for their professional noise-reduc-


THIS COMPACT DISC PLAYER, the DX3 from dbx , features three circuits that allow the user to control the quality of the sound.
tion systems, unveiled a player incorporating three special circuits. Those circuits give the user a wider degree of control over the sound produced by the player than previously possible.

One of those circuits is a compression circuit. Now that may seem antithetical to the dynamic-range-enhancing premise of digital audio, but there are times when you might not want or need a wide dynamic range. For example, when using a CD for background music at a party, or when recording cassettes for use in the car. In the latter case, compression evens out the loud and soft passages so you don't have to turn the volume up to hear soft passages over ambient road noise or turn down loud sections to hear an ambulance siren behind you. Another circuit, called "digital audio impact recovery" adds impact to sharp brass and percussion hits (transients), which often get lost in the recording process. The third circuit, an ambience control, lets you adjust out-ofphase left-minus-right information; that control is used to alter the spatial characteristics of the sound.

Many audiophiles complain that there is an overt two-dimensionality to music from a CD. Some feel that that is largely due to the fact that recording variables such as poor microphone placement are much more obvious in CD than in other recording media. Barcus-Berry ( 5381 Production Dr., Huntington Beach, CA 92649) disagrees. That company claims that any spatial difficulties with recorded music playback stem from improper matching between amplifier and speaker. Inserted into the audio chain at the control amplifier's tape loop input/output, their BBE-2002 "differential load reactance compensator" recovers transients and alters the $\mathrm{L}-\mathrm{R}$ and $\mathrm{L}+\mathrm{R}$ phase relationships of the signal. In use, the effect of the BBE-2002 was noticeable, even on the noisy floor of CES.

In other audio electronics, the Carver (19210 33rd Ave. W., P.O. Box 1237,


THIS CARVER 2000 STEREO RECEIVER features a circuit to combat the effects of multipath reception.

Lynnwood, WA 98046) 2000 AM/FM receiver was recognized for its inclusion of a number of advanced features. In addition to its 200 -watt-per-channel (into 8 ohms) magnetic field power amplifier (a Carver design), the unit included several unique features. For example, the unit's "asymmetrical charge-coupled FM stereo detector'" is designed to help cancel the effects of multipath reception. Another feature, called "Sonic Holography" by Carver, improves the spatial characteristics of the stereo sound.

Turning to receiving antennas, a striking product design for an indoor FM antenna comes from Italy via Parsec Electronics (540 Madison Ave., New York, NY 10022). The Parsec 7403 is an omnidirectional antenna that is housed in a slender, black plastic case that stands almost 17 inches tall. The unit also in-


IMPORTED FROM ITALY, the Parsec 7403 antenna can dramatically improve the quality of FM reception under certain circumstances.
cludes an amplifier with an adjustable gain of 0 to 24 dB . The lead from the antenna to the receiver is 75 -ohm coax, but a 300 -ohm matching transformer is included for those who need it. In addition, the unit requires 117 -volts AC for the built-in amplifier.

To keep your stereo system under control, Mediacom (46 Merrick Road, Rockville Center, NY 11570) offers a remotecontrol fanatic's dream. Not for the
fainthearted, that system can be used to remotely operate virtually any micro-processor-controlled piece of stereo gear-but to do so, a control board must be installed in parallel with each component's front-panel controls. The relays on those boards, and hence the equipment in which they are installed, are controlled through a master command terminal. That terminal controls each component individually, and all the components in the house, regardless of their location, can be linked to the terminal via one or two bus lines. The master command terminal can be operated via a hand-held infrared remote control. In addition, each listening location in the house can be equipped with its own small control panel or box.

If you wish, the required equipment modifications can be performed by Mediacom dealers at a cost of about \$100-\$150 per component. Add to that the cost of wiring the control bus (or buses) through your house, and you can see where a moderately complex system can run into thousands of dollars to install. But then again, living like a movie star never was cheap.

## Satellite TV

Satellite TV was represented in the exhibit, too. Chaparral Communications (2360 Bering Dr., San Jose, CA 95131) was honored for its Polaramp, a one-piece polarotor (a circuit that electronically rotates the polarity of a satellite receiving antenna), $50-\mathrm{dB}$ gain low noise amplifier (LNA), and straight-through waveguide. Combining three key elements of a satel-lite-receiving system in a single package such as that goes a long way toward making satellite components simpler to install, match, and maintain.

Looking more like a piece of high-tech audio gear, Kenwood's (1315 E. Watsoncenter Rd., Carson, CA 90745) KSR-I000 stereo satellite receiver features simple two-step tuning. Once you select the satellite (positions are preprogrammed into the receiver's memory) and transponder number, the receiver handles all other tasks, including selection of the proper dish position, polarization, and stereo standard. The built-in microprocessor allows parents to lock out two channels, and an infrared remote control allows complete tuning from across the room. The $K S R$-1000 is part of a block-downconversion system (the downconverter is bought separately), which means that several units can be connected to a single dish for simultaneous viewing of more than one transponder.

## Video

The category that seemed to generate the most excitement at the CES in general, and at the Design and Engineering exhibit in particular, was video. Magnavox (a division of NAP Consumer Products, P.O. Box 6950, Knoxville, TN

37914）picked up honors for one of its new 37 －inch rear－projection TV sets．The ma－ jor innovation of that set，the RF8506AK stereo receiver－monitor，is its new black matrix projection screen，which affords a 180－degree viewing angle with minimal color shift．The set also features an RGB video input．Most of the television set－ tings（color，tint，picture，brightness，and sound＂parameters＂）are adjustable via remote control．To aid in such adjust－ ments，onscreen＂dials＂can be dis－ played．Despite its large screen size，the set is designed to take up no more space than a 25 －inch console（ 32.5 －inches wide by 20.5 －inches deep）．


A BLACK－MATRIX SCREEN gives this Magna－ vox RF8506AK stereo rear－projection monitor－ receiver a wide $\left(180^{\circ}\right)$ horizontal viewing area．

If 37 inches isn＇t big enough，consider Kloss Video＇s（ 640 Memorial Drive， Cambridge，MA 02139）VB2000 profes－ sional color－projection－monitor．Intended for presentation－quality display of com－ puter－generated text and graphics，that monitor comes in two versions，featuring 10 －or 6.5 －foot screens．It offers three in－ puts：NTSC composite video，analog RGB，and TTL RGB．That last input is intended for use with IBM $P C$ and $P C$－ compatible computers．The video specifi－ cations of that unit are impressive；its RGB video bandwidth of 15 MHz allows 80 －and 132 －column computer－generated text to be readable．

On the VCR front，Sony was honored for two of its new products．In the half－ inch Beta format，the SL－HF900 SuperB－ eta VCR contains not only Beta Hi－Fi and Sony＇s other top－of－the－line features，but a professional－like＂jog search＂control to assist in editing tapes．The control re－ sembles a large tuning knob on a commu－ nications receiver．By turning it one way or the other，you control the speed and direction of the tape travel while you


AN ALL－IN－ONE 8 mm camcorder，the CCD－V8 from Sony can record two hours of video．
watch the picture；it is even possible to advance the tape in single frame incre－ ments．That feature offers considerably more precision for editing cuts than the now standard two－button visual search controls on other VCR＇s．

Although Sony is not the first to intro－ duce equipment in the relatively new 8 mm video format，its CCD－V8 cam－ corder won an award for its compact，one－ piece design．The unit can record up to 2 hours of video on one cassette，and up to 12 hours of digital audio（only）；the dy－ namic range of the recorded audio is claimed to be 88 dB ，which approaches that of a compact disc．Having learned its lesson from the less feature－laden Be － tamovie units，that 8 mm camcorder lets you play back the tape while you＇re still in the field，using the black－and－white elec－ tronic viewfinder as a display device，to make sure you got the shot you wanted． Also offered are a companion tuner／timer and a sophisticated editor console．

## Computers

While computer software had its own， separate design exhibit，several hardware products were included in the main dis－ play．Perhaps the most innovative was a device called the Space Pen from Soniture，Inc．（480 Vandell Way，Camp－ bell，CA 95008）．Perhaps the best way to summarize the function of that unit is to call it a three－dimensional light pen．The main unit is an L－shaped box that fits around the upper left corner of the com－ puter monitor．Connected by a wire to the unit is a handheld wand that you can move in any direction－up／down，left／right， and in／out．The box at the monitor detects the relative $\mathrm{X}, \mathrm{Y}$ ，and Z location of the wand at any moment．In the Z dimension， it is claimed that the unit can detect move－ ments as small as .01 －inch at distances of up to six feet from the monitor．The com－


THE SPACE PEN from Soniture allows three－ dimensional input to a computer．
pany had on display prototype software applications in which the Z axis con－ trolled paging through a series of screens （like flipping through pages in a book） and also the movement of a spaceship that could fly in front of and behind a fixed object on the screen．For the latter，as the pen was moved closer to the user，the ship appeared larger and the computer－gener－ ated engine sound grew louder．The unit is available for Apple，IBM，Commodore， Atari，and MSX computers；when used in its two－dimensional mode，the Space Pen is compatible with several popular touch－ tablet software programs．

A different kind of input device by Covox，Inc．（675－D Conger Street，Eu－ gene，OR 97402）lets you add voice rec－ ognition to a Commodore 64，Apple II，or Atari $X L / X E$ series computer．The Voice Master includes a patch to BASIC that allows you to build speaker－dependent voice recognition into your own pro－ grams．The unit also features a＂voice harp＂mode that causes musical notes to be generated in response to a hummed or whistled＂input signal．＂


IT FLOATS! The handset of the Float Fone from Webcor can even be used in the shower.

## Personal electronics

This is a fun category because although these products don't get a great deal of attention (after all, they are not audio, video, or computer related), they often feature some rather fascinating and surprising innovations. One such product is Brother International's (8 Corporate Pl., Piscataway, NJ 08854) Word-Spell, a spelling checker for use with a Brother electronic typewritter. As you type along, Word-Spell automatically checks your spelling, alerting you to any misspelled words. The unit features a 50,000 -word main dictionary, to which 300 words, chosen by the user, can be added.

These days, most of us own one of those credit-card-sized calculators. Casio (15 Gardner Rd., Fairfield, NJ 07006) has taken that product one step farther by incorporating both a full-function calculator and an FM radio into a 1.1 ounce package only 3.9 mm thick called the $R D-10$ FM Card Radio.

Another remarkable feat of miniaturization is the Panasonic (One Panasonic Way, Secaucus, NJ 07094) $R N-36$

## Telephones

With the wide consumer acceptance of owning your telephone, manufacturers are loading their units with all kinds of features in a race to win a share of that profitable market. One that has apparently won that features race, and a design award as well, is the model 7890 from Superphone (47001 Benicia St., Fremont, CA 94539). That two-telephone-line model combines a number of sophisticated timer and phone features into an impressive telephone workstation. For example, you can program it to forward incoming calls to different phone numbers depending on the time of day. When you program PBX or long-distance service numbers, it listens for the intermediate dial tone before proceeding with the rest of the call. If one of the two lines attached to the phone is an outgoing WATS line, you can call in on the local number and dial out using the WATS line (that feature is commonly called a WATS Extender). Moreover, if you have two lines connected, you can dedicate one to a data line, using the phone's built-in 300 -baud modem. While


THE WORD-SPELL from Brother automatically signals whenever if finds a misspelled word.


THE RN-36 from Panasonic is the smallest microcassette recorder yet.
one line is sending and receiving computer data, you can carry on a conversation on the other line. All that flexibility is not inexpensive, however, The phone has a suggested list price of almost $\$ 700$.

We've all seen ads for cordless phones in which the happy owner is sitting at or in the pool talking merrily away on the unit. But have you also wondered what fate the phone suffers when the inevitable happens and the unit sinks to the bottom of the pool? Webcor Electronics (107 Charles Lindberg Blvd., Garden City, NY 11530) has put that concern to rest with its Float Fone cordless telephone. Operating on the new $46 / 49 \mathrm{MHz}$ frequency pairs, the Float Fone's handset is waterproof and is fitted with a buoyant yellow rubber "boot." If you wish, the unit can even be used in the shower.
microcassette recorder. That dictation recorder is roughly the height and width of a credit card and measures just over onehalf inch thick-dimensions that Panasonic claims make the unit the smallest microcassette recorder available. The unit's tiny motor operates on 1.5 volts, and the recorder's electronics are manufactured using surface-mounted-component technology and a flexible printedcircuit board. One way the designers were able to make the unit so small was to remove the play back speaker. It is housed in a separate small desktop module. You can still playback the tape with the portable unit, however, through earphones. And despite its small size, the $R N-36$ features a digital tape counter and a removable microphone that can be clipped on a lapel.

R-E


#### Abstract

This low－cost home alarm system features a digital combination lock，optional display circuitry，simple installation；and it＇s not limited to home use．


ANTHONY J．LaMARTINA

IN TODAY＇S WORLD，BOTH PRIVATE HOME－ owners and commercial businesses are becoming more and more concerned with security．As with many other products， what was once affordable only by large businesses is now within reach of small businesses and average homeowners． Wired and wireless alarm systems are readily available now for both new and pre－existing homes．

While the new wireless systems are grabbing the lion＇s share of the market， wired systems are still effective．Our sys－ tem is a wired alarm，and has features found in the most expensive security sys－ tems，including a built－in exit timer，a digital combination lock with 65,535 pos－ sible combinations，a loop－test circuit， provision for both keyboard and failsafe－ keyswitch entry，and multiple output－con－ tacts for devices such as a siren，an auto－ matic telephone dialer and floodlights．

Although primarily designed to func－ tion as a low－cost，small－business or home－security system，our alarm can be used as an automobile or marine intrusion alarm，an ignition combination lock，or both．In addition，either the intrusion alarm or the combination lock may be extracted and used as－is in other circuits．

A wired alarm system like ours may be installed during the construction phase of a home，business，shop，or garage．Our system may also be added to pre－existing homes or businesses with very little＂loop wire＂showing，simply by using magnetic reed switches mounted near the tops of doors and windows．

As seen in the opening photo，our alarm has three main sub－assemblies：the control panel，the main chassis and the keyboard panel．The keyboard panel is mounted outside the protected area，near the point of entry．It contains the keyboard used to punch in the digital combination that disarms the alarm．

The other two assemblies are mounted inside the protected area，in somewhat inaccessible locations，to prevent un－ authorized tampering．The main chassis

houses most of the alarm＇s electronic sub－ assemblies；the control panel contains components used to arm the alarm，reset it，and test the sense loop．Care should be taken to ensure that unauthorized persons have no access to the control panel．It is not necessary to mount the control panel right by the exit，because the alarm＇s tim－ er provides an exit delay of as long as two minutes（after you press the MASTER RE－ SET switch）before the alarm is armed．

## Alarm operation

You must do three things to operate this alarm：program the digital－entry＂com－ bination lock＂code，arm the alarm when leaving the protected area，and re－enter the protected area without tripping the alarm．
－Programming－The four－digit entry code is programmed using eight－position DIP switches S1 and S2，shown in Fig． 1. Four individual switches from both S 1 and S2 are used to encode each digit of the four－digit entry code．Hence，each digit
may have any one of sixteen different val－ ues．
－Entry－After the proper four－digit code has been punched in，an LED on the key－ board panel is turned on and indicates that the alarm has been disarmed．When the ENTER switch，which is also on the key－ board panel，is pressed，the protected area may be entered．
－Exit－Before leaving the protected area，the TEST switch on the control panel should be pressed．If the sense loop is intact，the test lamp will light up．If it doesn＇t，there must be a break in the sense loop，which obviously should be cor－ rected before you leave the premises．As－ suming that the test lamp does illuminate，the MASTER RESET switch is pressed next．That clears the combina－ tion－decoding circuitry，which is indi－ cated by the appearance of zeros in each digit on the display board．The master reset switch also resets the timer board， causing the exit－delay timing to begin．

The exit delay gives you between 30


FIG. 1-THE MAIN CHASSIS ASSEMBLY of the alarm. There are two circuit boards, two relay boards, and a power-supply board.


FIG. 2-SCHEMATIC DIAGRAM OF THE TIMER BOARD. Note that two separate circuits are shown here, the alarm circuit above, and the timer circuit below.
and 120 seconds (depending on the setting of a resistor on the timer board) to leave the protected area. After that time, the ARMED lamp LMP5 on the control panel illuminates, indicating that the alarm is armed. From that time on, the main relay will activate an alarm bell-or whatever devices you have connected to it-if the sense loop is broken (unless you disarm the system by punching in the proper code or activating the failsafe keyswitch).

It is important that the TEST switch not be cycled while the system is armed. That would activate the alarm, because the TEST switch contacts are part of the sense loop. If a "make before break" switch is used for the test function, that precaution will not apply.
Whenever the sense loop is broken while the alarm is armed, the alarm lamp on the control panel will illuminate, and the main relay will activate. To disarm
the alarm, the continuity of the sense loop must be restored, and the alarm reset switch on the control panel must be pressed.

Now that we understand how how to use the alarm, let's discuss, in some detail, how each circuit board and sub-assembly works.

## Timer board

As shown in Fig. 2, both the alarm sensing circuit and the timer circuit are located on the timer board. See "PC Service" for that board's full pattern. The timer circuit uses a 555 (IC2) to generate clocking pulses with a frequency of about 1 Hz ; those pulses drive the counting chain consisting of IC3, IC4-a and IC5-a. The output of the 555 is connected to the clock input of IC3, which divides that signal's frequency by eight. The output of IC3 is fed to IC4-a, which again divides the signal by eight. The final signal is applied to "D"flip-flop IC5-a. Since its data line is tied high, its $Q$ output is forced high, which turns on transistor Q3 and, therefore, relay RY2.

The closing of the relay's contacts grounds the upper portion of the circuit, the alarm sensing circuit. Until the upper circuit obtains power, the alarm is unarmed, so breaking the sense loop would not trip alarm relay RY1. (That relay, and its associated circuitry, will be discussed more fully next time.) But after the upper circuit is grounded, of course, breaking the sense loop will trip RY1.

Visual confirmation that the counting circuit is operating is provided by LED1-LED4, and visual confirmation that the sensing circuit is operating is provided by LED5. Moreover, ARM lamp LMP3 is wired in parallel with LED5, and that provides visual confirmation (at the control panel) that the sensing circuit is operating. Diode D2 protects the coil of RY2 from high-voltage spikes induced when the relay is energized.

The master reset switch, S21, which is mounted on the control panel and will be shown next time, is normally in the RUN position; that grounds the RST lines of counters IC3, IC4-a and IC5-a. Pressing


FIG. 3-THIS ISOLATION CIRCUIT may be used for safe control of 115 -volt AC devices from a 12 volt DC input.


FIG. 4-SCHEMATIC DIAGRAM OF THE DISPLAY BOARD. Integrated circuits IC8-IC15 are the real heart of this circuit.

## PARTS LIST

All resistors are $1 / 4$-watt, $5 \%$, unless otherwise noted.
R1, R3-100,000 ohms
R2-4.7 megohm
R4, R5- 10,000 ohms
R6-100 ohms
R7-100,000 ohm potentiometer
R8-3,300 ohms
R9, R10, R11, R12, R13-150 ohms
R14, R15, R16-220 ohms
R17-R46-330 ohms
R47-25 ohms, 1 watt
R48-10,000 ohms, 1/2-watt

## Capacitors

C1, C5, C9, C12- $0.1 \mu \mathrm{~F}, 35$ volts, tantalum
C2-100 pF, ceramic disc
C3- $0.47 \mu \mathrm{~F}$, ceramic
C4- $10 \mu \mathrm{~F}, 10$ volts, electrolytic
C6, C8, C11- $1 \mu \mathrm{~F}, 10$ volts, electrolytic
C7-4000 $\mu \mathrm{F}, 25$ volts, electrolytic
C10- $2000 \mu \mathrm{~F}, 25$ volts, electrolytic

## Semiconductors

IC1-4001 quad NOR gate
IC2-555 timer
IC3-4017 decade counter
IC4-4518 dual BCD up counter
IC5, IC7-4013 dual "D" flip-flop
IC6-4081 quad AND gate
IC8-IC11-74LS85 4-bit comparator
IC12-IC14-74C173 4-bit latch
IC15-73C923 20-key encoder
IC16-IC19-9368 hexadecimal display decoder/driver
IC20-LM340T-5 5 volt regulator
IC21-LM340T-12 12 volt regulator
IC22-H11C5 or MOC3001 opto-isolator
BR1, BR2-BR31 1-amp bridge rectifier
D1, D2-1N4001
DSP1-DSP4-FND500 common cathode, 7 -segment display
LED1-LED10-standard LED's
Q1-2N4403
Q2, Q3-2N4401
Q4-2N2222
TR1-BT136-600 or similar triac
Other Components
F1- 0.6 Amp, 250 volts
F2, F3-1 Amp, 250 volts
LMP1-LMP3- 12 volt lamp
LMP4, LMP5-5 volt lamp
RY1- 12 volts DC, DPDT relay
RY2-RY4-SPST 5-volt reed relay
S1, S2-8-position DIP switch
S2-S18-16-position keyboard matrix
S19-SPST momentary
S20-SPST momentary
S21, S22-DPDT momentary
S23-SPST key-operated switch (optional)
T1-117-volt AC Primary, 8 and 13 volt secondaries (Northlake F3-214)
EC1, EC2-22-position edge connector
PL1, PL2-15 position Molex
PL3-PL5-16 position DIP header
PL6-9 position Molex
PL7-4 position Molex
PL8-12 position Molex
PL9-2 position Molex
SO1, SO2-15 position Molex
SO3-2 position Molex
SO4-6 position Molex
SO5-SO7-16 position DIP socket
SO8-8 position Molex
SO9-4 position Molex
SO10-12 position Molex


FIG. 5-SCHEMATIC DIAGRAM OF THE KEYBOARD PANEL. Switches S3-S18 may be a standard 16key matrix keyboard or separate SPST momentary switches.

S21 applies +5 volts to those lines, thereby resetting the IC's. When that happens, a " $\emptyset$ " appears at IC5's Q output, and that turns off the transistor, the relay, and, thereby, the alarm sensing circuit. At that point counting begins again and, after the pre-set time delay, the sensing circuit will again be operational.

Variable resistor R7 allows you to vary the speed of oscillation of the 555 and, thereby, the overall exit delay. With the component values shown, the overall delay may vary from 30 to 120 seconds. If a longer or shorter delay is needed, IC4's CLK input may be connected to a different output on IC3, thereby changing that IC's division constant.

The alarm circuit shown in the upper half of Fig. 2 has been adapted from an "off-the-shelf" public-domain circuit. It is composed of a bistable latch built from IC1-a and ICl-b. Pin 1 of ICl-a is normally held low by the sense loop, and that keeps pin 3 high. That prevents Q1 from getting the proper bias so it, Q2 and relay RY1 all remain off. When the sense loop is broken, C 1 begins charging through R2. Pin 1 eventually goes high, so pin 3 goes low. Q1 then turns on, turning Q2 on and energizing RY1. Diode D1 is in the circuit to protect the coil of RY1.

The value of capacitor Cl could be increased to provide a longer charging time, thereby allowing additional time before activating the alarm relay. Such additional time might be useful to prevent false triggering of the alarm brought on by doors or windows that vibrate because of the wind.

The alarm circuit will remain activated until the sense line is closed and alarm RESET switch S 20 is pressed. Merely closing the loop will not shut off the alarm;

S20 must also be pressed. When S20 is pressed, pin 6 of ICl goes high, and pin 4 goes low. If the sense loop has been closed, both inputs to the upper gate are low, so that forces the output at pin 3 high, and Q1 turns off.

RY1 may suffer from contact pitting if high-current devices are switched. If it does, the simple triac isolation circuit shown in Fig. 3 may be used to control 115 -volt AC devices.

## Display board

As shown in Fig. 4, the display board is composed of three parts: the keyboard scanning circuit, the display circuit and the comparator circuit.

National Semiconductor's MM74C923 is a 20-key keyboard encoder. Only sixteen keys were used in our prototype, but the MM74C923 was used to facilitate future expansion. A 74C922 16-key encoder could be substituted, but the two IC's are not pin-compatible, so the PC artwork would have to be modified. The keyboard itself is a surplus unit; individual SPST momentary switches could be used if desired. The wiring diagram for the keyboard panel is shown in Fig. 5.

Capacitors C5 and C6 on the display board are used to control the keyboard scanning rate and the key-switch debounce time, respectively. We found that values of $0.1 \mu \mathrm{~F}$ and $1.0 \mu \mathrm{~F}$, respectively, work well with several different keyboards, but the values of those capacitors may have to be adjusted slightly.

That's all we have room for this month. Next time, we'll finish up our look at the display board, and the rest of our alarm system, and begin to show you how to build the project.

R-E

## B（UTMD TTHIS

## VIDEO TITLER



## JACK FLACK

Part 2 WHEN WE LEFT OFF last month，we had just begun describing the video titler cir－ cuit．Let＇s start by finishing up our de－ scription of the power supply（which was shown in Fig． 6 in the November issue）．

A simple battery backup circuit was included in the power supply so that 30 pages of titles could be saved in user RAM．That lets you recall and display your best page creations in a snap！The junction of D11 and D12 is set at +5.7 volts using the unregulated +8.5 volts that feeds the +5 －volt regulator（IC25）． When the titler power is on，D12 is for－ ward biased，and +5 volts is supplied to the RAM．However，D13 is reverse bi－ ased，so current cannot flow into the lithium battery，B1．When the power is turned off，D13 becomes forward biased and D12 becomes reverse biased，so the RAM is supplied with +3 volts（which keeps it in the standby mode）and the battery is isolated from the rest of the titler＇s circuit．
The final thing we＇ll point out about the power－supply section is that the +3 ， +1.5 and +0.7 volt supplies are derived by using a simple diode chain that con－ sists of D14 through D17．

## External－sync generator

The external－sync generator section of the video titler extracts the basic timing components from the external video sig－ nal and provides the rest of the titler with them．The schematic of the external－sync section is shown in Fig．7．The external composite video can be input to the titler either through Jl（a 10 pin connector like those used on color video cameras）or through J4，（a standard RCA－type phono jack）．The video source switch（S1）de－ termines which jack is used for input．
The external video signal is terminated with R3 and is coupled to the clamping circuit through C1．Its sync level is clamped at 0.7 volt using D1 and R4，and is fed to the inverting input（pin 6）of comparator IC1－a．The non－inverting in－ put of that comparator is set slightly high－ er than 0.7 volts，and that causes a positive pulse to occur on the com－ parator＇s output（pin 1）during the most negative point of the input signal（i．e．，the horizontal－and vertical－sync level）．That positive pulse is referred to as＂composite sync＂and is used to generate three dif－ ferent signals．
The positive edge of the external com－ posite signal is used to trigger a non－re－
triggerable one－shot（IC2－a）．The nega－ tive－going output pulse（referred to as the external horizontal pulse），is set at about 50 microseconds by C4 and R9，so that alternate vertical serrations and equaliz－ ing pulses are ignored and only horizontal information is passed．

Noise that may be present in the exter－ nal composite－sync signal is also reduced by the one－shot．This noise reduction im－ proves the stability of the clock－generator circuit．

The negative edge of the external com－ posite－sync signal triggers the other one－ shot（IC2－b）．The positive－going output pulse of that one－shot（referred to as the BURST GATE）is set at about 3 microse－ conds by C8 and R10．Three microse－ conds is the approximate duration of the colorburst of the external composite－vid－ eo signal．

The external composite－sync signal is also fed through a lowpass filter（made up of R6，R7，C6，and C7）where only the vertical portion is passed．Another com－ parator（ICl－b）is used to generate a clean positive pulse during the vertical－sync in－ terval．

The comparators in IC1 are open－col－ lector devices and require pull－up re－


FIG. 7-THE EXTERNAL SYNC GENERATOR section of the video titler extracts the timing components from the external composite-video signal for use in the rest of the titler circuit.
sistors at the outputs. The outputs of two or more open-collector devices can usually be tied together ("or-tied") such that any one device can pull the output low. Conversely, all devices must be high for a high output.
In this case, the output of inverter IC3-a is or-tied to the output of comparator IC1b. The input of the inverter receives the vDP horizontal pulse, which is a narrow pulse generated external to the VDP (Video Display Processor. The trailing edge of that pulse is ultimately used to realign the VDP's internal horizontal counters.

Remember that the VDP does not provide a separate horizontal output pulse and does not generate vertical serrations or equalizing pulses in its non-standard video signal. Therefore to obtain a horizontal sync during the vertical period, a pseudo-horizontal pulse had to be constructed using available signals.

Our goal is to reset only the VDP vertical counters and to let the VDP's horizontal timing track the horizontal timing of the external video signal. Unfortunately, the leading edge of any vertical reset pulse applied to the the VDP's RESET/SYNC pin will also reset the VDP's
horizontal counters. And since our external video signal is operating in an interlaced mode, the leading edge of the vertical portion will occur every 262.5 horizontal lines. Thus, characters on the left of the screen would jump to the center and back with each successive external vertical sync.
We got around that problem by inserting the pseudo VDP horizontal pulses into the external video signal, and using that hybrid vertical reset signal to reset the VDP vertical counters. Thus a constant relationship between the internal VDP horizontal timing and the pseudo VDP horizontal pulse used by the PLL in the clock-generator section was re-established. Inverter IC3-b is used to establish the proper polarity of the HYBRID VERtical reset signal before it goes to the level shifter.

## Clock generator

The clock-generator section of the video titler provides the 10.7 MHz master system clock. Its schematic is shown in Fig. 8. The clock actually operates at one of two frequencies depending on the mode in which the titler is operating. When it's operating in the internal mode, the clock frequency is three times the chroma clock frequency. ( $3.579545 \times 3$ $=10.738635 \mathrm{MHz}$.) In the external mode, the master-clock frequency is equal to the external video horizontal frequency multiplied by 684. (3.579545 $\mathrm{MHz} / 227.5) \times 684=10.762235 \mathrm{MHz}$

The video titler must use two separate master frequencies because of the method by which the VDP horizontal frequency tracks the external horizontal sync.

The clock-generator section uses two IC's that operate together as a phase-locked loop or PLL. Motorola's MC4044 (IC4) digital phase detector and MC4024 (IC5-a) voltage-controlled oscillator (VCO) function as a special type of PLL referred to as a digital frequency synthesizer or frequency multiplier. (Note that those two IC's are not 4000 -series CMOS devices)
The output of a frequency synthesizer is an an exact multiple of the reference frequency. That's accomplished by dividing the output of the VCO by a desired value before it is input to the phase detector. The PLL then manipulates the VCO such that both the reference frequency and the divided-down VCO output are always in phase. Figure 9 shows the output and reference signals of the titler's PLL frequency synthesizer in the internal and external modes of operation.

In order to simplify the design of the titler, no attempt was made to operate the VDP using standard NTSC video timing in the internal mode. Instead, the VDP operates at the frequency in which it was originally designed, using a readily available reference frequency, the снroma


FIG．8－THE CLOCK－GENERATOR section provides the titler＇s master $10.7-\mathrm{MHz}$ system clock．IC4 and IC5－a make up a phase－locked loop．

CLOCK（free running at 3.58 MHz ）．
In the external mode，a pseudo VDP horizontal pulse is derived from the mas－ ter clock using the VDP＇s cpu Clock output（pin 37）．That signal is the master clock divided by 3．Two counters（IC6 and IC7）further divide the CPU clock by 228 ，which results in an output signal equal to the master clock divided by 684 （which is the equivalent to the horizontal timing inside the VDP）．The PLL com－ pares the falling edge of this pseudo VDP horizontal pulse to the falling edge of the EXTERNAL HORIZONTAL PULSE and ad－ justs the $10.7-\mathrm{MHz}$ system clock in a way that minimizes the phase difference of the two signals．

The rest of the components in the clock－generator section provide the switching mechanism between external
and internal modes．Four CMOS analog switches（IC8）determine which two sig－ nals are used by the PLL．The switches are controlled by two mode－select signals， which are generated in the video mixer／ buffer section，and which we＇ll talk about shortly．

## Video display processor

The VDP section is the heart of the titler＇s video generation circitry．Its sche－ matic is shown in Fig．10．Data is passed to and from the microcomputer section via a bidirectional data bus．The TMS9128 VDP（IC10）maintains its data lines in their high－impedance state until either control lines $\overline{\mathrm{CSW}}$ or $\overline{\mathrm{CSR}}$ go low，at which time the VDP reads from or writes to the data bus．The VDP＇s mode line（pin $13, \mathrm{IC} 10$ ）is tied to the low order address
line $(A \emptyset)$ and it controls whether the data transferred is address，memory，or register data．

Data transfers will bee discussed in more detail when we examine the software of the titler．All we＇ll say here is that you should note that all VDP data and address lines appear to be＂backward．＂In other words， $\mathrm{D} \emptyset=\mathrm{CD} 7, \mathrm{AD} \emptyset=\mathrm{A} 7, \mathrm{RD} 7=$ DQØ．Texas Instrument＇s documentation on the VDP refers to all least significant data lines as XX7．The most significant lines are labeled XX0．

A voltage－level adjustment for the RE－ SET／SYNC input of the VDP is made up of C16，R17，R18，D2，and Q2．Two signals， the hybrid vertical reset pulse and the MPU RESET are joined to generate a three－ level signal．

On power－up，MPU RESET remains low for a few microseconds causing the VDP to reset．It then rises slowly to the 5 －volt （inactive）level．In the internal mode，the HYBRID VERTICAL RESET pulse is not pres－ ent．In the external mode，the pulse is AC－ coupled to Q2，which inverts the signal and establishes its voltage swing between 5 and 12 volts．

As mentioned earlier，that 5 －to 12 －volt pulse maintains vertical synchronization between the VDP and the external video signal．It also maintains a constant phase relationship between the VDP＇s internal horizontal counters and the VDP horizon－ tal pulse（generated outside the VDP） used by the PLL．The Zener diode，D2，is used to hold the mpu reset line at，or below， 5.1 volts．

The VDP directly interfaces with two 4416， $16 \mathrm{~K} \times 4$ bit dynamic RAM＇s（IC11 and IC12）．The entire $16 \mathrm{~K} \times 8$ video RAM is needed by the VDP to generate the titler＇s video images．

## The chroma processor

The chroma processor performs a number of functions necessary to generate proper VDP color．Its schematic is shown in Fig．11．Remember that superimposing graphics with stable colors requires that the VDP chrominance information be phased－locked to the external video color－ burst signal．

We can accomplish that using RCA＇s CA3126 chroma processor（IC14）．That IC generates a continuous $3.58-\mathrm{MHz}$ sinewave that is phase－locked to the color－ burst of the external video signal．The chrominance portion of the external video signal is extracted using highpass filter L1，C20，and C21．The burst gate signal，applied to pin 9 ，provides a win－ dow for extracting the colorburst signal．

In the external mode，the titler passes the external burst（as well as the external horizontal and vertical sync signals）di－ rectly to the video out jack，J6．The chroma filter phase－shifts the incoming colorburst in such a way that the resulting chrominance information contained in the


FIG. 9-SIGNALS ASSOCIATED WITH PLL FREQUENCY SYNTHESIS. Signals for the internal mode are shown in $a$, and signals for the external mode are shown in $b$.


FIG. 10-THE VIDEO DISPLAY PROCESSOR section is the heart of the titler's video generator. The TMS9128 VDP (video display processor) directly interfaces to two $441616 \mathrm{~K} \times 4$ dynamic RAM's.

## PARTS LIST

Note: This is a corrected Parts List and supersedes that shown last month.

## All resistors $1 / 4$-watt, $5 \%$ unless other-

 wise noted.RN1- 10,000 ohms $\times 9$ resistor network
R1,R16- 10,000 ohms
R2- 6800 ohms
R3- 75 ohms
R4,R48-51,000 ohms
R5, R11, R12, R15, R25-R27, R35, R43, R49- 1000 ohms
R6, R8, R30, R31, R39-4700 ohms
R7- 3900 ohms
R9, R38- 5100 ohms
R10- 4300 ohms,
R13, R16, R18, R22, R24, R28, R402200 ohms
R14, R23- 680 ohms
R17, R36- 120,000 ohms
R19, R20, R21- 470 ohms
R29, R32- 1000 ohms, PC-mount trimmer potentiometer
R33, R37-1500 ohms
R34, R47-220 ohms
R41, R42- 3300 ohms
R44, R45-820 ohms
R46- 10,000 ohms, PC-mount trimmer potentiometer
R50-33 ohms
R51-68 ohms

## Capacitors

C1, C40-C42, C44, C64-10 $\mu \mathrm{F}, 25$ volts, electrolytic
C2, C3, C5, C9, C11, C14, C15, C16.C26, C33, C38, C39, C45, C47, C48, C49, C50, C51, C53, C60, C61, C62, C63, C68, C70, C72-C74-0.1 $\mu \mathrm{F}$ ceramic disc
C4, C22, C23, C25, C29, C31, C32, C34, C36-0.01 ceramic disc
C6- $0.0022 \mu \mathrm{~F}$ mylar
C7-. $0047 \mu \mathrm{~F}$ mylar
C8- $0.001 \mu \mathrm{~F}$ mylar
C10- $0.47 \mu \mathrm{~F}, 25$ volts, electrolytic
C12, C27-5-30 pF PC-mount trimmer
C13, C28-12 pF, ceramic disc
C17-C19, C66-220 pF, ceramic disc
C20-150pF ceramic disc
C21-390 pF ceramic disc
C24, C30, C58-1 $\mu \mathrm{F}, 25$ volts, electrolytic
C35, C37-47 pF ceramic disc
C45- $22 \mu \mathrm{~F}, 10$ volts, electrolytic
C56, C57-100 $\mu \mathrm{F}, 25$ volts, electrolytic C46, C52, C59, C67, C69, C71- $47 \mu \mathrm{~F}, 16$ volts, electrolytic
C54-2200 $\mu \mathrm{F}, 25$ volts, electrolytic
C55- $4700 \mu \mathrm{~F}, 16$ volts, electrolytic
C65-33 pF, ceramic disc

## Semiconductors

IC1-LM339 quad comparator
IC2-74LS221 dual non-retriggerable one-shot
IC3-74LS05 hex inverter
IC4-MC4044 phase-frequency detector
IC5-MC4024 dual voltage-controlled multivibrator
IC6, IC7-74LS191 up/down binary counter
IC8, IC9, IC13-4066 quad analog switch IC10-TMS9128 video display processor (Texas Instruments)

IC11，IC12－4416 16K $\times 4$ dynamic RAM， 200 ns
IC14－CA3126 chroma processor（RCA）
IC15－74LS00 quad nand gate
IC16－LM1889 TV video modulator（Na－ tional）
IC17－SN75108 Dual in－line receiver
IC18－74LS139 dual 1－of－4 decoder
IC19－2764 4K $\times 8$ EPROM
IC20－HM6264 LP 8K $\times 8$ static RAM
IC21－74LS273 octal D－type flip－flop
IC22－74LS244 octal 3－state driver
IC23－MC6809 microprocessor
IC24－ 7812 K regulator，+12 －volts
IC25－7805K regulator，+5 －volts
IC26－75L05 regulator，-5 －volts
D1，D3，D4，D5－D7，D11－D18－1N914
D2－1N751 Zener， 5.1 volts
D8，D9－1N4001 rectifier， 50 PIV
D10－not used
BR1－full－wave bridge rectifier， 6 amps
LED1－standard red LED
Q1，Q6－2N2222
Q2，Q3－2N3906
Q4，Q5－2N3904
Other components
XTAL1－ 3.579545 MHz
L1－56 $\mu \mathrm{H}$
T1－14－volt secondary，center－tapped
J1－10－pin video camera jack
J2，J4－J6－PC－mount phono jack
J3－3／32 inch phone jack
S1－SPST PC－mount slide switch
S2－DPDT rocker switch，vertical PC mount

Miscellaneous：Lithium－battery holder and battery，49－key keyboard，16－conduc－ tor ribbon cable，enclosure，etc．

The following are available from Micro－ Video－Technology，P．O．Box 76，Chat－ tanooga，TN 37343：main PC board （silk screened，with gold fingers），$\$ 40$ ； Programmed EPROM，\＄25；Custom keyboard，$\$ 80$ ；Custom enclosure， \＄40；All switches，jacks，and con－ nectors，\＄30； 14 VCT wall transformer， \＄30；TMS9128 VDP，\＄30；partial kit（in－ cludes all the above）， $\mathbf{\$ 2 5 0}$ ．All orders add \＄5（\＄13 outside U．S．）for shipping and handling．

The following are available from JDR Microdevices， 1224 South Bascom Ave．，San Jose，CA 95128 （800） 538－5000：All components－except those available from Micro－Video－ Technology－$\$ 69.95+\$ 2.50$ for ship－ ping．

The following is available from MFJ En－ terprises，Inc．， 921 Louisville Road， Starkville，MS 39759：Complete titler， assembled and tested with 1 year un－ conditional guarantee，$\$ 599.95$ plus $\$ 6$ shipping．（Return if not satisfied within 30 days for refund，less shipping．）Or－ ders only（outside Mississippi） 1－800－647－1800．Information and Mis－ sissippi orders 601－323－5869．Master－ Card and Visa accepted．


FIG．11－THE CHROMA PROCESSING section is responsible for generating proper VDP color．IC14，a CA3126 chroma processor，generates a $3.58-\mathrm{MHz}$ sinewave that is phase－locked to the external video signal＇s colorburst．That is necessary for proper VDP color．
titler＇s signal is in phase with the external colorburst．

In the internal mode，the crystal used by $\mathrm{IC14}$ is allowed to free－run．

The VDP＂Y＂signal has no chromi－ nance（or color）portion．In order to add color，the VDP has two color－difference signals： $\mathrm{R}-\mathrm{Y}$ and $\mathrm{B}-\mathrm{Y}$ ．

Figure 12 is an illustration of the VDP video signals．The color－difference sig－ nals represent color information as positive or negative excursions from a center voltage（Fig．12－a）．The chroma modulator，LM1889（IC16），converts the color－difference signals into a chromi－ nance signal which modulates the VDP


FIG. 12-VIDEO DISPLAY PROCESSOR SIGNALS. In a, we see the VDP signals in the internal mode, while $b$ shows signals in the external mode.


FIG. 13-THE VIDEO MIXER/BUFFER section of the video titler determines whether the VDP video or the external video signal is sent to the output.
" Y " signal through R33. One requirement of the LM1889 is that the center voltage of the color-difference signals must be stable. This stable reference is referred to as the chroma bias (pin 3, IC16).

The method used by the titler to DCclamp the center voltage of the color-difference signals is to introduce the chroma bias voltage during the sync period when the signals are at the center voltage. That works fine while in the internal mode, but, the color-difference signals function differently in the external mode.

Figure 12-b shows how the color-difference signals fall to a special level to signify that the external video should be selected for display. As will be discussed later, the external video-select information is derived from the $\mathrm{B}-\mathrm{Y}$ signal.

In order to establish a constant chroma bias during external mode, the titler introduces a different voltage to the AC-coupled color-difference signals. That voltage is adjustable via R29.

A positive-going VDP sync pulse is generated by Q3, C43, R36, C66, and R37. That pulse determines when the voltage is introduced, while the external mode select and the internal mode select signals are used to determine which voltage is introduced. Those two select-signals turn on the appropriate set of analog switches in IC9.

Other areas within the chroma-processing section include the VDP video level adjustment, which consists of C44, R32, and D4. That adjustment establishes the proper DC level of the VDP video before mixing with the external video signal. The chroma clock, a TTL-compatible 3.58MHz signal, is used as a PLL reference during internal mode.

## Video mixer/buffer

The video mixer/buffer determines whether the VDP video, or the external video signal, is fed to the titler's output. Its schematic is shown in Fig. 13. Using the B-Y signal, a high-speed comparator (IC17) switches on one of the two analog switches (IC13-c and IC13-d). R46 is used to adjust the level at which the switching will take place. The external video signal is buffered by Q5 and R43 (see Fig. 7), while Q6, R47, and C52 provide the necessary drive and impedance for the output signal.

The remaining two comparators (IC1-c and ICl-d) are used to generate 2 modeselect signals. In the external mode, C26 discharges through D18 and causes a high on pin 13 and a low on pin 2 of IC1-c. In the internal mode, D18 is reverse biased, which allows C26 to charge through R48. That reverses the outputs on pins 2 and 13 .

We're out of room for now. When we pick up again, we'll look at the microcomputer section, build the titler, and take an indepth look at the titler's software. R-E


Compact disc players are the most exciting development in audio in years.
In this article we'll show you how those devices work, and how you can

Part 2THIS MONTH, WE'LL turn our attention to the features found on a typical CD player. But first, let's finish our look at how CD players work.

## Constant linear velocity

The CD is scanned by the servo-controlled optical pickup at a constant linear velocity of 1.3 meters-per-second. To get that scan rate, the rotational speed of the disc is progressively changed from 500 rpm at start-up to 200 rpm at the outside edge of the disc. That allows the data stream of digital information to be taken from the disc at a constant rate.

The data from the disc is stored in a
memory. That memory is allowed to fill to half-capacity, then data is taken from the memory at the same rate as the specified input rate. As long as those rates are actually equal, the memory's "half-full" condition is maintained. But if incoming data is received at too fast a rate, the memory exceeds the half-full condition, and an error signal is developed. That error signal is applied to the turntable motor, and disc speed is reduced until the memory returns to the half-full condition and remains there. The opposite occurs if data is received too slowly.

With that scheme, changes in disc rotational speed have no effect on the rate at which data is removed from memory. As a
result, disc-speed changes are not detected in the reproduced sound.

## CD player features

Keep in mind that the features described here are found on most, but not all CD players. There are subtle differences in operating features that you must consider. For example, the disc compartment of some players must be closed manually, while most others are automatic. That can be a problem, particularly when you first begin CD player service. There is nothing more frustrating than troubleshooting a failure symptom when the player is supposed to work that way. Study the service literature before beginning any repairs!


FIG. 7-FRONT PANEL of the Sony CDP-30 compact disc player. This unit uses front loading.

CD players are available in both topload and front-load models (see Fig. 7). The top-load models are best suited as stand-alone components, but can also be mounted as top-rack components in an audio system. On most top-load models, the disc compartment cover or lid is opened by a control (pushbutton), but must be closed by hand. Top-load CD players generally do not require a loading motor, and are thus mechanically simpler than front-load units.
Front-load units can be used as standalone components, or can be operated at any location in an audio system rack. With horizontal front-load units, you press a front-panel button to open a drawer or tray, insert the disc, and close the drawer (manually) or tray (with a control). With the tray version (the most popular type model) the tray is operated by a loading motor. One touch of an OPEN/CLOSE button moves the tray out to a position where the disc can be inserted (or removed). Another touch of the button causes the tray to be pulled in, and positions the disc over the turntable.

With the vertical front-load units, a loading motor opens and closes a vertical door (hinged at the bottom) so that the disc can be inserted and removed. On virtually all CD players, there are circuit breakers and safety switches (interlocks) that prevent operation of drive motors, and the laser, when the disc drawers/trays/ covers are open.
The player normally plays from the beginning of the audio track to the end. If you wish, however, the random-memory programming feature allows you to preset up to 15 programs (individual selections on the disc) for playback in any order. That feature is made possible by the sys-tem-control microprocessor, which reads codes on the discs (transmitted along with the the audio) and causes the player to reproduce only the desired selections and in the desired sequence.

CD's are digitally-encoded at the be-
ginning of the program material so the player will know the length of the audio track. Also encoded at the beginning of each selection is an individual code that identifies the location of the particular selection. That system of identification (sometimes called the disc directory) allows each selection of the disc to be accessed (by the system-control microprocessor) on command.

Random-memory programming features are easy to use. Typically, you enter the number of the desired programs by first pressing a PROGRAM button, and then use buttons marked 1 and 10 . If the first program you want to hear is the third program on the disc, you press the ProGRAM button and enter the number 3 by pushing the 1 -button three times. The program indicator displays the number 3 for confirmation. Pushing the play button after the full sequence has been entered starts playback of the program sequence entered.

Self-program search lets you skip torward and backward to locate the beginning of each program on the disc. Typically, you press the FF (fast forward) control once, and the pickup advances to the beginning of the next program (and begins playing the disc at that point). When you press the REv (reverse) control once, the pickup moves back to the beginning of the current program to begin play. If you press the REv control twice, the pickup moves back to the beginning of the previous program to begin play.

In the disc scanning mode, brief samples of programming at various point in the disc are played. When activated that mode causes a brief sample of the current program to be played. Then the pickup advances to a point approximately 30 sec onds ahead (or behind) in disc play time, and another brief sample is played. That process continues as long as the disc scanning mode is engaged.

With memory stop you can mark any point on the disc for instant location with
the RECV control. In either play or pause modes, you mark the current disc location (the beginning of a favorite program, for example) by pushing the MEMORY STOP control. The point can then be returned to while in either play or pause by pushing the recev control. The pickup moves back to the memory stop location, and the player automatically goes into the pause mode. You then push the play control to start play from the memory stop point.

Play of the entire disc, or play of a random-memory programming sequence, can be repeated continuously on most CD players. For play of the entire disc, you push the REPEAT control at any point prior to or during play of the disc. After the full disc is played, the pickup returns to the beginning of the audio track and the disc is played again. Repeat play of a random memory programming sequence is generated in the same way.

Typically, there are indicators on the front panel that show such thing as total disc-playing time, elapsed playing time, number and total time of the programs entered, (via random memory programming), and possibly the track being played or the index numbers of the selections being played.

## Transit screw

Most CD players have a transit or transport screw used to hold the pickup in place when the player is moved or shipped. Without such a screw, the rotating-arm or slide can move back and forth, causing possible damage to the delicate optics. Typically, the transit screw is accessible from the bottom of the player.

Make certain to remove or loosen the transit screw before using the player, and to install the screw when transporting or shipping the player. Typically, the transit screw can be installed only when the optical pickup is in one position (the at-rest or secured position). That usually means turning on the power, shutting the tray, and then tightening the screw. When transporting the player, always install a transit screw. After transport, be sure to remove that screw (otherwise the player will not work).

## Connecting a CD player

External connections between the CD player and stereo amplifier are usually made at the back of the player. Generally the only connection needed is between the player's left and right outputs, and the corresponding inputs on the amplifier. That connection is made with a stereo patch cord terminated at both ends with RCA-type phono connectors. Sometimes the appropriate cord is supplied with the player. If not, you should have little trouble obtaining one from the usual sources.

Although the connections are very simple, certain precautions must be observed for all players. Always connect the player
output to the amplifier＇s AUX or TAPE－ play input；some newer amplifiers may also have an input labeled CD．Never con－ nect the player to the phono input．The player＇s output is about 2 volts．That volt－ age level could damage the amplifiers or speakers，and will overdrive the amplifier， if the CD player is connected via the pHo－ no inputs．Even on CD players with ad－ justable outputs，the output impedance （about 50 kilohms）is best matched to the aUX or TAPE inputs on an amplifier．As with any other audio components，always switch off the power before making or breaking connections between the player and amplifier．

## General operating notes

The following notes are a supplement to （not a substitute for）the operating and installation sections of the player－service literature．

Check to be sure that the CD is set to operate at the appropriate voltage level． Many of the newer players are for world－ wide use，and can be set to operate at 120 or 240 volts．Usually，there are special connections，or switch settings，required for dual－voltage players．Always check the service and／or operating literature for such connections or settings．

CD players produce strong magnetic fields．Do not place video or audio cas－ settes on or near the player．

If the player is brought directly from a cold to a warm location，or is placed in a very damp room，moisture may condense on the optical pickup lenses．The player will not operate properly（if at all）should the lenses become fogged．In such a case， remove the disc and leave the player turned on for about an hour to evaporate the moisture．

Although the compact discs are not del－ icate，and should last＂forever，＂the discs should be handled with some care（hold discs by their edges，don＇t touch the sur－ face of the disc，keep the disc clean，etc．）．

Never use solvents such as benzene， thinner，commercially－available cleaners， or anti－static spray intended for LP rec－ ords to clean the discs．Any of those can eat through the CD sealant，and destroy the disc．However，there are cleaners spe－ cifically designed for CD＇s；those are the only ones that should be used．

Some CD players cause interference to radio and TV reception．Any such RFI problems can be eliminated in the usual manner．

With CD players，special attention should be paid to the setting of the ampli－ fier＇s volume controls．The dynamic range of a CD player is much greater（ 90 dB or better）than any LP system，and the peaks are recorded with high fidelity． Also，you get much greater $\mathrm{S} / \mathrm{N}$ ratio（also 90 dB or greater）with a CD player．（Back－ ground noise is practically eliminated in most CD players．）If you turn up the vol－
ume while listening to a portion of the disc where no audio signals are recorded（or to very low－level audio），the speakers may be damaged when that portion of the disc with peak signals is played．Likewise，if you are listening with stereo headphones connected to the front－panel of the player， and set the player volume in an attempt to hear background noise，the effect during the audio peaks can be devastating．

## Safety precautions

In addition to all the usual precautions that should be observed during electronics servicing，the laser used in CD players requires some special consideration．As with any very intense light source，direct exposure to a laser can cause permanent eye injury or skin burns．Also，the light beam produced by the laser diode is invis－ ible（in contrast to the red light produced by the laser used in videodisc players）． Since the light is invisible，you can＇t be sure if the beam is present．

CD players are designed so that the operator has no contact with the laser beam．They are also designed so that the servicer has no contact with the laser．But that design can be defeated．Overriding safety interlocks，removing protective shields，etc．，can subject you to direct contact with the operating laser，and that is something that should be avoided．Al－ ways be on the alert for any laser warning labels during service．

Most CD player manufacturers recom－ mend some means of checking the laser diode without direct exposure to the beam．Study the service literature to find out more about it．

Here are some additional tips on check－ ing the laser．Even though the laser beam is invisible，the diffused laser beam is usually visible at the objective lens．（The lens appears to glow when the laser is on．） Also，when power is first applied to the optical circuits，the objective lens moves up and down two or three times to focus the beam on the disc．So，if you apply power and see the objective lens moving， it is reasonable to assume that the laser is on and producing enough power．

That brings up some obvious problems． First，on most players，if you open the disc compartment and gain access to the lens， you must override at least one interlock． Next，many players have some provision for shutting down the player optics if there is no disc in place，so you must also over－ ride that feature．Most important，never， never look directly into the objective lens with power applied to the player．Also， keep your eye at least 12 inches from the surface of the lens．The purpose of the lens is to focus the beam sharply onto the disc．Needless to say，the lens can also focus the beam sharply onto your eye！

Some manufacturers recommend that you not check the laser directly，but monitor the output of the photodiodes．If
that output is normal，or can be adjusted so that it is normal，the laser must be functioning properly．Consult the man－ ufacturer＇s service literature for more de－ tails．

While on the subject of the objective lens，keep in mind that the lens is a key part of a CD player．The lens surface must be clean and free of moisture．Try not to touch the lens surface，and keep the disc compartment closed，except when insert－ ing and removing the disc．If too much dirt or dust accumulates on the lens， sound quality can be degraded．Dust can be removed with an air blower such as used to remove debris from a camera lens．

As with MOS／CMOS devices，the laser diode can suffer electrostatic breakdown． Treat the laser diode as you would a MOS／ CMOS or any other electrostatically－sen－ sitive part．That includes placing a con－ ductive sheet on the workbench，using wriststraps，etc．Keep in mind that the laser diode is usually considered part of the optical system or pickup assembly， and that most player manufacturers rec－ ommend replacement of the complete as－ sembly as a package（and often ship the replacement pickup in a bag made of con－ ductive material to prevent static break－ down）．

## Test equipment and tools

The test equipment used in CD player service is basically the same as that used for servicing conventional audio equip－ ment．However，one piece of equipment bears special mention．

That is a test disc（also known as a check disc，reference disc，or alignment disc）．Those discs are available from some CD player manufacturers．A test disc is essentially a standard CD with several very useful signals recorded at the factory using very precise test equipment and sig－ nal sources．You play the test disc and note the player＇s response（or use the disc signals for adjustment）．Of course，you can use any known－good disc for a final， after－service check of the player，but such a dise will not provide you with the neces－ sary signals to perform the response tests and／or adjustments．

Some CD player manufacturers supply special tools for service；others do not． Always use the recommended tools．Keep in mind that most CD players are man－ ufactured in Japan，and as such the hard－ ware，etc．，adheres to the metric standard． When we continue this article，we will describe some of the special tools used in CD service．

Now that we have gotten some of the preliminaries out of the way，it＇s time to turn our attention to some typical CD player circuits．

## Typical circuits

Now that we know a little more about how CD players are supposed to operate


FIG. 8-THE MAJOR FUNCTIONS of a typical CD player are shown in this block diagram.


FIG. 9-IN THE FOCUS CIRCUITS, the illumination pattern detected by a four-quadrant photodiode is used to derive the focus information.
when everything is fine, it is time to take a look at the various circuits found in a unit and how they do their job. To keep things simple, we will concentrate on only one
type of unit-front-loading players that use a slide or sled optical pickup system. Figure 8 is a block diagram of a popular player of that type. Note that most of the
circuitry is contained on just a handful of IC's. Indeed, that is the case for many late-model units. But others, including most older models, feature a much lower degree of integration. Thus in the discussions of the individual circuits that follow, we will look at circuits that feature a much higher parts count; those circuits will be taken from a variety of players. Doing that will accomplish two things: First, it will familiarize us with some of the circuits we are likely to encounter in CD-player servicing; and second, it will help us learn more about how the various circuits function.

Figure 9 shows some typical focus circuits. As previously mentioned, a defraction grating is used to divide the main laser beam into three separate beams. The focus circuitry makes use of the center (brightest) beam. That center beam is focused on a a 4-quadrant photodiode. When properly focused by a cylindrical lens, the beam illuminates all four quadrants equally. If the beam is focused above the track, the lens causes the beam to illuminate the photodiodes in an elliptical pattern, with quadrants 2 and 4 receiving more light than quadrants 1 and 3 . If the focal point is below the track, the photodiodes are still illuminated in an ellip-


THIS CD PLAYER features remote－control convenience and 19－step program capacity．


FIG．10－THIS RADIAL－TRACKING CIRCUIT is used to make sure the laser beam correctly follows the track of pits and flats．
tical pattern，but quadrants 1 and 3 receive the greater amount of light．The focus circuits move the lens up or down to main－ tain good focus．

Let＇s see how the focus circuits work． The outputs from the two pairs of pho－ todiodes are applied to the inverting in－ puts of two comparators．The outputs of the first pair of comparators are applied to the inputs of a second pair of com－ parators．One of those is for signal－detec－ tion；its output is used by the signal detection circuits（those will be discussed shortly）．The other is the focus com－ parator．When in focus，the beam illumi－ nates all quadrants equally，causing the inputs to the focus comparator to be equal．As a result，the output of the com－ parator is zero（or，more accurately，a fixed DC reference）．

If the beam goes out of focus，either the even $(2,4)$ or the odd $(1,3)$ quadrants re－ ceive more light．The result is a time－ varying positive or negative voltage devia－ tion from the reference level at the output of the focus comparator．That deviation signal is called the Focus ERror signal，or FER．The FER signal is amplified by IC04 and IC100，and is applied through a FET－ type analog switch to IC150．The FET is turned on by a high Fine－Focus－OK or FFOK signal．The FFOK signal is produced by an AND gate in IC01．The AND gate produces a high FFOK output only when all three inputs（from the focus and signal detection comparators，and from the laser－monitor diode）are high．The output from IC150 is amplified and used to con－ trol the positioning of the cylindrical lens．

When a disc is first loaded，a rough
focus is produced by the Focus－ Up／－Down，or Fud signal from the system microprocessor．The fud signal consists of two 1 －second squarewave pulses that are applied to IC150 through integrator R112 and C115．That causes the lens to move up and down two times．The first time the lens starts to move，the laser turns on．The lens stops when an FOK signal （derived from the FFOK signal）is applied through IC250 to IC150．If no disc is detected（no reflection）the first time，the lens moves toward the disc a second time （on the second fud pulse）．The system microprocessor shuts the system down if no disc is detected a second time．

## Radial tracking

Figure 10 shows show some typical ra－ dial tracking circuits．If everything is
working normally, the two tracking, or first-order beams, precede and follow the main beam as it sweeps along the track. Also, the tracking beams are slightly offset from the center beam. To be more specific, half of each tracking beam is focused on the pit, while the other half is focused on the reflective area between the. pits. Each beam is detected by its own photodiode (TRA or TRC).

The output of photodiode TRA is applied to an op-amp that is configured as a current-to-voltage converter. The converter gain is set by the tracking servo offset control. The output of that converter is applied to one input of a comparator. The output of TRC is also connected to the comparator, but via a $30-\mu$ s delay (CP03 and CP04). The time delay is necessary because the tracking circuits require that the TRC and TRA beams sweep the same point on the disc and that the photodiode outputs that are generated as a result must reach the input of the comparator simultaneously.

With proper tracking, the outputs of TRA and TRC are equal, and the comparator output is zero. If the main beam drifts, the TRA and TRC outputs are different, and the comparator output varies above or below 0 volts. The comparator output is called the Tracking ERror, or TER; it is amplified and applied to IC183 through an adjustable voltage divider. IC183 provides a variable gain function. The amount of gain is varied by switching resistors into and out of the circuit. That switching is under control of the system microprocessor, via IC181. During the initial read of the disc, the gain is set to a predetermined level; that gain remains constant as long as the unit is in the play mode. When the player enters the jump or search modes, the gain is changed to a different predetermined value, called the TYP gain (about 0 dB ). In most late-model CD players, the system microprocessor is programmed to compensate for variable signal levels from the disc.

From IC183, the TER signal is gated by Q150, and applied to the radial tracking coil through IC151 and Q151 and Q152. As shown in Fig. 10, FET Q150 is gated on by signals from four different sources. Four diodes (D150, D151, D152, D161) must all be reversed-biased by those sources to gate Q150 on. One source, $\overline{\text { TSW }}$ is generated by the system microprocessor. We have already discussed FOK. The other two sources, TOK (Tracking $O K$ ) and error (damage) detection will be discussed later in this article.

Figure 11 shows the circuits used to detect and decode the information recorded on the disc. When a pit is present, the main beam is absorbed and no reflection occurs. When a pit is not present (a flat), the beam reflects back to the 4 -quadrant diode. The diode outputs are processed by IC01 as previously discussed and then am-


FIG. 11-THIS CIRCUIT is used to detect and decode the information stored on the disc.


FIG. 12-THE SIGNAL-PROCESSING circuitry in a CD player can not be altered or adjusted.
plified by IC03. The output from that IC is called the EFM signal; it is sometimes also called the HF signal, the RF signal, or the eye pattern. In the EFM signal, sinewaves are used to represent digital information.

The efm signal is next fed to an AGC amplifier and then to an equalization network (actually a transverse filter) composed of Q03, Q04, Q05, CP01, CP02, and IC05). The transverse filter ensures that the high-frequency components of the EFM signal have the same amplitude as the low frequency components. The filter output is applied to a comparator called the EFM comparator (more on that in a moment) and then fed to the signal processing circuits (see Fig. 12). In the signal processing circuits, the signal is detected by the data strobe and then passed to the controller. The controller develops two squarewave signals-the Data Slice Level Control, or DSLC, and the PREFerence pulse, or Pref. Those signals are fed back to IC01, where they are combined and used to form the threshold voltage for the EFM comparator. That comparator shapes the EFM signal into a squarewave. The resulting signal is called the EFMS signal and is the EFM signal in true digital form.

Next, let's look at the signal processing circuitry a little more closely. Those circuits, which are shown in Fig. 12, process the digital signal so that it can be applied
to the $\mathrm{D} / \mathrm{A}$ converter (where the digital information is converted to audio).

Typically, all of the signal processing circuitry is contained within one to four IC's; the following is a brief description of the major functions performed within those IC's. Keep in mind that the functions can not be altered, adjusted, or even checked, since the circuits are not accessible. At best, you can check inputs and outputs at the IC pins during troubleshooting. (We will show you how that is done later on in this article.)

The major function of the data strobe is to detect and generate sync pulses derived from the efms signal. The main function of the error detection and correction circuit is to demodulate the EFM signal, and restore the signal to its original 16 -bit form (using processes known as Cl and C2 decoding). The error detection and correction circuit also has the task of processing control and data information. The controller governs many of the digital circuit functions, but its main function is to address memory locations in the RAM. The controller also has the responsibility of outputting the 16 -bit digital audio signal to the $\mathrm{D} / \mathrm{A}$ converter.

That's all we have room for this month. Next time, we'll finish up our look at CDplayer circuits. After that, we 'll begin to examine the mechanical systems in those devices.

R-E

## Switching Power Supplies



You too can design switching power supplies! In this article we present you with
the theoretical background and several experimental circuits.
VAUGHN D. MARTIN

SWITCHING POWER-SUPPLY DESIGN HAS long been regarded as something of a black art practiced only by magicians and wizards. However, recent innovations in electronics have provided us with components that simplify such designs considerably: high-density IC's, high-permeability ferrite inductor cores, and highpower, high-speed switching transistors. We will review fundamental power-supply operation, discuss underlying switchingsupply theory, and then present theoretical and practical information on how to design switching supplies using two popular LSI IC's. To simplify your design tasks, we include complete schematics, parts lists and PC artwork for several functional switching power supplies.

## Regulator basics

Any regulated DC power supplyswitching or linear-is governed by a feedback mechanism that senses changes in the output voltage and generates a control signal to compensate for those changes. As shown in Fig. 1, the basic linear voltage regulator has four major components:

- 1) A voltage reference
- 2) An error amplifier
- 3) A feedback mechanism
- 4) A control element


FIG. 1-THE FOUR BASIC COMPONENTS of a linear regulator are shown here. The control element is a transistor operated in the commonbase mode.

The control element is usually a transistor operated in the common-base mode. Its base current determines the current flowing through its emitter-collector circuit, and thus, indirectly, the output voltage of the regulator. That base current is supplied by the error amplifier, usually an op-amp. The op-amp compares an internal reference voltage to a portion of the control element's output voltage. The opamp's output is proportional to the difference between those voltages, and that is what controls the base current, and hence the collector current, of the control element.

Not shown in Fig. 1 is additional cir-
cuitry that protects the regulator from over-current and over-heat conditions. Like the error amplifier, the protection circuit samples a portion of the output voltage; the output of the protection circuit would be connected to the base of the series pass transistor, and would force the transistor into cutoff when it sensed an overload condition.

In a linear regulator the series-pass transistor operates in the linear region; that is, current flows through the transistor continuously as long as the input voltage exceeds the minimum necessary to keep the regulator working. (Output may also be cut off, as we discussed above, because of thermal or over-current shutdown.) But a regulator does not necessarily have to operate in such a fashion. It is possible for it to switch rapidly between saturation and cutoff, remaining in the linear region only for the short period of time required to perform the switching action. That is the basis of the switching regulator.

## Fight or switch?

Although it might seem odd to operate a power supply in such a manner, with proper circuit design much greater efficiency can be obtained than with linear operation. Output voltage in the switch-


FIG. 2-SWITCHING REGULATORS differ from linear regulators by the addition of control logic and an oscillator; the latter allow control of frequency, duty cycle or both, and those parameters determine the regulator's final output voltage.


FIG. 3-THREE BASIC switching regulators are shown here. Step-down, step-up, and inverting regulators are shown at $a, b$, and $c$, respectively.
ing mode is controlled by varying the duty cycle, the frequency, or both, of the signal that turns the transistor on and off. As shown in Fig. 2, the switching regulator is conceptually similar to the linear regulator. The blocks labeled "control logic" and "oscillator" are what allow the duty cycle or frequency to be controlled.

Switching supplies allow greater efficiency than linear supplies, but that is not their only advantage. As shown in Fig. 3, by rearranging a few components, the same basic circuit can be made to step the input voltage down (a), to step it up (b), or to invert its polarity (c). And inversion may happen simultaneously with the stepup and step-down functions.

In each of the circuits shown in Fig. 3, S 1 is represented as a mechanical switch, but in the circuits we will be concerned with, it is actually an electronic multi-
vibrator whose duty cycle, frequency, or both may be varied by connecting appropriate components. In the past (that is, before the invention of high-voltage, high-power semiconductors, and LSI IC's), that switching action was performed by rotating machinery.

In Fig. 3- $a$, the coil-capacitor network may be thought of as an energy reservoir that is fed by the power source each time the switch is closed. The diode serves two functions. First, it provides a discharge path for the coil so that when the switch opens, there will be no arcing. Second, it also provides a path through which the current that is stored in the coil and the capacitor may flow while the switch is open. That helps smooth the rough sawtooth that is the normal product of such switching into a fairly "flat" triangle wave. Thus, less noise is generated, and the average DC value of the output voltage is higher than it would be without the return diode. That type of circuit is known as a series switching supply, because the switching device is in series with the input voltage source.

By contrast, the step-up circuit in Fig. $3-b$ is called a shunt switching supply, because the switching device is in parallel with the input voltage. When the switch opens, energy stored in the magnetic field of the coil is released as a large spike that serves to charge capacitor $\mathrm{C}_{\mathrm{O}}$. Diode D1 here serves to prevent the capacitor from discharging through the switch while it is closed.

The inverter scheme in Fig. 3-c combines elements of both the step-up and step-down circuits. Again the switching
device is connected in series with the voltage source; the coil again dumps its stored energy to the capacitor when the switch is opened. Here the diode ensures that the discharged energy flows "backward" through the load.

Modern switching regulator IC's usually contain the voltage reference, switching mechanism, protection circuitry and a low- to medium-current series-pass element. The circuit designer usually supplies discrete components to set the oscillating frequency of the switching supply, an appropriate inductor, and additional series-pass elements to increase the circuit's current output.

The three most popular switching voltage regulator IC's today are probably Signetics' NE5560N, National's LM3524 and Fairchild's $\mu \mathrm{A} 78$ S40. Robert Frostholm explained the Signetics IC in the February 1980 issue of Radio-Electronics, so for the remainder of this article, we will concentrate on the latter IC's.

## The LM3524

A Pulse Width Modulator (PWM) is a circuit that varies the duty cycle of a pulse train while keeping its frequency constant. The LM3524 has a built-in PWM that is used to vary the "on" time of the series-pass elements. As shown in Fig. 4, the output of the oscillator drives a flipflop that in turn drives two NOR gates. Note that they are driven out of phase by the flip-flop's Q and $\overline{\mathrm{Q}}$ outputs. Also note that the IC's output is disabled by the comparator whenever its output goes high. That will happen whenever the error amplifier determines that the internal ref-


FIG. 4-NATIONAL SEMICONDUCTOR'S LM3524 can tolerate a maximum input of forty volts, and can provide as much as 100 ma of output current.
erence voltage equals（or exceeds）the sampled portion of the output voltage． That will also happen whenever the cur－ rent－limit amplifier senses an overload condition．

The LM3524 has a built in five－volt reference capable of supplying 50 mA to external circuitry．If $\mathrm{V}_{\text {IN }}$ applied to the regulator is less than eight volts，then the five－volt internal reference will not work properly，so pin 15 should be tied to pin 16．If more than six volts will be applied to the IC in that configuration，a pre－reg－ ulator should be used as shown in Fig． 5.

The frequency of the IC＇s internal os－ cillator is determined by external resistor $\mathrm{R}_{\mathrm{T}}$ ，and external capacitor $\mathrm{C}_{\mathrm{T}}$ ．The re－ sistor should have a value between 1.8 K and 100 K ；the capacitor should have of a value between $0.001 \mu \mathrm{~F}$ to $0.1 \mu \mathrm{~F}$ ．A nomograph is presented in Fig． 6 that helps you select values for $R_{T}$ and $C_{T}$ ， given a desired period of oscillation．

The error amplifier is a transconduc－ tance differential－input type with a nomi－ nal gain of about 80 dB ；that gain may be set either by feedback or output loading， and loading does not necessarily have to be purely resistive．The output of the am－ plifier，which is also the input to the PWM，has an impedance of about 5 megohms，and that enables it to be over－ ridden by a DC voltage，thereby forcing a desired duty cycle to appear at the output． The amplifier＇s inputs have a common－ mode range of 1.8 to 3.4 volts，and the IC＇s on－chip regulator is typically used to bias the inputs to a value within that range．
The LM3524 performs its current limit－


FIG．5－PREREGULATION IS NECESSARY when using the LM3524 with input potentials between 5.5 and 8.0 volts．


FIG．6－TIMING COMPONENTS versus os－ cillator period are shown in this nomograph．


FIG．7－A COMPLETE FIVE－VOLT，ONE－AMP SWITCHING REGULATOR may be built using the LM3524． Transistor Q1 should have a proper heatsink，such as the Staver No．V5－1．

TABLE 1－LM3524 SWITCHING REGULATOR CHARACTERISTICS

| Parameter | Conditions | Characteristics |
| :--- | :--- | :--- |
| Output Voltage | $\mathrm{V}_{\mathbb{I N}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=1 \mathrm{~A}$ | 5 V |
| Switching Frequency | $\mathrm{V}_{\mathbb{I N}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=1 \mathrm{~A}$ | 20 kHz |
| Short Circuit <br> Current Limit | $\mathrm{V}_{\mathbb{I N}}=10 \mathrm{~V}$ | 1.3 A |
| Load Regulation | $\mathrm{V}_{\mathbb{I N}}=10 \mathrm{~V}$, <br> $\mathrm{I}_{\mathrm{O}}=0.2-1 \mathrm{~A}$ <br> Line Regulation | $\Delta \mathrm{V}_{\mathbb{I N}}=10-20 \mathrm{~V}$, <br> $\mathrm{I}_{\mathrm{O}}=1 \mathrm{~A}$ <br> Efficiency |
| Output Ripple | 3 mV |  |

ing by decreasing the width of its output pulses．The output duty cycle drops to about $25 \%$ when a potential of 200 mV is present between terminals +CL and -CL ； 210 mV will reduce the duty cycle to $0 \%$ ． The total potential difference between those two terminals must not exceed -0.7 to +1.0 volt．

The output stage of the LM3524 con－ sists of two NPN transistors driven $180^{\circ}$ out of phase with each other by the flip－ flop．Each transistor is capable of supply－
ing a current of 100 mA ．

## A practical regulator

A complete step－down switching volt－ age regulator is shown in Fig．7．For prop－ er operation，the input voltage should exceed eight volts．Transistor Q1 acts as the series control element；with proper heatsinking，it can provide a current of about 1 amp ．Resistors R4 and R5 divide the IC＇s five－volt reference in half in order to bias the error amplifier＇s non－inverting

## PARTS LIST-LM3524 CIRCUIT

All resistors $1 / 4$ watt, $5 \%$
R1, R2, R4, R5- 5000 ohms
R3- 0.15 ohms see text
R6-6500 ohms
R7- 30,000 ohms
R8- 510 ohms
R9-420 ohms
R10- 560 ohms
Capacitors
C1, C2-.01, ceramic disk
C3, C5-1, ceramic disk
C4- $10 \mu \mathrm{~F}, 35$ volts, electrolytic
C6- $-500 \mu \mathrm{~F}, 10$ volts, electrolytic
Semiconductors
IC1-LM3524, switching regulator
D1-MR850
Q1-BD344
Q2-2N5023

## Other Components

$\mathrm{L} 1-500 \mu \mathrm{H}$
Note: L1 is 40 turns of 22-gauge wire wound and evenly spaced on a Ferroxcube No. 502T300 toroid core, available from Permag Corporation , 400 Karin Lane, Hicksville, NY 11801; 516-822-3311.

## PARTS LIST- $\mu$ A78S40

STEP-DOWN CIRCUIT
All resistors $1 / 4$ watt, $5 \%$ unless otherwise noted
R1-1 ohm, 2 watts
R2-100 ohm trimmer potentiometer
R3- 1300 ohms
R4-50,000 ohm trimmer potentiometer
R5-R7-not used

## Capacitors

C1- $100 \mu \mathrm{~F}, 35$ volts, electrolytic C2-.01, ceramic disk
C3, C4-1 $\mu$ F, 50 volts, tantalum
C5- $470 \mu \mathrm{~F}, 35$ volts, electrolytic

## Semiconductors

IC1- $\mu \mathrm{A} 78 \mathrm{~S} 40$, switching regulator
D1-1N4001

## Other Components

J1, J2, J3-Banana jacks
L1-300 $\mu \mathrm{H}$, Ferrite Potentiometer Core (Indiana General F1153-1-06-620); PC mounting bracket (Indiana General B662), and PC bobbin (Indiana General B657-12). See text.

## PARTS LIST- $\mu$ A78S40 STEP-UP CIRCUIT

Note-unless otherwise specified, all components are identical to components specified in the $\mu \mathrm{A} 78 \mathrm{~S} 40$ stepdown circuit.

## R5-180 ohms, $1 / 4$ watt, $5 \%$

D1-not used

## PARTS LIST- $\mu$ A78S40 INVERTER CIRCUIT

Note-unless otherwise specified, all components are identical to components specified in the $\mu \mathrm{A} 78 \mathrm{~S} 40$ stepdown circuit.

## All resistors $1 / 4$ watt, $5 \%$

R3- 1000 ohms
R6-680 ohms
R7-100 ohms
Q1-2N6051


FIG. 8.-COMPONENT PLACEMENT DIAGRAM for the switching supply shown in Fig. 7.


FIG. 9-FAIRCHILD'S $\mu$ A78S40 is very similar to the LM3524. However, the Fairchild part also contains an uncommitted diode and an uncommitted op-amp that is similar to the popular 741.
input within its acceptable range. Each of the LM3524's output transistors is on for about $45 \%$ of the time, and they have been wired in parallel. That allows a duty cycle of as much as $90 \%$, thus resulting in a lower input-voltage requirement, and greater overall efficiency. The output voltage is given by

$$
V_{O}=V_{N 1}(1+R 1 / R 2)
$$

where $\mathrm{V}_{\mathrm{NI}}$ is the voltage present at the error amplifier's non-inverting input (pin 2). The circuit's current limit is established by the value of R3 and is stated as: $\mathrm{I}_{\mathrm{L}}=0.2 \mathrm{~V} / \mathrm{R} 3$. In that case, $\mathrm{I}_{\mathrm{L}}=0.2 \mathrm{~V} /$ $0.15 \Omega=1.3 \mathrm{~A}$. Fabricate R 3 by winding 1.45 feet of 30 -gauge wire around a 100 ohm, $1 / 2$-watt resistor.
Table 1 shows the characteristics of the power supply; and full-size artwork for a printed-circuit board may be found in the "PC Service" section of this magazine. A component-placement diagram is shown in Fig. 8.

## The $\mu$ A78S40

Whereas the LM3524 is a pulse width modulator, Fairchild's $\mu \mathrm{A} 78 \mathrm{~S} 40$ may be considered a variable pulse-width, variable frequency modulator. It is capable of operating from a supply as low as 2.4 volts, and it has a standby mode that draws only 2.4 mA at five volts. That is
rather remarkable when you consider that the IC can handle 40 volts at 1.5 amps . Anyway, its low power requirements make the $\mu \mathrm{A} 78 \mathrm{~S} 40$ ideal for batterypowered operation.

A block diagram of the $\mu \mathrm{A} 78 \mathrm{~S} 40$ is shown in Fig. 9. The current-controlled oscillator (CCO) is probably the most important part of that IC. It's purpose is to generate the gating signal that turns output transistors Q1 and Q2 off and on. A single external capacitor controls the frequency of the CCO, which may vary from 100 Hz to 100 kHz . The duty cycle of the CCO is set internally to a ratio of 6:1, but that may be varied by manipulating the current-limiting circuitry attached to pin 14 (I $\mathrm{I}_{\text {SENSE }}$ ).

The $\mu \mathrm{A} 78 \mathrm{~S} 40$ 's manner of output-voltage control is somewhat more complex than that of the LM3524. The and gate shown in the block diagram of Fig. 9 is fed by the CCO and a comparator. The comparator, as you have probably guessed, functions as the error amplifier we discussed above. The comparator is a highgain differential type with a commonmode input range extending from ground to within 1.5 volts of $\mathrm{V}_{\mathrm{CC}}$. It functions to inhibit the $\mu$ A78S40's "on" cycles. The comparator can inhibit several "on" cycles, one "on" cycle or any portion of an
＂on＂cycle．After such a cycle has begun， the comparator cannot inhibit output until the beginning of the next＂on＂cycle．

The comparator samples a portion of the output voltage and compares it to the on－chip reference voltage．As long as the sampled portion of the output voltage is less than or equal to the reference voltage， the comparator remains high，so the flip－ flop may be set and reset by the normal operation of the oscillator．If the output voltage exceeds the reference voltage，the comparator goes low．In that state，the AND gate cannot turn on，the flip－flop can－ not be set，so the transistors will remain off．

The flip－flop is always reset during the negative－going ramp of the oscillator． And once the flip－flop is set，it will remain set until the negative－going ramp of the oscillator cycle．However，the current－


FIG．10－KEY POINTS IN A $\mu$ A 78 S 40 circuit are shown in these scope photos．In both，the upper trace represents the output ripple，and the lower trace represents the voltage across the timing capacitor．
limiting circuitry，which is temperature compensated，can speed up the reset ac－ tion．The limiting circuit senses the output transistors＇current across an external re－ sistor．Current limiting is initiated when a 300 mV potential appears between pin 13 $\left(\mathrm{V}_{\mathrm{CC}}\right)$ and pin 14 （ $\left.\mathrm{I}_{\text {SENSE }}\right)$ ．

Normally there is a steady current flow of about $50 \mu \mathrm{~A}$ into the timing capacitor， but when an overload condition is de－ tected，that current is boosted to about 1 mA ，and that causes the positive－going oscillator threshold to be attained more rapidly than it would without the addi－ tional current．Operation under such cir－


FIG． $11-$ THE $\mu$ A 78 S 40 USED IN A STEP－DOWN CONFIGURATION．Given a +25 －volt input，the circuit provides +10 volts at 500 mA ．


FIG．12－THE $\mu$ A78S40 USED IN A STEP－UP CONFIGURATION．Given a +10 －volt input，the circuit provides 25 volts at 100 mA ．
cumstances then causes a very short＂on＂ time followed by the normal＂off＂time that is provided by the negative－going ramp of the oscillator．

The $\mu \mathrm{A} 78 \mathrm{~S} 40$＇s voltage reference is de－ rived from an ultra－temperature－stable band－gap reference．A band－gap refer－ ence is a combination of several semicon－ ductors that have both positive and negative temperature coefficients；they are arranged so as to exactly offset or cancel one another，and that results in a temperature coefficient of almost zero．In practical terms，that gives us a rock－ steady reference voltage that is almost completely insensitive to temperature changes．The reference is capable of providing as much as 10 mA of current without additional circuitry．

Output transistors Q1 and Q2 are con－ figured as a Darlington pair．That mode of DC coupling gives a very high gain that，
as you recall，is the product of the betas of both transistors．For example，if each transistor has a beta of 250 ，then the over－ all gain will be beta ${ }^{2}$ ，or $250 \times 250=$ 62,500 ．The collectors of both transistors are brought from the IC to the outside world separately，and their emitters are brought out together．They can handle as much as 1.5 A peak current at a collector－ emitter potential of 40 volts，but still have excellent switching times ranging from 300 to 500 nanoseconds．

One special feature of this versatile IC is the inclusion of an uncommitted opera－ tional amplifier．It is a bipolar op－amp quite similar to the popular 741 type． However，its output capabilities have been boosted so that it can source 150 mA and sink 35 mA ．Another improvement is that its common－mode input range includes ground．

The last major component of the

| TABLE 2-JUMPER CONFIGURATION |  |  |  |
| :---: | :---: | :---: | :---: |
| Jumper | Down | Up | Invert |
| 1 | $\checkmark$ |  |  |
| 2 |  |  |  |
| 3 | $\checkmark$ |  |  |
| 4 | $\checkmark$ |  |  |
| 5 |  | $\checkmark$ |  |
| 6 | $\checkmark$ | $\checkmark$ |  |
| 7 |  |  |  |
| 8 | , | $\checkmark$ |  |
| 9 | , |  |  |
| 10 |  |  |  |
| 11 |  | $\checkmark$ |  |
| 12 |  | $\checkmark$ |  |
| 13 |  | $\checkmark$ | $\checkmark$ |
| 14 |  | $\checkmark$ |  |
| 15 |  | $\checkmark$ |  |
| 16 |  | $\checkmark$ |  |
| 17 |  | $\gamma$ |  |
| 18 |  | $\checkmark$ |  |
| 19 |  |  | $\checkmark$ |
| 20 |  |  | $\downarrow$ |
| 21 |  |  | , |
| 22 |  |  | $\checkmark$ |
| 23 |  |  | $\checkmark$ |
| 24 |  |  | $\checkmark$ |
| 25 |  |  | $\checkmark$ |
| 26 |  |  | $\checkmark$ |
| 27 |  |  | , |
| 28 |  |  | $\checkmark$ |
| 29 |  |  | $\checkmark$ |
| 30 |  |  | f |
| 31 | $\checkmark$ | $\checkmark$ | , |
| 32 | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| 33 | $\checkmark$ | $\checkmark$ | $\checkmark$ |

$\mu \mathrm{A} 78 \mathrm{~S} 40$ is the uncommitted diode available at pins 1 and 2. That diode can be used as desired, provided that some precautions are followed. Since the IC's substrate is normally at ground potential, the cathode of the diode must not be subjected to a negative voltage; that precludes its use in step-down and inverting configurations. The diode's internal construction results in a current flow from the anode to the substrate amounting to about $10 \%$ of the normal anode/cathode current. Because of the high voltage usually present between the anode and ground in stepup regulators, power dissipation will probably limit the diode's usefulness in such applications. The diode is probably most useful protecting the output transistors from reverse-polarity voltage spikes.

The scope photos shown in Fig. 10-a and $-b$ show the waveforms present at several key points in a step-down regulator circuit built from a $\mu \mathrm{A} 78 \mathrm{~S} 40$. In both cases the upper trace represents the output ripple and the lower trace represents the voltage across $\mathrm{C}_{\mathrm{T}}$. The photo in Fig. 10-a was taken at an output current of 22 mA , and the photo in Fig. 10-b was taken at 495 mA . In both photos the rising edge of the $\mathrm{C}_{\mathrm{T}}$ wave represents "on" time and the falling edge represents "off" time. Notice


FIG. 13 -THE $\mu$ A78S40 USED IN AN INVERTING CONFIGURATION. Given a +12 -volt input, the circuit provides -15 volts at 100 mA .


FIG. 14-COMPONENT PLACEMENT DIAGRAM for the switching circuits shown in Fig. 11-Fig. 13. See TABLE-2 for information on how to install the jumpers for the different circuit configurations.
that as the output current increases, the "off" time decreases to a minimum. Note also that the switching frequency increases with increased output current. In a PWM circuit, that frequency would remain constant; only the ratio of "on" time to the total period of the wave would change.

## Sample circuits

Now we present circuits you can use for designing a step-down, a step-up or an
inverting switching supply. The same PC board can be used for any one of those circuits; installation of various jumpers allows selection of the appropriate mode.
The step-down switching supply shown in Fig. 11 allows you to obtain 10 volts at 500 mA from a nominal 25 -volt input, which may actually vary between 14 and 35 volts. Efficiency is typically $65 \%$ or better.
The step-up switching supply shown in continued on page 110

## PC Service

One of the most difficult tasks in build－ ing any construction project featured in Radio－Electronics is making the PC board using just the foil pattern provided with the article．Well，we＇re doing some－ thing about it．

We＇ve moved all the foil patterns to this new section，where they＇re printed by themselves，full sized，with nothing on the back side of the page．What that means for you is that the printed page can be used directly to produce PC boards！
In order to produce a board directly from the magazine page，remove the page and carefully inspect it under a strong light and／or on a light table．Look for breaks in the traces，bridges between traces，and， in general，all the kinds of things you look for in the final etched board．You can clean up the published artwork the same way
you clean up you own artwork．Drafting tape and graphic aids can fix incomplete traces and doughnuts，and you can use a hobby knife to get rid of bridges and dirt．

An optional step，once you＇re satisfied that the artwork is clean，is to take a little bit of mineral oil and carefully wipe it across the back of the artwork．That helps make the paper transluscent．Don＇t get any oil on the front side of the paper（the side with the pattern）because you＇ll con－ taminate the sensitized surface of the copper blank．After the oil has＂dried＂a bit－patting with a paper towel will help speed up the process－place the pattern front side down on the sensitized copper blank，and make the exposure．You＇ll probably have to use a longer exposure time than you are used to．

We can＇t tell you exactly how long an
exposure time you will need because we don＇t know what kind of light source you use．As a starting point，figure that there＇s a 50 percent increase in exposure time over lithographic film．But you＇ll have to experiment to find the best method to use with your chemicals．And once you find it， stick with it．Don＇t forget the＂three C＇s＂of making PC boards－care，cleanliness， and consistency．

Finally，we would like to hear how you make out using our method．Write and tell us of your successes，and failures，and what techniques work best for you．Ad－ dress your letters to：

## Radio－Electronics

Department PCB
200 Park Avenue South
New York，NY 10003

## PC Service



SOLDER SIDE of the $\mu \mathrm{A} 78 \mathrm{~S} 40$-based switching power-supply

## PC Service



SOLDER SIDE of the LM3524 5－volt switch power－supply PC－board．For more on switching power supplies，see the story on page 77.

## PC Service



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# Robotics 

## Ultrasonic rangers and stepper motors



MARK J. ROBILLARD
ROBOTICS EDITOR

WE'VE DISCUSSED ULTRASONIC-RANGing and stepper-motor circuits the past several months; this month we'll mention several other ways to implement such circuits, and then go on to discuss how they may be used to map and navigate an arbitrary area.

We discussed the operation of a typical ultrasonic ranger in detail last month, but, to summarize briefly, a timing pulse is applied to a circuit that generates a burst of ultrasonic waves. A timing circuit begins counting at the same time, and, after an echo has been received, the time is divided by two, and the distance is calculated, based on the speed of sound in air. Normally you connect an ultrasonic ranging system to a microcomputer that is used to initiate ranging, do the calculations, and control the robot's stepper motor (or motors).

Last month we discussed Polaroid's ultrasonic ranging kit; another such kit is made by Texas Instruments. The SN28827 is available from the Micromint ( 25 Terrace Drive, Vernon, CT 06066) with a transducer for about \$60. I recommend using a kit rather than a home-made system. That way you can get to the really difficult prob-lems-like mapping and navigat-ing-without spending time debugging standard circuits.

A stepper motor allows you to rotate the ranging circuitry (or the whole robot, if necessary) a precise amount, and then make, and record, a distance measurement. Steppers require a little more power than regular DC motors, but their position is much easier to control. The circuit in Fig. 1 shows how an LSI IC can be used to drive


FIG. 1
a four-phase stepper motor. A direction signal is applied to the mODE input, and then the clock input is pulsed. The reset input should normally be held high. Resistor R2 determines the base current of the IC's driver transistors, and it must be selected in accordance with the current requirements of the motor that will be used.

The SAA1027 used in that circuit is made by Signetics; Sprague also manufactures stepper-motor drivers, the UCN-4202A and UCN-4203A, among others. Moreover, there are ways of controlling steppers without custom IC's. Let us know if you're interested; we could devote a column to that subject.

## Robot mapping

With a stepper motor and an ultrasonic ranger we can make a rough map of an area by rotating the ultrasonic transducer while taking distance readings at periodic intervals. Using a computer to record those readings will allow a map of distances to be compiled. With a simple system
such as that we're discussing, the resulting map barely resembles the space mapped; in early experiments, I could hardly recognize the area I had just mapped. The reasons for the discrepancies are many.

For example, assume we want our robot to navigate a room with a table and an open doorway. When we see a table, we have no trouble distinguishing it from a doorway. But the robot would see the legs as spikes and the in-between areas as caverns. The robot might try-mis-takenly-to pass under those legs. As robot programmers, it is our job to interpret what the map actually represents, so that we could, for example, find the doorway without running into the table.

But let's assume that a trial-anderror method is necessary to move the robot out the doorway. At any arbitrary location, three readings might be taken: straight ahead, $90^{\circ}$ right, and $90^{\circ}$ left. Those readings will tell you whether it is safe to move in one of those directions. If the straight-ahead reading reveals that there is an obstacle within a
continued on page 113

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# Antioue <br> <br> Radios 

 <br> <br> Radios}

## Phonographs and antique radios.

PHONOGRAPHS ARE NOT NORMALLY considered relevant to the subject of antique radio, but many antiques have built-in phonos, or at least some provision for connecting an external phono. To the best of my knowledge, built-in phonos and phono adapters became common in the mid 1920's. And most phonos had their own volume controls mounted on the base, or even on the pick-up arm. As you can imagine, a phono with the volume control on the arm required a deft touch to adjust the volume while a record was playing!

The addition of an extra jack, switch, or both, to accommodate a phono can cause many service headaches. Many of those jacks and switches were never used; they were included in early radios mainly to impress the buyer.

There were several common methods of coupling the phonograph signal into the radio; phonographs often required their own volume controls because the signal was fed "downstream" from the radio's own volume control. For example, some sets had an arrangement whereby the detector tube could be used as an amplifier; and, as we have discussed in previous columns, volume controls were frequently part of the antenna/coil circuit. There was also an arrangement whereby an adapter could be plugged into the detector-tube socket to accommodate the phono's output. Some radios had no radio-phono switch, so the dial had to be tuned to an empty spot in the broadcast band in order to hear the output of the phonograph.


FIG. 1

## The antique radio of the month

Our project this month, shown in Fig. 1, lacked any and all identifying marks. I assume that it is a Silvertone because many of its very old tubes are Silvertones. The most unusual feature of that radio/ phono "combo" is the record changer under its lid. It's not just a record player, but a complete changer; and that is unusual for that era.
The turntable has a speed control, and it plays records of two sizes, but it is not a two- (or three-) speed changer in the modern sense of the term. There was a fad,
in the post-WWII era, to replace those obsolete phonos with a complete three-speed changer. If your antique has one of the latter, you can bet that it's not original, no more so than if you find a stereo cartridge in the arm. If you want to keep your radio/phono in nearoriginal condition, I wouldn't replace the phono with a modern three-speed, even if the original can't be repaired. It's better to leave it inoperative than to destroy the antique value of the entire set.

I was lucky with the receiver portion of that set; it came in good operating condition. The chassis has seven tubes, including the rectifier, a modern version of the workhorse 80. The chassis also has a four-gang tuning capacitor with a trimmer on top of each gang. The RF and detector tubes, as well as the tuner, are all well shielded, a very common feature of antique radios. It has a radio/phono switch that cuts off RF at the detector and substitutes the phono signal.

## Troubleshooting

One nice thing about radio/ phono combinations is that the components can be used to troubleshoot each other. For example, if the radio doesn't operate, but the phono does, you know that the audio amplifier works, and so does the speaker. Therefore, something must be wrong with the RF section.

On the other hand, if the radio operates but the phono doesn't, the trouble must be somewhere between the phono's needle (now called a stylus) and the input jack. The needle will always pick up something no matter how badly it

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## -

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HiTech's Sam 3001

## ON THE COVER

It's that time of year once again, and the message on our cover display was generated by Fontrix ${ }^{\text {TM }}$. We'll be covering that, and other graphics software in an upcoming issue. The computer you see passing on our wishes for a happy holiday is the SAM 3001 from HiTech International. It's one of the first of the AT-compatibles to hit the market. For a review on the machine, turn to page 4.

## COMING NEXT MONTH

We've got a great story lined up on everything you want or need to know about hard disks. Then we're going to begin a two-part article on how to use your computer to help design loudspeaker enclosures. And to round out a great issue, you'll get an in-depth article on the ubiquitous 555 timer. Don't miss it!

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## LETTERS

## Mor-On Hackers

I read your last letters section with great interest. It seems that people can always find a way to justify anything they do, can't they?-R.W., Denver, CO. They can TRY to. But the wheel keeps right on turning, and wait until they get caught and have to stand before a judge and face the penalties. Suddenly the slamor and romance vanishes as they have to dig down and pay fines, court costs, lawyers fees, etc. The "proud parents" of what the newspapers labelled "Computer Wiz Kids" won't be so proud when they have to shuck out the big bucks. If only these people would think ahead!
"Ham" A Nasty Word? How would you feel about not calling yourself a Ham Operator anymore if some amateurs were violating the FCC rules? That's why I
want to keep calling myself a "Hacker!"-J. R., Tulsa, OK. I am proud of my ham ticket but if the same sort of thing occuirred, l'd go back to calling myself an "Amateur Radio Operator." Language keeps changing. "Gay" once meant "happy". If you felt happy, would you tell all your friends you were say?

## Everybody Does It!

I really don't see what the fuss is all about. Everybody hacks at one time or another, in one way or another. Maybe some more than others, but what's the big deal?M. L., Cranbury, NJ. First of all, everybody does not do it! I don't do it; neither do a lot of other respectable computer experimenters that I know. And the "fuss" as you call it is twofold. For one thing it's illegal. The other point is that it's giving us all a bad name.

## Thank You!

Okay. It took two of your editorials to convince me, but l've seen the point and you're right. And it took guts on your part to stand up for that right. You are to be complimented. Now suppose you peer into your crystal ball and tell us what's liable to happen if things go on like this.-S.W., Fairbanks, AK.
Thank YOU, S. W. What I can foresee is a Government examination on the use of computers before you're allowed to use one. An examination that goes into the do's and don'ts so that if you elect to violate the law and get caught, you won't be able to say "Hey! I didn't know." And I can see much tighter controls on modems too, with Government agencies freely tapping in to monitor and droppins in on violators to confiscate their equipment. $\langle\boldsymbol{D}\rangle$

# COMPUTER PRODUCTS 

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DISK-DRIVE/TAPE BACKUP COMBINATION, consists of the model AT HD disk drive and the model Q/C-60 AT tape backup unit.

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The model AT HD and QIC-60 AT sell for a combined sugsested retail price of $\$ 3290.00$.-Tencar, Inc., 6225 Cochran Road, Solon (Cleveland), OH 44139-3377.

EDUCATIONAL GAME, Pipes, teaches children the concepts of spatial relationships and economics. The object of the game is to create a water network, using the least amount of pipe and money. The player must connect all the houses in town to the main water supply. After hooking up all the houses, the player tums on the water supply to make sure that there are no leaks. The VIC-20 version of Pipes was the 1983 CES Software Showcase award winner for "Best Home Education."

Pipes is available for the IBM/PCjr

and Commodore 64 at a suggested retail price of $\$ 29.95$. All versions are on disk.-Creative Software, 230 East Caribbean Drive, Sunnyvale, CA
94089. <CD

# HiTcch's SAM 3001 PAT O'BRIAN 

## The AT-compatibles are coming! Here's a look at one of the first to hit the market.

-The IBM PC/AT received great critical acclaim when it was introduced a little over a year ago, mostly for its unmatched price/performance ratio. IBM's pricing policy was obviously intended to beat the clone competition even before it started.

The public, however, did not regard the AT as highly-at least if you believe the sales figures. The extra speed was impressive, but not enough to warrant buying a new machine. And then there was the harddisk drive that created reliability problems. If not for the IBM Local Area Network, the machine could have headed the way of the Junior. The software writers also were-and still are-slow in taking advantage of the AT's impressive capabilities.

All that may change now, because the ATcompatibles are on the way. And that's sure to make even IBM's machine more popular than ever. We had the opportunity to examine one of the first AT-compatibles to hit the market: the Sam 3001 from HiTech International ( 1180 Miraloma Way, Sunnyvale, CA 94086 (408) 738-0601). You might remember HiTech as the manufacturer of the Sam 2001 XT-compatible computer kit that was featured in July's Radio-Electronics.

## What the PC/AT offers

Before we look at what the SAM 3001 has to offer, let's see what IBM's machine does. The IBM PC/AT is not the fastest and most powerful computer you can buy, but it is the fastest and most powerful IBM personal computer. While it is compatible-for the most partwith the rest of the PC family, it is certainly an improved machine. For example, the AT uses the faster and more powerful 80286 microprocessor (which runs easily at 6 MHZ , has a true 16 -bit data bus interface, can address 16 MB of memory, and is upward compatible with the PC's 8086). The AT's system board can hold up to 512 K of RAM and offers 8 expansion slots. The disk controller can support fixed- or floppy-disk drives, including the
new higher capacity ( 1.2 megabyte) floppy-disk drives. A CMOS clock/calendar memory with battery backup was added to the main board. CMOS RAM on the clock chip-instead of on-board switches-holds most of the system-configuration information. The keyboard is much better than anything else IBM has offered. The AT's BIOS (Basic Input/Output System ) is 4 times bigger than the XT's. Some of the reasons for that is that it includes the hard-disk BIOS instead of relegating it to the controller card; the floppy-disk BIOS can handle various media and drive types, and multitasking-support functions were added.

## Can a compatible be better?

You might expect the clone-makers to try to out-do IBM. And that's exactly what HiTech tried to do with the SAM 3001. And-as shown in the table below-for the most part, they succeeded.

The Motherboard (manufactured by Faraday Electronics of Sunnyvale, (A) comes standard with 640 KB of RAM and is expandable on-board to 1 MB . You'd have to tie up an expansion slot on the AT if you wanted 1 MB of RAM. Two serial ports and one parallel port are included on the main board, so they don't tie up any slots. What's more, each serial port can be configured for either the RS-232 or RS-422 standards.

The SAM 3001 runs PC-DOS 3.0, as well as MS-DOS 3.1 and XENIX. Of course, as with any other PCcompatible, the SAM 3001 does not support IBM BASIC, but it will run the similar GW-BASIC.

A hercules-type graphics card, which includes a parallel port, comes standard with the SAM 3001, as does a 20 MB hard-disk drive.

To sum up the SAM 3001, it's basically an AT with the added benefits of extra on-board memory and serial and parallel I/O ports, a monochrome/graphics card, and greater expandibility. We found the machine to be compatible with IBM's; it will even run IBM's advanced diagnostics program. We are not without complaints, however. We had problems with the board's power-on reset, and with the configuration-memory. A new set of ROM's cleared up the configuration-memory problem, and a call to HiTech cleared up the power-on reset problem. (It really wouldn't have been a much of a problem if the documentation supplied with the SAM 3001 was better-a change in the reset circuit was never documented.)

Another complaint was with the floppy-disk drive. It was extremely loud, and we were victimized by frequent data errors. Hitech has realized that problem, however, and has switched to Mitsubishi as its drive supplier: $\langle\boldsymbol{\omega}$

|  | Price | Microprocessor | RAM | On-board <br> RAM (max) | Disk Drives | I/O Ports | Display <br> Adapter | Empty <br> Slots |
| :--- | :---: | :---: | :---: | :---: | :--- | :--- | :--- | :---: |
| IBM PC/AT | $\$ 3995$ | $80286(6 \mathrm{MHz})$ | 256 KB | 512 KB | 1.2 MB Diskette | None | None | 7 |
| IBM PC/AT <br> Enhanced | $\$ 5795$ | $80286(6 \mathrm{MHz})$ | 512 KB | 512 KB | 1.2 MB Diskette <br> 20 MB Fixed | 1 Serial <br> 1 Parallel | None | 6 |
| HITECH <br> SAM 3001 | $\$ 4395$ | $80286(6 \mathrm{MHz})$ | 640 KB | 1024 KB | 1.2 MB Diskette <br> 20 MB Fixed | 2 Serial <br> 2 | Hercules-type <br> 2 Parallel <br> mono/graphics | 7 |

## ATTENUATORS AND MINIMUM-LOSS PADS

## Larry Friedman

"Attenuators" and "Minimum Loss Pads" is a program that quickly calculates the resistance values for H and T attenuators and the values for minimum-loss L-matching networks. Simply key in the input and output impedances and the desired amount of loss. The screen will display the actual pad configuration and the required resistance values. If you haven't the displayed resistor values on hand, you get a chance to recycle the program and make minor changes in loss or impedance until you come up with a pad using resistor values you happen to have.

To make the program truly universal, it is written in "Timeshare Basic," which means it should run on every computer without modification of any kind except perhaps for the CLS (CLEAR SCREEN) function. If your own computer uses something other than CLS for CLEAR SCREEN simply make the substitution. Otherwise, no further modification to the program should be required.

Because PRINT USING is not a universal BASIC function, resistance values are rounded off to their nearest integer value. For example, 144.3 ohms is displayed as 144 ; 144.6 ohms is displayed as 145 ohms. Rounding should have no practical effect on an attenuator. On the other hand, decibel loss is displayed in decimal values because that is the common and desired notation.

To make the screen and printer graphics universal, they are created from conventional ASCII symbols. Figures 1,2 and 3 show how the three kinds of calculated pads are both printed and displayed. The negative ( - ) symbol in front of some of the resistor values in Figure 1 indicate that the impedance on that side of the circuit is less than on the opposite side. Note from the illustrations that two different ways are used to indicate input and output impedances. You can substitute either kind of labelling in the program since both employ universal characters.

Depending on the kind of computer used, the graphic display will either fill most of the screen or appear scrunched on the left. This is caused by writing the program so it will run on all computers regardless of the display's column width; also, screen dumps to a printer will print on every kind of printer used with personal computers, from conventional 80/100 column models to pocket printers which use narrow calculatortype tape. $\langle\boldsymbol{\omega}\rangle$


600 OHMS IN 150 OHMS OUT
DO IT AGAIN? (Y OR N)?


FIG. 1-THIS IS HOW the three types of pads are both printed and displayed. Please refer to the text for additional information.
z= 500 --> <-- z= 50
z= 500 --> <-- z= 50
do it maain? (Y OR N)? .

# DOUBLE DENSITY THE PERCOM WAY 

Can you modify your Model I<br>Double Density Adapter? Sure you can!

## GARY FOSTER

MIf you bought the Radio Shack Model I Double Density Adapter Kit to increase disk storage on your old Model I you soon realized that you were all alone in the world. All your Model I owning friends had done the smart thing. They bought the PERCOM Doubler which let them use all the neat new operating systems on the market. You and I were stuck with Model I Double Density TRSDOS and little else. You may have decided to make the best of a bad deal and live with your double density (of a sort), but I spent a lot of time looking at my NEWDOS80 Version 2 manual with all those PDRIVE command options, references to "double density" and so forth.

## Circuit descriptions

A comparison of the schematics showed me that the same double density disk controller chip was used on both adapter boards. The only differences between the two were the method of addressing the bit which selected single or double density and the method of enabling write precompensation. On the Radio Shack version, density selection is done by using the upper three (3) bits of data written to the sector write register at address 37 EEH (Al is high and A0 is low). See figure 1 for the schematic of the circuit. A normal write to the sector-write register will contain zeroes in data bits D5D7 (there are no sectors greater than 31). This inhibits the density select decoder (U10) by placing a logic


FIG. 1-SCHEMATIC DIAGRAM of the Radio Shack doubledensity adapter enable circuitry.
high on the G2A input. A write to the sector write register with any of D5-D7 active (D5'-D7' low) will enable the select decoder (U10) and allow a density change to occur. Remember that all data lines on both adapter boards are present only in their inverted state. See Table 1 for the control functions of each combination of D5-D7. Notice that write precompensation is enabled by the software.

The PERCOM method of density selection is entirely different (as we already knew). This circuit, shown in figure 2, clocks a D-type flip-flop when address lines A1 and A0 are low (this forms address 37ECH when the disk controller write enable line is active) and data lines D3-D7 are high. Data line DO is then latched into the flip-flop to select single or double density. Address 37ECH is the address for the disk controller's command register. At first glance it appears that this method of addressing would cause the active controller to

| D7 | D6 | D5 | FUNCTION |
| :--- | :--- | :--- | :--- |
| 0 | 0 | 0 | No function |
| 0 | 0 | 1 | No function |
| 0 | 1 | 0 | Select drive side 0 N/A PERCOM |
| 0 | 1 | 1 | Select drive side 1 N/A PERCOM |
| 1 | 0 | 0 | Set double-density mode |
| 1 | 0 | 1 | Set single-density mode |
| 1 | 1 | 0 | Disable precompensation |
| 1 | 1 | 1 | Enable precompensation |

TABLE 1-CONTROL FUNCTIONS of data lines for the Radio Shack Double-Density Adapter.
execute an unwanted (and possibly data-eating) instruction. A peek at the command summary for both controller chips quickly shows why this is not a problem: There is no instruction which corresponds to a high condition on datalines D3-D7. The active disk controller does not mind beins given a meaningless command and we can safely swap active controller chips without fear. Write precompensation is always selected in double density with the PERCOM system.

At this point you might say that this is an interesting comparison of two relatively simple address decoding schemes, but where's my double density?

Because of an interesting quirk on the part of the designers of the 7400 TTL family I found a reliable solution to my problem. The Radio Shack circuit board uses a 74LS10 integrated circuit (U11) in the enable circuit for the select decoder. This chip is a collection of three NAND gates with three inputs each. I was able


FIG. 2-REPRESENTATION OF PERCOM doubler enable circuitry.


FIG. 3-RADIO SHACK'S double density adapter modified to perform as a Percom doubler.
to alter the address decoding scheme of the PERCOM Doubler to the circuit shown in figure 3 and then to figure 4. But this method requires three input NOR gates. By the grace of the 7400 TTL designers though we almost have our solution. There is a triple 3 input NOR gate chip (7427) that is pin compatible with its triple 3 input NAND gate cousin ( 7410 ). If we can replace our old 74LS10 chip with its NOR counterpart 74LS27 some of our work is already done by the PC board design. We will cut our expensive circuit board in fewer places! (See Fig. 5.)

While we are at it, we will change the circuit board to enable write precompensation any time double density is selected. We will also wire the interrupt lines from the controllers back into the circuit. This will make us fully compatible with the PERCOM Doubler. The best part is that the complete modification will only cost about $\$ 1.00$ in parts and two hours in time.

As this will most assuredly void any warranty on your Radio Shack Double Density Adapter proceed with caution. We are going to remove your old Radio Shack Double Density board and, with a little static-free luck, turn it into a perfectly functional look-alike to a PERCOM Doubler. If you are not completely comfortable working on a $\$ 150$ circuit board get a friend or pay someone to do this modification for you. You must remove an IC from your board, cut 10 traces, and connect jumpers in 14 places.


FIG. 4-THE RADIO SHACK double density adapter installed in the LNW System expansion board.

## Modification procedure

First, take a hard look at everything you have stored in double density mode with your Radio Shack Double Density Adapter. Copy everything you want to keep onto single density TRSDOS diskettes. Once you modify your board you will no longer be able to read or write to your TRSDOS Double Density diskettes.

Next, assemble the following items:
1 74LS27 Integrated circuit
116 pin IC solder tail IC socket
1 Low heat soldering iron
1 Short length of 22 ga . solder or equivalent $24-\mathrm{in}$. of 28 ga . Wirewrap wire or equivalent 1 Pair of small diagonal cutting pliers
1 Pair of small needle nose pliers or tweezers
Disassemble your expansion interface and locate the disk controller pissy-back board. It should be the only small circuit board you see. Carefully remove the circuit board without bending any pins. Be patient and rock it back and forth until it comes free from the socket. Hold the board with your fingers on the edges (don't touch the IC's any more than you can help). Turn the board so that the component side is facing you with the Tandy Corp. printing in the upper risht hand comer. The IC on the very bottom (just below U3) labelled U11 is our target. Using soldering iron with a low heat element and an IC removing tip, desolder and remove U11. (See Fig. 6.) Another suitable method is to cut the IC loose from its pins and discard it. Then heat each solder pad in turn and, with a small pair of needle nose pliers or tweezers, remove the metal scraps from the hole. Carefully clean up your solder flux and clean out the holes, then solder an IC socket into the holes where the IC used to be. Using a very sharp razor blade, or an Exacto knife cut the following traces: between U8 pin 9 and U6 pin 9 between socket header pin 39 and ground between U11 pin 1 and U11 pin 13 between U11 pin 8 and U11 pin 5 between U11 pin 3 and U11 pin 12 between U11 pin II and U10 pin 1 between U11 pin 10 and U10 pin 2 between U11 pin 9 and U10 pin 3 between U11 pin 6 and U10 pin 4


FIG. 5-CLOSE-UP VIEW of the Radio Shack double density adapter. Arrow points to U11, the IC we're going to replace.


FIG. 6-WIRING MODIFICATIONS to U11. Notice that we deliberately make large gaps in the trace cuts to ensure a good cut.

With a soldering iron on low heat use \#28 gase wire wrap wire to make the following jumper connections:
between U3 pin 39 and U4 pin 39
between U3 pin 39 and socket header pin 39
between U10 pin 2 and +5 volts
between U10 pin 3 and ground
between U10 pin 1 and U9 pin 6
between U9 pin 5 and U3 pin 7
between U11 pin 2 and U3 pin 11
between U11 pin 13 and U3 pin 10
between U11 pin 12 and U9 pin 9
between U9 pin 8 and U11 pin 3
between U11 pin 8 and U9 pin 11
between U9 pin 8 and U111 pin 5
between U11 pin 6 and U9 pin 13
between U9 pin 12 and U10 pin 4

## Reassembly and testing

Carefully insert a 74LS27 IC in the socket you installed. Be sure that the notch in the IC package is oriented properly and that no pins are bent or broken (even though we have 16 on this one, we don't have any to spare). Just as cautiously as you removed it, install the double density board back in its socket on the expansion interface board. Be certain that you install the adapter board in the same orientation as it was before. (See Fig. 7.)


FIG. 7-THE COMPLETELY-MODIFIED BOARD. Use this illustration as a guide while working on your own board. Leave as little slack in jumper wires as possible.

Give your handiwork one last look for safety, then button it up and hook up your keyboard-El cable. If at any time the computer does not behave normally quickly turn it off and find out what is wrons. Turn on your computer. If all seems well, boot up a single density diskette; you should still be able to boot up with any single density operating system. If this does not work then turn the computer off, disassemble your Expansion Interface and check your work. The most probable causes for problems at this point are wiring errors or a static-damaged single-density disk controller. If all is well up to this point follow the instructions for creating a double density operating system diskette. NEWDOS80 Version 2 will allow you to do this with the PDRIVE command. Your computer should now have written double density to a diskette and verified it correctly. If so, then you are throush except for the rather pleasant task of copying everything in your software library onto your new double density diskettes. If it does not work in double density but appears to be okay in single density, you have probably damaged your double density


FIG. 8-A JOY TO BEHOLD! NEWDOS/80 with ever so much memory available for only the cost of a 29 cent IC!

## controller chip. (See Fig. 8.)

If the wiring is okay after you have thoroughly rechecked it, try replacing this chip and try again.

Since this chip is easily subject to damage, and since its cost is so low, there should be little or no hesitation in suspecting it, and if necessary, switching it for a new one. It's certainly a small enough price to pay.

The Radio Shack Double Density Adapter is a nice addition to the TRS80 Model I. It is a well made circuit board and appears to be a reliable design. It is certainly capable of withstanding rigors of this modification. When Tandy released this device however they chose to make it incompatible with the popular operating systems produced by other companies. The great majority of good software for the TRS80 Model I is on non-TRSDOS operating systems. If you bought the Radio Shack and are not satisfied with the options available to you, then this modification is for you. I have used this modification on my system with NEWDOS80 Versions 1 and 2 for several months with no pad side effects; it should work equally well on any double density operating system which uses the PERCOM Doubler.
(I)

## TUNING THE COMMODORE 1541

How to align and time the 1541

## JIM STEPHENS

-While the Commodore 1541 disk drive is a marvel of electronic hardware, it has its flaws. Sooner or later it will require servicing. The following is a description of what you might try before sending it off on a long and expensive vacation.

One day my 1541 began to blink slightly while trying to load my favorite program and this led me to believe a problem might be brewing. Since the 1541 doesn't give up easily, it continued to re-read the data and finally load the required blocks from the disk. The blinks, which meant mis-read data bits, kept getting worse until one day, it gave up and refused to load those programs at all. Often, it would get completely through the load and reset itself back to the ready prompt screen.

On newly-written programs, where I had just formatted the diskette, there was no problem. The light stayed on continuously and the programs loaded fine. If I tried to load a piece of commercial software or a program from a friend's machine, things got really bad. After losing a complete database file disk from which I couldn't recover, I decided that now was the time to fix it. Fix what? I wasn't quite sure.

I found that there are three main mechanical malfunctions that could cause blinking lights and misread data. These are a dirty read head, a slow disk speed, and a misaligned head to the data track. Since these problems are mainly mechanical, they can be fixed by the average mechanical tinkerer. However, poking around inside a delicate piece of equipment such as the 1541 with a screwdriver does require some experience and is recommended only to those who feel capable and confident.

## Case disassembly and head cleaning

Occasional program glitches and garbage on the screen without a blinking red light could mean a dirty read head. Rather than pay a small fortune for a head cleaning kit, use a cotton swab slightly dampened with alcohol. The only problem is getting at the head in order to clean it.

The drive case is formed of an upper and lower half. The top is held together by four screws through the bottom. Unplus the 1541 from the power cord and serial buss. Turn the drive over and remove the four screws. Then re-invert the drive carefully and remove the top cover. You will see part of the circuit board and some of the diskette guides. The rest is covered by a steel cover. This interior cover is attached by two small screws on the left side of the drive's metal frame. The right side is secured by two small detents that hold the cover in place. Remove this cover and you can view
the rest of the circuit board and drive mechanics. Also, you can easily see the read head near the center below the circuit board.

The head is the small white square towards the bottom of the case that is recognizable by the black line that crosses it from left-to-right as shown in Figure 1. Most people think that the head is the sizmo that rides on top of the diskette. This is only a small piece of white felt that holds the diskette surface close to the head. The data is read from the bottom of the diskette, not the top.

Using a good-quality cotton swab which is slightly damp with alcohol, you can gently clean the surface of the head at this point. Make sure you don't soak the swab, or you may flood the head causing more problems. You'll be surprised to find a generous amount of grime on the swab if you have used your drive extensively. Many times, this cleaning is all that is required to get the drive back to normal operation. You may want to try a program or two before proceeding further. Always replace the metal cover and case top before powering up.

## Speed adjustment

The 1541 is tolerant of variations in diskette speed. Several things can cause the speed to vary. Dras on the drive mechanics, asing in the electronic drive components and slipping in the drive hub, to name a few. If the drive makes a low, screeching sound, the bearing that holds the plastic centering guide may have become dry. This bearing is located in the center and on top of the diskette guide arm. A small washer holds the shaft in place and lifts the centering guide when the door is raised. This shaft is directly over the center hole of the diskette when the disk is locked in place. A small drop of light machine oil on the washer will oil the bearing further down this shaft. Be sure that only one drop is used and any excess is wiped from the top of the assembly.

To actually check the speed of the drive, Commodore has included a dynamic speed indicator


FIG. 1-LOCATING THE HEAD is easy if you follow the instructions and refer to the diagram above. Look for the small white square with the black line crossing it from left to right.
on the bottom of the diskette drive flywheel. To see this, it is necessary to remove the bottom of the plastic case.

You probably noticed that the drive is attached to the bottom of the case with six screws. After these are removed, the drive can be carefully turned on its side. The metal cover should be attached before this is done. The green indicator light is still attached to the case, so care should be used when the assembly is turned on its side in the case bottom.

Figure 2 shows the bottom of the drive assembly. The speed indicator is the pattern on the bottom of the drive flywheel. The pattern is used like the timing light on a car to set the correct rotation speed of the flywheel. The outside pattern is used for house wirings that operate on 60 Hz and the inside patern is for 50 Hz as found in England.

To check the speed of the drive, connect the drive to the computer and load a program while the assembly is on its side. Be careful since many components are exposed and should not be touched while the drive is operating. Shine a small fluorescent lamp on the flywheel with other lights in the room out. The outer pattern on the flywheel should appear stationary. While a small amount of movement can be tolerated, it should be corrected. If the pattern seems to be rotating rapidly, some adjustment to the speed control will be required.

The speed control is located under the metal case below a small access hole above and to the left of the flywheel. It is a small potentiometer with a screw that is held securely by a green glue. A small screwdriver inserted into this screw and turned will either speed up or slow down the pattern. Turn the screw until the pattern stops rotating and remains still. You may have to try several times. You should not have to turn the screw more than $1 / 1$ oth inch in either direction. If you do, there


FIG. 2-BOTTOM OF THE DRIVE ASSEMBLY. The speed indicator is the stroboscopic pattern on the bottom of the drive flywheel. Outer pattern is for 60 Hz , inner is for 50 Hz .
may be other problems causing the incorrect speed. If you cannot get the pattern perfectly still, get as close as possible.

## Read-write head alignment

The last and most-probable cause of problems is a misaligned head. The head has a tendency to slip from the correct path over the disk track after awhile. Since the diskette has some 35 tracks and only about one inch of lateral space around the disk, each track can be no more than $1 / 32$ inch wide. Any small deviation can make the head appear to be badly misadjusted. A deviation of even $1 / 64$ th inch and the head would be half way off the track.

The problem of course, is to get it back on track and this is no simple task without test equipment. There is a way however, It's called "trial and error." We can do it, but it may take several tries.

Figure 2 shows the bottom of the main drive assembly. Note that the flat stepper motor is to the right of the patterned flywheel that we just used. This stepper motor controls the position of the head. This motor was meant to be moved to correct any small error in head alignment. The motor is held by two screws. These fit through holes that are really channels. They are elongated so the motor can be positioned up to $1 / 4$ inch forward or backward. We use this adjustment to correct head misalignment.

To correct head misalignment, load a commercial program disk. If your head is badly out of line, note that the red indicator blinks as the drive misses data and tries again. Use this disk to check the accuracy of adjustments. With the drive off, mark the position of the motor relative to the metal case. A small scratch or pencil mark on both sides of the motor will do. This is important, as you need to know how far you moved the motor and in what direction.

After marking the starting location, loosen the retaining screws slightly. You will have to scrape most of the green glue from around the washer. The washer will not rotate unless it is free.

By observing the location of the marks, rotate the motor clockwise, only slightly, no more than $1 / 32$ inch. Move too far and you may overshoot the track completely. Tighten the retaining screws and try the blinking disk program again. If the blinking has been reduced, you moved in the right direction. If it has gotten worse, repeat but in the other direction.

Once the red light is steady, turn off the drive. Retighten the retaining screws and make a final test. Load a disk with error clatter and make sure the clatter doesn't crash the load. If it does, you'll need to adjust the stepper motor again. You should not have to move the motor more than $1 / 8$ inch in any direction. If all else fails, move the motor back to the original starting point and adjust in even smaller increments.

This is not a job for the faint of heart, and you'd better believe it's better than sending eighty-five big ones to Commodore's repair-replace department. Tuning the 1541 is like tuning up a high-powered race car at the Indy 500 . But with just a little effort, you can have your 1541 purring like a kitten.


# YOU CAN BUILD THIS MODEM FOR YOUR COMMODORE 64 

## PART II

## JIM STEPHENS

Last month we began a two-part article on how to build this Modem for your Commodore 64. Here is the conclusion of that article.

## Circuit adjustment

The most difficult part of the whole project is the adjustment of the circuit for proper operation.

The Commodore 64 can be made to handle many of the chores done by expensive testers and generators. The 64 has a sophisticated sound chip. which we use to set the frequency of our modulator and can use the small program in Listing 1 to check the output of the demodulator. It is not the best alignment method, but it works.

Do not connect the modem to the user port yet. Turn the variable resistors R13 and R14 to the center of their adjustment. These are the timing resistors of the modulator that set the pitch of the two modulator tones. Flip the toggle switch on the modem to ON and you should hear a high-pitched tone coming from the speaker. If not, adjust R11 of the modulator upward and readjust R16 until the volume increases. Keep the volume low or the tone will become distorted. If no amount of adjustment produces a tone, there is probably a wiring error and the circuit needs to be rechecked. Always check the power leads first with a volt meter.

If a tone is being produced, connect test jumper 1 from pin 9 of the 2206 to ground with a small jumper. Notice that the tone changes. Each time ground is touched, the tone shifts frequency. This is the keying
input of the modulator and it is this pin that reads the ones and zeroes from the computer. Turn the modem off and connect the modem to the user port. Remember to orient the connector properly. Turn on the 64 and the READY prompt should appear. If it does not, turn the 64 off check all connections on the connector and the wiring of the modem. Always check the voltage levels and their connections first. If the prompt appears, enter the frequency set program shown in Listing 1 and save it. Note that the modem is still off.

## LISTING 1

1 REM TEST TONE FOR 2225 HZ
REM $F=1070 \mathrm{HZ}$ DATA $=69114150$
3 REM $F=1270$ DATA $=8194150$
4 REM F $=2025$ DATA $=130,67,150$
$5 \mathrm{~S}=54272$
10 FOR L $=$ STOS $+24:$ POKEL, $0:$ NEXT
20 POKES $+5,9:$ POKES $+6,250$
30 POKES $+24,15$
40 READHF,LF,DR
50 IF HF < 0 THEN END
60 POKE S +1 , HF: POKES,LF
70 POKE S $+4,33$
74 END
110 DATA $143,115,150$
LISTING 1-TONE SETTING PROGRAM for the modulator. Data in line 110 is set to the numbers shown in lines 2 through 4 for the other frequencies as necessary. Data for a 2225 Hz tone is already shown in line 110.

The frequency set program is used to set the tones of the modulator. The small program is initially set up to produce a tone of 2225 Hz using the 64 sound chip. Line 2 through 4 of the program are REM statements that show the data statement changes necessary for the program to produce the other frequencies. Normally, the tones of the modulator will be set to 1070 Hz and 1270 Hz , but first, we must get the demodulator section working before these frequencies are finally set. The demodulator needs to be adjusted to respond to frequencies 2025 Hz and 2225 Hz . To do this, we need to set these frequencies into the modulator first since we will use the modulator section to feed the demodulator when we adjust it.

Run the frequency set program and a tone at 2225 Hz will sound throush the $\mathrm{TV} /$ monitor speaker. Make sure jumper J1 is not connected to ground and turn on the modem. The tones from the two sources will be quite different. With each tone at about the same volume, adjust R13 (the one that connects to pin 7 of the 2066) until the tones are exactly the same. Listen to the pitch of each at the same time. As the tone of the modulator nears the same frequency of the TV/monitor, the sound will start to rapidly "chirp." This means that the tones are about the same frequency but not exact. Slightly turn variable resistor R13 until the chirps start to slow down to a slow "beat" like a wave of increasing and decreasing volume. The slower the wave, the closer the two tones are. If you have ever tuned a guitar using the fifth fret and listened to the beats, this will be no problem. If you find that all you can get is chirps,
you may be tuning to a harmonic of the 2225 Hz frequency and the tone on the modulator should be raised or lowered. If no amount of adjustment brings the slow beat, you might be tone deaf and should get a friend to help or your variable resistor is too critical and may have to be chansed to another with a more even or smoother taper. If you have one, a 10-turn potentiometer works well here. Of course there might be a wiring error Get the beat as slow as possible and proceed to the next step.

Connect the jumper (J1) to ground. Notice that the tone changed. Turn off the modem with SWI and the tone on the TV/montor by entering RUN and hitting RUN/STOP quickly. Change the data in line 110 to that shown in line 4 of the program. This is the data that will produce our 2025 Hz tone. Repeat the above procedure while adjusting R14 (the variable resistor that connects to pin 8). Once the slow beat is obtained, you have the modulator tuned as though it were an answering modem. Alternately connect and disconnect J 1 from ground and listen to the tone altemate from 2225 to 2025 Hz . We can now use these tones to set the demodulator section.

## Tuning the demodulator

Place the modulator speaker near the mike of the demodulator. With the modem on, disconnect J1 from sround. The modulator should be producing a low volume 2225 Hz tone. Place a voltmeter or logic probe on the collector of Q1. The logic level should be high or around five volts. R8 (the volume control on IC3) should be about in the center of rotation. Connect J1 to ground and the output at the collector of Q1 should drop to zero volts. If it does not, vary R7 slowly on the demodulator until it does drop from high to low when ground is touched by the JI lead. If no amount of adjustment produces this change, readjust the volume control R8 either higher or lower until the modulator locks in with the above adjustment. Once it does, alternately connecting and disconnecting ground with $J 1$ will produce a high and low reading at Q1's collector. A partial low or high on the collector of Q1 is not correct. It must swing all the way to ground and most all the way to five volts. Keep adjusting R8 and R7 until you get the proper swing in voltage level. Don't readjust the timing resistors on the modulator, however, unless you feel the first frequencies could be wrons and you want to start over. Too much volume on the speaker and it could distort the tone being heard by the demodulator. It should work even with the modulator volume quite low. If the level does not change correctly adjust R8 on the LM386 of the demodulator for more drive. If you have one of the modem.programs mentioned earlier, you can run it and type into the computer and the response of the modem should print out your typins correctly on the screen. This is yet another method to test the response of the demodulator.

Once the demodulator is responding correctly to the 2225 Hz and 2025 Hz tones, you can reset the modulator (the speaker) to the correct frequencies at which it will operate ( 1070 Hz and 1270 Hz ): This is
done with the same procedure as you used in the above example except you change the data in the frequency set program to read as shown in line 2 and 3 of the program. Remember that these tones must be almost exact to work correctly. You could be off as much as twenty cycles and the modem would still work with most answering terminals.


LISTING 2-SMALL PROGRAM which reads the user port and prints resulting data characters on the screen. There are no ASCII interpretation tables so many of the characters will appear as graphics.

## Testing the modem

With the speaker still placed near the mike, enter the small test program shown in Listing 2. Save it and enter RUN. The screen should clear. Then, by rapidly connecting and disconnecting J1 to ground, several graphic and alpha characters should appear on the screen in response to the varying tones. If you have a small modem program, you could call up a friend with an answering modem, insert the phone into the coupler's cups, and have the answering modem type some communications which could be used to check the unit. You can even use the small program in listing 2 but a lot of the data will appear as graphics although some of it can be read with a letter missing here and there.

If you are receiving data over the phone line, it may be necessary to slightly adjust the volume controls and even R7 on the demodulator for the best response. Do not readjust the timing resistors on the modulator however unless you are doing it using the frequency set program as in the above example. The mike's LM386 is not too powerful and the tone on the phone can get quite low. The LM386 amp is at its maximum gain/ quality level and a bad connection and low answerins volume may not make it through.

If you find that the answering modem is responding correctly to your modulator when you enter data, but the demodulator is not responding to the tone coming in, you probably have too much or too little volume going from the phone to the 2211. Try readjusting the volume control on the mike's LM386 until the data is correctly printed on the screen. Occasional garbage on the screen usually means low volume to the 2211 from the mike's LM386.

If all else fails, an audio tape is available from Syntronics that has all of the tones recorded and a .continuous data stream in the answering frequency which can be used to adjust and set your new modem with the use of an audio tape recorder. Details of how to order are given in the parts list.

Above all remember, if it fails to work the first time, you can learn a lot from finding the fault. The feeling of accomplishment you get when it does work correctly is worth more than all the expensive modems ever produced $\langle\boldsymbol{\text { D }}$

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is worn. With power removed from your set, measure the continuity between the wires plugged into the cartridge and the input jack. The wires in the phono arm often come loose; they can cause noise merely by sagging and rubbing the record. Make sure that all contacts of the input jack are clean.

If you don't find an open anywhere, the cartridge is probably the source of the trouble. Cartridges can seldom be repaired; they must usually be replaced. Sometimes the phono arm has to be disassembled to remove the cartridge. If you have any problems, try laying a small mirror under the pick-up arm to see if there are any hidden screws that must be removed. It is usually easier to unplug the wires after you remove the cartridge from the arm. It will require some patience to get the tiny screws started in the new cartridge. Caution: If you have sol-der-clips on the wires that plug into the cartridge, don't solder them until they have been unplugged from the cartridge. You'll be sure to ruin it otherwise.

Loose (or open) connections and bad cartridges are the most common source of problems in phonos that use radios for amplifiers. But other problems can crop up. For example, a turntable motor that has been in disuse for many years may be "frozen" from its lubricants drying out. Try rotating the turntable by hand. Is the drive wheel or belt slipping? If everything seems OK, the motor may have to be replaced.

There are other reasons why a turntable might not turn. Radio/ phono combinations, as well as separate turntables, usually have a separate AC plug for the turntable. Make sure that the phono is plugged in! With everything unplugged you might try making a continuity check across the turntable's AC plug. If you find an open, the line cord might be bad, or the motor might have an open coil. Another caution: An old turntable, like an old radio, may be shorted, so it thereby poses a dangerous shock hazard. If you haven't been trained in electronics, be sure to get help from a friend who has before even plugging your antique in.


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# SERVICE Clinic 

## Wave propagation and antennas.



SOME TIME AGO (IN 1959), I WROTE A book about TV antennas. Before doing the actual writing, I did some reading on the subject (which is always a good idea), and found out that OTH (Over The Horizon) TV reception was impossible. However, I knew better! I lived in the mountains, and I could catch three or four stations that were well over the horizon just about any time I wanted. So I knew that the books were wrong, but I wanted to know how the signals did arrive. An answer came to me that may be questionable, but I wrote it up that way, and, so far, nobody has proved me wrong.

The traditional line-of-sight description of wave propagation is shown in Fig. 1. The signal is radiated from the transmitting antenna as if it were a bullet from a gun. In order for the "bullets" to reach the receiving antenna, nothing can obstruct the path they travel. Therefore, according to that explanation, OTH reception is impossible. Once the radiated waves passed beyond the receiving antenna, they would continue on out into space, lost forever.

I didn't see it that way. When a signal first leaves the transmitting antenna, if its field were plotted, it would resemble a doughnut. But soon the signal would appear as a series of wavefronts perpendicular to the ground. After a while, those waves would begin to tilt, as shown in Fig. 2, and lean forward at the top! The reason for the tilt is that the refractive index of the atmosphere decreases with height, so radio waves travel more slowly near the ground. That difference in speed causes the wave to tilt,


FIG. 1


FIG. 2
and, I believe, gives it the ability to travel over the line-of-sight horizon of about 60 miles.

Of course there is another possible explanation for over-thehorizon reception. The upper portion of the signal, called the skywave, leaves the transmitting antenna and travels up into the ionosphere. The ionosphere is composed of several layers; the $E$ layer is about 70 miles from the surface of the earth, and it is that layer that most commonly reflects high-frequency radio waves. Such skywave reflection is called E-skip, and that is another means by which OTH reception may occur.

However, E-skip is not at all reliable, due to the fact that the various layers of the ionosphere are constantly shifting. Of course, there are DX hounds who are constantly on the lookout for such things, and they have recorded some long distances. I think the record is held by someone on the east coast who watched the BBC for several days. Those DX'ers take photos of their TV screens to prove that they
have actually received the stations they claim to have received. So Eskip can explain occasional occurrences of DX, but not the sort of daily OTH reception we experience here. Perhaps there's a better explanation than my "tilt theory," you have but I haven't heard one.

## Myths about antenna height

It is a commonly-held fallacy that the higher the receiving antenna, the stronger the received signal will be. I have measured, with a field-strength meter, every antenna I ever put up, and I have found that, around this area, there is an optimum height: about 30 feet. Once I owned an extendible tower that would go as high as 40 feet (or 50 feet, if I added a 10-foot section of mast). I found that up to about 30 feet, signal strength increased with height. Above that, there was no perceptible difference, so I have been installing antennas at that height ever since with no complaints.

One last word about towers: Be sure that the mast is properly guyed. That means at least two sets of guys for a 30 -foot antenna: one right under the antenna, and another halfway down the mast. For a higher tower, add more guys. My tower, a 65 -footer, half of which I could have done without, has four sets, and it has withstood storms with winds clocked at over 100 mph . Two nearby 90 -foot pines were blown over by the wind once, but my mast remained standing. Use sturdy cable, and fasten everything well.

## Yagi antennas

Around here I usually install a
six－element Yagi antenna．Inven－ ted long ago by Dr．Hidetuge Yagi of Japan，the Yagi is probably the best all－around type for both gain and selectivity．Yagis typically have a dipole antenna cut to the desired channel or channels．Normally you choose a Yagi with several ele－ ments，each cut to one channel transmitting in your area．

Average gain，with three direc－ tors and one reflector，runs at least $12-14 \mathrm{~dB}$ ．Antenna gain is almost always stated in comparison with a channel－cut dipole．Since a deci－ bel is a ratio，an antenna with a gain of 12 dB means that it delivers signals that are 12 dB stronger than the standard would deliver if it were mounted in the same posi－ tion．

One feature of the Yagi is its long，narrow＂frontal lobe＂that gives you a high degree of selec－ tivity，and allows you to bring in signals without too much co－chan－ nel interference．The latter is what causes the blanking－bar from an undesired station to float across the picture，occasionally appear－ ing as a negative image．That may also appear，at times，as closely－ spaced horizontal bars．Co－chan－ nel interference can be caused by E－skip；if so，you just have to wait it out．It usually disappears in a few minutes，but sometimes takes an hour or more．

Since Yagis are so directional， you almost always have to add an antenna rotator to get the most gain from your system．Rotators are not too expensive，but they＇re an absolute necessity in rural areas like mine．

R－E

## SERVICE QUESTIONS

## VCR KNOCKS OUT TV

My Zenith 19CC19 TV set blacks out after about one hour when used with my VCR．When that happens， the set remains inoperable for the next few hours．That doesn＇t occur when viewing over－the－air TV or using the VCR tuner．It only happens when I play a cassette．Could there be something in the $T V$ set that charges up and knocks out the pic－ ture，and then takes a few hours to discharge？－H．B．，Silver Springs， MD

The most essential thing re－ quired in service work is a good diagnosis．That sets the stage for all the troubleshooting steps that follow．Your preliminary diagnosis is a difficult one to accept；first that only playback kills the pic－ tures，and second，that once gone， the set needs several hours to rid itself of some excess．My instincts tell me that you have jumped to a conclusion，and that further test－ ing may show the picture blacking out in any mode of operation．

However，I＇ve had enough sur－ prises in this business to convince me to never say never．When the picture is gone，that＇s the time to act．Do you lose the high voltage？ What happens to the kine socket voltages？Do the bias voltages change，cutting the tube off？ Those questions must be resolved before you can make any progress．

## NO BLUE IN RASTER

Can you help me with a model L－2556DE9 Zenith color TV？I have no blue in the raster．I＇ve replaced the chroma－luminance module and checked the output transistors（in－ cluding drivers）on the kine module． I suspect the blue gun in the picture tube，but since the set is only 4 years old，it＇s hard to believe it might be the CRT．－G．S．，Phila．，PA．

Check the DC voltages going to the kine，comparing those of the blue gun against either of the other two．If G1，G2 and K voltages all match，then chances are the blue gun is bad．On the other hand，if one of the voltages is short，trace it back．

## NO SOUND

I really need your help on this one． I＇ve had to change an AN－239 IC on a Quasar 19TS－955 chassis 3 times for loss of sound．Each time the set went bad，the voltage on pin 6 of the IC increased．All other voltage mea－ surements look good．I hope you can help！－J．S．，Independence， OH

The audio－output transistor has 124 volts on its collector，and its base is directly coupled into the sound IC．A momentary flash across the transistor junctions could destroy that IC．There can be no quick way to find out if that＇s what is actually happening，but changing the transistor couldn＇t hurt．

R－E

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## THE MARK III

## HV CIRCUIT SCANNER

$\star$ Checks the horiz output circuit for open / shorts,
$\star$ Checks the flyback, yoke, PC, and HV mult,
$\star$ Checks all scan derived B+ sources,
$\star$ Checks all circuits that rely on scan derived B + voltage,
$\star$ Checks for open safety capacitor,
$\star$ Checks the emitter circuit of the horiz output,
THEN,
$\star$ Provided the green normal light is lit, the Mark III will safely power up the TV set so that you can "look" for open circuits by examining the picture on the CRT.
$\star$ Circumvents all start up and horiz drive related shut down circuits.

APPLICATIONS: The Mark III will analyze the horiz, flyback, hivoltage, scan derived B + sources, yoke, pin cushion, HV multiplier circuits in any TV set that employs either an NPN transistor or a single SCR for its horiz output device. This applies to any age, any model, any chassis, any brand - - including Sony.

In brief, the "test" function scans for shorts, the "run" function permits you to observe any "open" circuits via the symptoms that appear in the CRT screen.

HOOK - UP: Simply remove the set's horiz output device and replace it with the scanner's interface plug. No wires to disconnect, no other connections required (not even a ground connection).

MISTAKE PROOF: No damage will result if an error is made during hook up. The scanner simply won't turn on until the error is corrected.

RED OPEN LIGHT means the emitter circuit of the horiz output stage is open (no ground path).

YELLOW SHORT LIGHT means the flyback primary, HV multiplier, vertical output, horiz driver, and R-B-G color output stages are not shorted. Instead, a circuit that normally draws a small amount of current is shorted (i.e. the tuner, IF, AGC, video chroma, matrix, vertical or horiz oscillator).

RED SHORT LIGHT means either the flyback, the HV multiplier, the vertical output, horiz driver or one of the R-B-G output transistors is shorted.
GREEN NORMAL LIGHT means the TV set's entire flyback circuit is totally free of shorts. It also means that it is safe to power up the TV set with the "run" button so that you can look for open circuits by observing the symptoms on the CRT screen.

FEATURES: All start up circuits and all horiz drive related shut down circuits are automatically circumvented by the Mark III during all test and run functions. During the test function all flyback secondary output is limited to approx $80 \%$ of normal. 2nd anode voltage is limited to approx 5 KV

This means all circuits that are not shorted will have some $80 \%$ of their normal B + voltage during the "test" phase. It also means that any shorted circuit will have zero DC volts on it. This feature makes any short easy to isolate.

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PUSH THE TEST BUTTON Just one of the four lights will lite.

$\star$ Checks the horiz output stage for opens／shorts，
$\star$ Checks flyback，yoke，PC，and HV mult，
$\star$ Checks all scan derived B＋sources，
$\star$ Checks for open safety capacitors
$\star$ Checks for open ground path for horiz output stage
$\star$ Checks for open primary LV supply，
$\star$ Checks for error in interface connections，
＊Checks for proper LV regulation，
＊Checks for proper start up circuit operation，
$\star$ Checks for shorted horiz driver transistor，
$\star$ Checks the operation of the horiz osc／driver circuits，
$\star$ Checks B＋＂run＂supply for the horiz osc／driver circuits，
$\star$ Checks all circuits in the TV set that rely on scan derived B + ．
$\star$ Automatically circumvents all start up circuits and horiz drive related shut down circuits．

HOOK UP：（Identical to Mark III）
OPERATION：Turn the Mark V on，turn the TV set on，then，simply look at the lights．
RED＂HOOK UP＂LIGHT means that you have made an error in hook up．No damage has been done，correct the problem then continue．
RED＂EMITTER＂LIGHT means that the ground path for horiz output stage is open．Correct the problem then continue．
RED＂B＋OPEN＂LIGHT means that the primary LV supply in the TV set is open．Correct the problem then continue．
No＂top row lights＂equals normal．

## Look at the middle row of lights

RED＂START UP＂LIGHT means that the start up circuit in the TV set is not working（no start up pulse）．
GREEN＂START UP＂LIGHT means the start up circuit in the TV set is working normally．Yes，it is $100 \%$ accurate．Even on Zenith＇s single pulse start up circuit！
RED＂HORIZ DRIVE＂LIGHT with a green start up light means that the horiz driver transistor in the TV is shorted（ $E$ to C）．
GREEN HORIZ DRIVE LIGHT means that the horiz oscillator and driver circuits are operational．

READ THE DC VOLTAGE METER THEN， PUSH THE TEST BUTTON
If the meter comes up to，or，falls back to，factory specified DC collec－ tor voltage，the LV regulator circuit is working．If it fails to do so，it is not working！

RED＂B＋RUN＂LIGHT means that the B＋source that normally keeps the horiz osc／driver circuits running after the start up $B+$ pulse has been consumed has become open．

GREEN＂B＋RUN＂LIGHT means that the B＋resupply voltage （scan derived）is being provided．All is normal if all three lights are now green．

The scan circuit short detector in the Mark $V$ is identical in all ways to that which is used in the Mark III．Operation is also identical．Both units are virtually indestructable when simple directions are followed．Both units carry a full year＇s warranty against defects in materials and workmanship（parts and labor）．Either unit can be easily repaired by almost any technician in his own shop．

## If the green＂circuits clear＂light is now lit

It is now safe to push the＂run＂button and examine the symptoms that appear on the CRT screen，for the purpose of isolating any＂open＂cir－ cuits．

Except for hook up and CRT filament warm up time，this test can easily be completed in two to five seconds！

## The Mark V sells for only $\$ 995^{\circ 0}$

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# Drawing Board 



ROBERT GROSSBLATT,
CIRCUITS EDITOR

The world's simplest Z80 circuit.


FIG. 1
duty cycle) are easy to handle. As a matter of fact, if you have a TTLlevel squarewave with a duty cycle
of $50 \%$, you can pretty much ignore those restrictions altogether. And since the Z80's clock input ap-
pears as one TTL load，we can even drive it with a CMOS astable multi－ vibrator．

The complete schematic for our Z80 circuit is shown in Fig．1．The clock circuit shown there should look familiar．That＇s right；it＇s our handy－dandy，all－purpose CMOS clock circuit．You should be able to put one of those together in your sleep by now．The output fre－ quency of that circuit is approx－ imately equal to $1 /(2.2 \times \mathrm{R} 2 \times \mathrm{C} 1)$ ， so the values shown should give us about 1 MHz ．The additional gate is a buffer used to isolate the clock circuit from the Z80＇s clock input， because it needs a 330 ohm pull－ up resistor．

Believe it or not，the only other control signal we have to generate for our bare－bones system is RESET． You＇ll notice in Fig． 1 that I gener－ ate that signal with R3 and C2．They take care of power－up reset；if you want to be able to generate RESET manually as well，all you need do is wire a momentary switch in paral－ lel with C2．

Now that we＇ve got a clock and a reset pulse，the next thing to talk about is memory．There has to be a place to store the software that drives the circuit，so we have to add some ROM．I chose a 2716 EPROM because，of all the perma－ nent memory around，EPROM＇s are about the easiest to program， and 2716＇s are both cheap and available．Connecting the EPROM to the Z80 is simple．The address pins on the EPROM go to corre－ sponding pins on the micro－ processor；likewise with the data pins．

The only part of the memory cir－ cuit that needs to be explained is the 2716＇s chip－enable pins．You＇ll recall from our earlier discussions that when the Z80 wants to read something from memory，it puts a low on both the $\overline{M R E Q}$ and $\overline{R D}$ lines．Those lines are connected to the enable pins of the 2716 so that a read request from the $Z 80$ will en－ able the EPROM，thereby allowing data to be routed to the Z80．

So what about I／O？Well，in order to be consistent with our de－ sign goal of making this the world＇s simplest Z80 circuit，we＇ll connect a 7475 quad latch to the data bus． Whenever the Z80 wants to do an I／O operation，it puts a low on the
$\overline{\text { IORQ }}$ line．The $Z 80$ also brings $\overline{W R}$ or $\overline{R D}$ low depending on whether it wants to read from or write to an I／O port．So，by NORing the $\overline{\mathrm{ORQ}}$ and $\overline{W R}$ lines from the $Z 80$ ，we can send data to the latch by using an out instruction．Since the enable pin of the 7475 is active high，the Z80 will be able to store data in the latch．

If you＇ve been following my ear－ lier columns on the Z80，there should be one question upper－ most in your mind：What about the RAM？

## Where＇s the RAM？

Even a minimal Z80 system needs some temporary storage space．There is such space，of course：the Z80＇s internal regis－ ters．There＇s not much space，but a system like ours doesn＇t really need much．We can run simple programs without any additional RAM storage．

So there we have a complete Z80 system with a minimum of compo－ nents．In fact，there are more parts in some light dimmers！With our circuit the Z80 can read data and program instructions from the EPROM；it can also output data to the real world through the 4－bit latch．Actually，our little circuit can do quite a few things such as play－ ing music，storing and displaying tables of data，or working as a sim－ ple controller．All you have to do is tell it what you want it to do．And there，as Shakespeare said，is the rub：software．

In order to deal with software you need a good grounding in both logic and the Z80＇s instruc－ tion set．The point is that micro－ processor circuits，unlike the circuits we usually talk about，re－ quire very little understanding of electronics per se．Volts，ohms， amps，and all the rest go out the window．

By the time we get together next month we＇ll have had time to di－ gest that particularly distressing fact；we＇ll see then what we can do to get our little circuit to do some－ thing useful．In the meantime， spend the next couple of weeks reading up on the $Z 80$ instruction set．But don＇t throw out your mul－ timeter just yet－we＇ll get back to ＂pure＂circuit design before you know it！

R－E


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# DESIGNER'S Notebook 

## A handy low-voltage indicator

I KNOW IT'S HARD TO BELIEVE NOW, BUT once upon a time all electronics gear had to be powered from the AC lines. Batteries were put in flashlights, and nowhere else. Most other devices required much more power than a dry cell could deliver. These days, whenever I see someone walking down the street plugged into a tiny radio or cassette player, I think about companies like Webcor and get a bit nostalgic. Maybe some of you do too.

Anyway, more and more devices today use batteries for power, and that includes home-made devices as well. In the past we've presented several circuits that help keep batteries well-charged. We've also talked about nickelcadmium cell charging and standby power supplies, but we've never shown how to keep an eye on batteries' energy reserves. And we all know (by Murphy's law) that batteries have a nasty habit of pooping out just when we need them most.

Well, I've used the little lowvoltage indicator shown in Fig. 1 in almost every battery-powered device I ever built. The circuit has only five parts, and it pays for itself over and over in the amount of irritation it helps prevent.

## How it works

Input terminal $V_{\text {iN }}$ is connected to the $+V$ line of the circuit the indicator is to monitor, and the grounds of both circuits are connected together. The position of potentiometer R1's wiper determines Q1's base voltage. As long as the transistor gets enough bias to


FIG. 1


FIG. 2
remain on, the low voltage at the collector will keep the SCR from firing. As the battery voltage starts to fall, the transistor's base voltage will fall as well.
The magic moment comes when the base voltage drops too low to keep Q1 on. When Q1 turns off, the collector voltage goes up, and that provides enough gate drive to turn on the SCR. That provides enough power to turn on the LED, which could also be a buzzer or
almost any other type warning device.
The circuit uses very little power. As a matter of fact, most current is drawn through the potentiometer, although some current flows through Q1 as long as it remains on. We've shown a value of 25 kilohms for R1, but you can use just about anything as long as you provide enough current to keep Q1 on. Of course, the larger the value of R1, the less wasted current there will be. Different 2N2222's will let you get away with larger potentiometers. As always, experiment. The capacitor is not strictly necessary; it's in the circuit to act as a "sponge" to prevent the SCR from firing when you change batteries.

## The SCR

The last thing to discuss is the SCR. Remember that when the transistor turns off, the SCR is going to get its gate drive through the resistor. Not much current can flow through R2, so I've called for the most sensitive SCR I could find. If you need to drive something that draws more current than an LED, you'll have to adjust the back end of the circuit. There are many ways to do that: You could, for example, have the SCR trigger another SCR, or turn on a relay.

As it is, the circuit will work over a 25 -volt range, and the potentiometer can be adjusted to make the circuit fire over most of that range. So, breadboard the circuit and try it out. I'm sure a bit of thought on your part will result in enhancements. Let me know what you come up with.

R-E

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## Optional accessories

- VC-10 VHF converter for R-2000 covers
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- YG-455C 500 Hz CW filter for R-2000
- HS-4 Headphones
- HS-5 Deluxe headphones
- HS-6 Lightweight headphones
- HS-7 Micro headphones
- DCK-1 DC cable kit for 13.8 VDC operation
- AL-2 Lightning and static arrester
- Service manuals are available for all receivers and most accessories.

Additional information on Kenwood all-band receivers is available from authorized dealers

## KENWOOD

TRIO-KENWOOD COMMUNICATIONS 1111 West Walnut Street Compton, California 90220

# State Of Solid State 

## Analog IC's and new MOSFET's



ROBERT F. SCOTT SEMICONDUCTOR EDITOR

MANY INTERESTING IC'S HAVE RECENTLY been developed for audio applications. For example, the MTA1537A is a precision AGC/Voltage-controlled attenuator developed by Aphex Systems. It is specially suited for high-speed precision control of signal level, dynamic range, and phase and amplitude equalization in applications such as high-quality audio controllers, analog computers, precision oscillators, robots, video-effects generators, servo controllers and precision phase detectors. The MTA1537A comes in a standard 14 pin DIP package; typical specifications are:

- 110-dB dynamic range
- $120-\mathrm{dB}$ attenuation
- less than 0.05\% THD
- 2-mv control-voltage feedthrough
- 200-MHz bandwidth

Figure 1 shows how the MTA1537A can be used as a stereo attenuator. Typical specifications for that circuit include maximum input and output levels of 7.75 volts rms , maximum attenuation of 100 dB over a frequency range of DC to 200 kHz , and a control voltage feedthrough of 5 mV at 100 dB attenuation. Note that at high frequencies, circuit layout will affect attenuation.

The op-amps used in that circuit NE5534's could be used for lownoise applications. The MTA1537's data sheets include basic circuit diagrams for applications such as a voltage-variable resistor, voltagetunable high-pass filter, voltagevariable inductor, voltage-con-


FIG. 1
trolled band-reject filter, and a voltage-controlled graphic equalizer.

For two samples of the MTA1537A, and a copy of the data sheet, send $\$ 17.00$ to the manufac-turer.-Aphex Systems Ltd., 13340 Saticoy St., North Hollywood, CA 91605

## New JFET op-amp family

The MC34080 series is a new generation of JFET-input op-amps
from Motorola. They are available in single, dual, and quad versions (both compensated and uncompensated), and they offer bandwidth and slew rates as much as four times greater than previously available types.
A combination of JFET and bipolar technologies, along with an all-NPN output stage, has yielded a fully-compensated op-amp family with a gain-bandwidth product of 8.0 MHz and slew rates in excess
of $30 \mathrm{~V} / \mu \mathrm{s}$ ．For greater speed，un－ compensated versions are offered with a gain－bandwidth product of 16 MHz and slew rates of $60 \mathrm{~V} / \mu \mathrm{s}$ ．

The all－NPN output stage provides a peak－to－peak output voltage swing that is $33 \%$ greater than standard op－amps that use NPN／PNP output stages．Other features include：input imped－ ance of $10^{12}$ ohms，open－loop out－ put impedance of 30 ohms at 1.0 MHz ，THD of $0.01 \%$ ，phase／gain margins of $55^{\circ} / 7.6 \mathrm{~dB}$ for fully com－ pensated devices，and $30 \mathrm{nV} / \sqrt{\mathrm{Hz}}$ input noise voltage．

Prices range from $\$ 0.59$ for the single op－amp MC34080（uncom－ pensated）and MC34081（compen－ sated）to $\$ 2.80$ for quad versions． For data sheets and complete pric－ ing information，contact Motorola Semiconductor Products，PO Box 20912，Phoenix，AZ 85036.

## Low－power quad op－amps

National＇s LP124 family of quad op－amps is a pin－compatible，low－ power version of that company＇s LM124 family．The new series con－ sists of the LP124，LP2902 and LP324 high－gain，internally－compen－ sated micropower op－amps．They are well suited for CMOS applica－ tions，battery－powered equipment and other circuits that require good DC performance and low supply current．The LP124 joins Na－ tional＇s LP139 and LP165 low－power quad comparator families．

Maximum power consumption is $125 \mu \mathrm{~A}$－one－tenth that of Na － tional＇s LM series，and lower than many CMOS amplifiers．In addi－ tion，input bias current has been reduced by a factor of ten to a max－ imum of only 4 nA ．Available in 14 － pin plastic and ceramic DIP＇s，the LP324N sells for $\$ 0.75$ in quantities of 100 or greater．For more infor－ mation，contact National Semicon－ ductor Corporation， 2900 Semicon－ ductor Drive，Santa Clara，CA 95051.

## New temperature sensors

The LM34，LM34C，and LM34D make up a series of precision tem－ perature sensors with output volt－ ages linearly proportional to the Fahrenheit temperature scale． Those devices，from National Sem－ iconductor，require no external calibration or trimming to provide
typical accuracies of $1 / 2^{\circ} \mathrm{F}$ at room temperature and $\pm 1 / 2^{\circ} \mathrm{F}$ over a full range of -50 to $+300^{\circ} \mathrm{F}$ ．Tem－ perature stability is aided by the LM34＇s low self－heating，which is typically less than $0.2^{\circ} \mathrm{F}$ in still air．

Interfacing to readout or control circuits is easy．The output imped－ ance is only 0.1 ohm at 2 mA and the output is linear at $10 \mathrm{mV} /{ }^{\circ} \mathrm{F}$ ．It can be used with single power supplies or with split（plus and minus）supplies delivering from 5 to 30 volts and draws less than 60
mA ．The LM34 can be glued or ce－ mented to a surface and its tem－ perature will be within $0.02^{\circ} \mathrm{F}$ of the surface reading．

The LM34 is rated from -50 to $+300^{\circ} \mathrm{F}$ ，the LM34C from -40 to $+230^{\circ}$ F ，and the LM34D from +32 to $+212^{\circ} \mathrm{F}$ ．All are available in TO－ 46 packages．The LM34C and LM34D are also offered in the TO－92 pack－ ages．The LM34 is $\$ 1.55$ each in 100 and up lots．－National Semicon－ ductor Corp．， 2900 Semiconductor Drive，Santa Clara，CA 95051．R－E

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# COMMUNICATIONS CORNER 

HERB FRIEDMAN, COMMUNICATIONS EDITOR

## Improving reception with an active antenna

IT WAS MOST LIKELY MARCONI WHO said, "If you can't hear 'em, you can't work 'em." And so for years, shortwave listeners put up the longest antenna possible in the belief that the longer the wire, the greater the RF signal induced in it, and therefore, the greater the chance of hearing the transmission.

In the early days of the wireless, it was not unusual to find receiving antennas that stretched well beyond the limits of one's property, and if a neighbor refused to give permission to string a wire over his "south forty," the wire could at least reach to your own property line.

Unfortunately, today's property line is often right outside one's liv-ing-room window. If you are an apartment dweller, just the stub of a wire hanging out the window is bound to produce at least five neighbors who've nothing better to do than spend the day staring at the sides of buildings to see if anyone has defaced the property with wire. As such, many SWL's (shortwave listeners) are limited to some form of indoor antenna, which are not known for efficiency when it comes to receiving RF signals.

## Active antennas

Today, instead of using the bedspring as an indoor shortwave


FIG. 1

Figure 1 shows how the Datong active antenna works. Although the operating frequency range is from 0.2 to 30 MHz , the antenna itself is only three meters long. At its center is a solid-state amplifier that's connected through 13 feet of coaxial cable to an interface preamplifier, which matches the active antenna to the low impedance of the receiver's antenna input.

The output of the amplifier is approximately 6 dB greater than that of a conventional dipole antenna. The interface device also has a 6 dB gain that is switch-selected by the user, so the total system gain is either 6 dB or 12 dB . The power source for the device is a common plug-in AC adapter.
Although the amplifier is enclosed in a small cabinet, the antenna itself is a flexible wire that can be placed under a carpet, draped around a window, concealed in an attic, or slipped under the bed. The only thing that you have to remember is that the amplifier will amplify all signals presented to its input. So, if the received signal is under at-
mospheric and man-made noise to start with, it's still going to be under the noise when amplified.

## Squashing the QRM.

Even if you get the signal level up to the minimum level required by the receiver, there's still a reception problem because the VHF frequencies are jam-packed, and the interference is often stronger than the desired station. The usual way to reduce interference is to narrow the bandwidth of the IF amplifier through the use of filters.
Many shortwave receivers, even the less expensive models, have some form of user-selected IF filtering. But those factory-installed filters often don't cut the bandpass down to rock bottom. So some receivers have expansion sockets for user-installed upgrade IF filters. For some other units, there are more selective plug-in filters that can be purchased and installed by the user.
On the other hand, most of the less expensive receivers make no provision for extra IF selectivity. In such instances, the user can use
an audio filter．While an audio fil－ ter is not as effective as an IF ampli－ fier filter，it＇s a lot better than nothing at all．
An audio filter eliminates the in－ terference＂grind＂heard in speak－ ers or headphones．That is done by using an audio filter with a very sharp cut－off．Such a filter is found in the Laboelectron SF－0330（also available from Gilfer Shortwave）， which connects between the re－ ceiver＇s audio output and the speaker or headphones．The SF－ 0330 contains a $300-3300-\mathrm{Hz}$ band－ pass filter，a $500-\mathrm{Hz}$ highpass filter， $1500-$ and $2200-\mathrm{Hz}$ lowpass filters， and a small amplifier that drives the speaker or headphones．

The filters in the Laboelectron unit feature an unusually high de－ gree of filtering．That degree of fil－ tering is made possible through the use of active（amplified）rather than passive（inductive and capaci－ tive）filters．The receiver＇s normal output signal is fed into one end of the special active filter，and the speaker／headphones are con－ nected to the other．

If all filters are switched out， whatever normally comes out of the receiver is fed to the speaker and phones．But if the $300-3300 \mathrm{~Hz}$ filter is switched in，the overall re－ ceiver－system bandwidth is sharp－ ly limited to the voice－frequency range，generally attenuating the sputtering and hissing from sta－ tions on nearby frequencies．

If the interference is still severe， the bandwidth can be restricted even farther by switching in the individual filters．At maximum fil－ tering，the bandpass is only $500-1500 \mathrm{~Hz}$ ．Although it is not hi－ fi ，it can make the difference be－ tween getting some intelligence or hearing nothing but beeps and groans．

Of course，while few add－on gadgets are adequate substitutes for a high－performance receiver and an outdoor antenna，antenna gain and selectivity devices often are the only way to get any worth－ while audio－especially when you＇re using a budget receiver with a wire lying on the livingroom floor as an antenna．

R－E


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## SWITCHING SUPPLIES

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Fig. 12 takes a nominal 10 -volt input and converts it to 25 volts at 100 mA . Efficiency in that configuration is typically $60 \%$ or better.

The inverting converter shown in Fig. 13 takes a 12 -volt input and delivers a - 15 volt output at 100 mA ; the circuit will actually operate over an input range of 10 to 20 volts. Efficiency in that configuration is typically $60 \%$ or better.

As we said, the same printed-circuit board may be used to experiment with the $\mu \mathrm{A} 78 \mathrm{~S} 40$ in any mode. Printed-circuit artwork is shown in the "PC Service" section of this magazine, and a component layout diagram is shown in Fig. 14. Follow standard construction practices and be careful installing the jumpers; Table 2 details the appropriate jumpers used in each circuit.

The only part that requires special attention is inductor L1, which must be assembled before it is mounted. First, evenly wind 20 to 25 turns of 18 gauge wire around the bobbin, leaving several inches of slack on either end. Next, insert the two black ferrite cores into the bobbin, and snap the assembly in the mounting bracket. The bracket can now be attached to the PC board with short screws. The pins on the bobbin should be inserted into the holes on the PC board and soldered. The slack ends of the coil should now be soldered to those pins.

Now the board must be calibrated; that involves setting the two potentiometers. The 100 ohm potentiometer R2 in parallel with one-ohm resistor R1 sets the shortcircuit current limit; in the step-up and step-down modes they should have a composite value of 0.33 ohm , so R 2 should be adjusted for a value of 33 ohms. For the inverter circuit the composite value is 0.28 ohm, so R2 should be adjusted for a value of 28 ohms. In either case the IC should be removed from its socket before calibration.

The second step of calibration involves setting 50 kilohm potentiometer R4. Connect a 1000 -ohm resistor between the output terminal and ground, and then apply your input voltage. While monitoring the output with a voltmeter, adjust R4 for the desired voltage.

## Beyond the basics

Designing a switching power supply is not just a matter of manipulating mathematical formulas; the actual components used to implement the circuit can have a great effect on that circuit's performance. In particular, the inductor, the switching transistors and the output capacitor must be carefully selected. Moreover, circuit layout must also be carefully thought out;
switching power supplies are notorious generators of EMI (ElectroMagnetic Interference), and a poor layout can cause excessive amounts of EMI to be generated.

Saturation of the inductor's core is a common problem with switching regulators. The inductor's current peaks should appear very sharp on an oscilloscope; there should be no cusping, rounding, or clipping. Further, the core's window must have a large enough diameter so that enough turns of the proper wire size may be wound through it. Otherwise DC losses at peak current may become excessive, power may be lost, and the output voltage may drop below the value the circuit was designed to maintain.

In linear voltage regulators the output transistors usually operate at a quiescent point so that a relatively constant current flows continuously. However, that is not the case with switching voltage regulators. The transistors must withstand the unique stresses caused by rapidly switching from completely on to completely off. So parameters such as switching times, saturation voltage, power dissipation and secondary breakdown ratings are now all crucial.

The last component to consider is the output capacitor. In general, such capacitors have large values ( $100 \mu \mathrm{~F}$ or more) and must operate at high frequencies (20 kHz or more). They require low ESR (Effective Series Resistance) and ESL (Effective Series Inductance). An excellent trade-off between cost and performance is the solid tantalum capacitor. Compared with aluminum electrolytic capacitors, solid tantalum capacitors have higher capacitance per unit volume, they are more stable, and they have hermetic seals to eliminate the effects of humidity.

## Fighting EMI

EMI is primarily due to wiring inductance in the circuit. That inductance can cause rapid changes in current, which may thereby generate high-voltage spikes. Those spikes are proportional to both the rate at which the current changes and the wiring inductance. Here are some suggestions to help reduce EMI:

- Keep loop inductance to a minimum by using compact layouts.
- Keep loop area as small as possible, and keep lead lengths short.
- In the step-down mode, return the input capacitor directly to the diode. That helps reduce ground-loop noise.
- Select an external diode that can maintain peak recovery current as low as possible. That reduces the energy content of the voltage spikes.

Integrated circuits can greatly simplify the design of switching power supplies. And careful component selection and circuit layout can eliminate the disadvantages of switching supplies.

R-E

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## NEW IDEAS

continued from page 38
mines the number of beats per measure.

Each time the 4017B is reset, its Qooutput goes high. That signal is fed to LED1 for a visual indication of the start of each measure. The Q0 signal is also fed to IC1-d, another buffer. The signal is also mixed with the astable's free-running output (after buffering by IC1c). The mixed signal is what provides the extra "oomph" signalling a new measure.

There's not much else to the circuit. The network composed of C2 and R6 sharpens up the downbeat pulse, and the network composed of C3 and R7 sharpens up the freerunning pulses. By making C2 larger than C3, the downbeat gets greater emphasis. You may vary the values of those two components to obtain different sound outputs.

The mixed signal is coupled by C4 and R10 to an outside amplifier. You may connect the metronome's output to any convenient amplifier; alternatively, the circuit shown in Fig. 1-b may be used for that purpose, and will provide a compact, portable metronome.

## Construction

The metronome may be built in any convenient manner; just be sure to use sockets for all IC's. After mounting the components, check the board carefully for wiring errors, especially to the battery, a standard nine-volt unit. Use a small speaker, and mount everything in a small plastic case. Just check over your wiring before inserting the IC's in their sockets.

The metronome can be calibrated by marking off ten equallyspaced divisions on the front panel around R1. Attach a knob with a pointer to R1. Turn the metronome on, set R1 to point to the division, and count the number of beats in 20 seconds. Multiply that value by ,three and record the result. Do the same for each of the other nine divisions, and then transfer the results to the plastic case with rubon lettering or labels from a labelmaking gun.-C. R. Fischer

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closure. Accessories include: Xacto knife, masking tape, a red and blue pencil, general-purpose template, three pieces of $18^{\prime \prime} \times 14^{\prime \prime} \mathrm{my}$ lar, aluminum locators, three


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given distance (one foot, for example), the robot should make a measurement to the right. If that direction isn't blocked, movement may proceed in that direction. If both the front and right are blocked, the left should be checked. If all three directions are blocked, the robot should back up and examine both the left and right sides for a possible escape route. The whole process would be repeated as necessary until the robot found the doorway and exited. If you think such a procedure is complicated, consider how complex an optical image-recognition system must be!
At this point you may be wondering if robotics is really a hardware or a software discipline. To tell the truth, it encompasses both fields, and others such as mechanical engineering. To get the most out of robotics, you've got to be expert in them all.

## Miscellaneous news

Expect the industry leader in personal robotics to introduce a new high-performance product shortly. Unusually, rather than rush their new model to market, that company has been using the robots internally for almost a year. That should provide us with a welldebugged product when it does make it to market.
The Rhino Robot company has reduced the price of their Scorpion (reviewed here a few months ago) drastically. In an upcoming column I'll show you how to slave the Scorpion to a Heath HERO. We also have ideas for some other interesting HERO projects. Drop us a line if you're interested.

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DIGITAL Klock kit plays 1 -of-12 melodies each quarter hour. Displays time, date, and other features. Send $\$ 2.50$ for assembly plans and pricing to KERBER KLOCK KO., 36117 Hillcrest, Eastlake, OH 44094.
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| 2114-4 |  |  |
| (1) | $1024 \times 4$ $1024 \times 4$ | (200ns (iP) |
|  | ${ }_{2048 \times 8}^{4096 \times 1}$ | ${ }_{(1500}^{\text {(150ns) }}$ |
| TM M 2016 - 100 | 204888 | ${ }^{(1000083)}$ |
| HM6116-3 | $2048 \times 8$ | (150ms) (cmos) |
| ${ }_{\text {HM61116LP. }}$ | 204888 |  |
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| 4013 | ． 35 | 4511 | ． 69 | 7406 | 29 | 74151 | ＋ 55 | 74504 | ． 29 | 745175 | 79 |  |  | 49 | 7905T | 59 59 | TL074 | 1.95 | LM748 | 59 |
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| 4024 | ． 49 | 4529 | 2.95 | 7417 | 25 | 74163 | .69 7 | 74537 | ． 69 | 745240 | 1.49 |  |  | －0．92 | CASE |  | LM311H | ． 89 | LM1489 | 49 |
| 4025 | ． 25 | 4532 | 1.95 | 7420 | ． 19 | 74164 | ． 85 7 | 74538 | ． 69 | 745241 | 1.49 |  | 05 | ． 49 | 79L05 | ． 69 | LM317K | 3.49 | LM1496 | 85 |
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| 4040 | ． 69 | 4585 | ． 75 | 7438 | 29 | 74177 | ． 75 74 | 745112 | ． 50 | 745258 | ． 95 | LM338KAd 5A |  |  | TO－3 | 3.95 |  |  | XR2206 | 3.75 |
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| 4046 | ． 69 | $74 \mathrm{C85}$ | 1.49 | 7474 | ． 33 | 74192 | 79 | 74S157 | ． 79 | 745374 | 1.69 |  |  |  |  |  | LM335 | 1.40 | CA3081 | ． 99 |
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| 4049 | ． 29 | 74 C 150 | 0.75 | 7476 | ． 35 | 74196 | ． 79 7 | 74S161 | $\begin{array}{lll}1.29 & 745571 & 2.95\end{array}$ |  |  |  |  |  |  |  | LM337K | 3.95 | CA3086 | 80 |
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| 4052 | ． 69 | 74C163 | $3 \quad .99$ | 7486 | 35 | 74221 | 1．35 | DATAMEO IMTERFMRE |  |  |  |  | PIN |  | ． 15 | ． 12 | LM340 ${ }^{\text {s }}$ | 7800 | CA3146 | 1.29 |
| 4053 | ． 69 | 74 C 164 | 4.39 | 7489 | 2.15 | 74246 | 1.35 － |  |  |  |  |  |  |  | ． 17 | ．13 | ${ }_{\text {LF350 }}$ | 4.60 59 | CA3160 | 1.19 |
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| 4077 | ． 29 | 74 C 374 | 41.99 | 74100 | 2.29 | 74273 | 1.95 A | ADC0817 | 9.95 | 8 8 97 | ． 59 |  | PIN | ST | 49 | ． 39 | LM383 | 1.95 | LM3911 | 2.25 |
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| 4094 | 2.49 | $74 \mathrm{C923}$ | 34.95 | 74143 | 5.95 | 9602 | 1.50 D | DAC1020 | 8.25 | DS8835 | 1.99 |  | PIN | ww | ． 69 | ． 58 | TL497 | 3.25 | RC4558 | ． 69 |
| 14411 | 19.95 | $74 \mathrm{C926}$ | 67.95 | 74144 | 2.95 | 9637 | 2.95 D | DAC1022 | 82.95 | DS8836 | ． 99 |  | PIN | ww | ． 99 | ． 90 | NE555 | 29 | LM1360 | 1.49 |
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| EDG | QECA | NNE |  |  | 36 PIN GENTRONIFS |  |  |  |  | INTERSIL |  |  | $\begin{aligned} & 40 \text { PIN WW } 1.99 \\ & \text { WW=WIREWRAP } \end{aligned}$ |  |  |  |  | LM566 LM567 | 1.49 |  | 1.25 1.25 |
| 100 P | PIN ST | S－100 | ． 125 | IDCEN36 MIBBON CABLE |  |  |  | 6.95 | ICL7106ILI7107 |  |  | 16 PIN ZIF 4.95 CALL |  |  |  |  | NE570 | 2.95 | 75451 | ． 39 |
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| 62 P | PIN ST | IBM PC | ． 100 | CEN36CEN36PC |  | SOLDER CUP |  |  | 4.95 | $\begin{array}{lr}\text { ICL7660 } & 2.95 \\ \text { ICL8038 } & 4.95\end{array}$ |  |  | 28 PIN |  | ZIF | 6.95 | CALL | NE592 | ． 98 | 75453 | 39 |
| 50 P | PIN ST | APPLE | 100 |  |  | RT ANGLE PC MOUNT |  | T | 40 PIN ZIF 9.95 CALL |  |  |  |  | LM723 | 75 | 75477 | 1.29 |
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|  |  | 9 | 15 | 19 | 25 | 37 | 50 |
| SOLDER CUP | MALE |  | DBxxP | 82 | ． 90 | 1.25 | 1.25 | 1.80 | 3.48 |
|  | FEMALE | DBxxS | ． 95 | 1.15 | 1.50 | 1.50 | 2.35 | 4.32 |
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| Hoods | METAL | MHOODxx | 1.25 | 1.25 | 1.30 | 1.30 | －－ | －－－ |
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